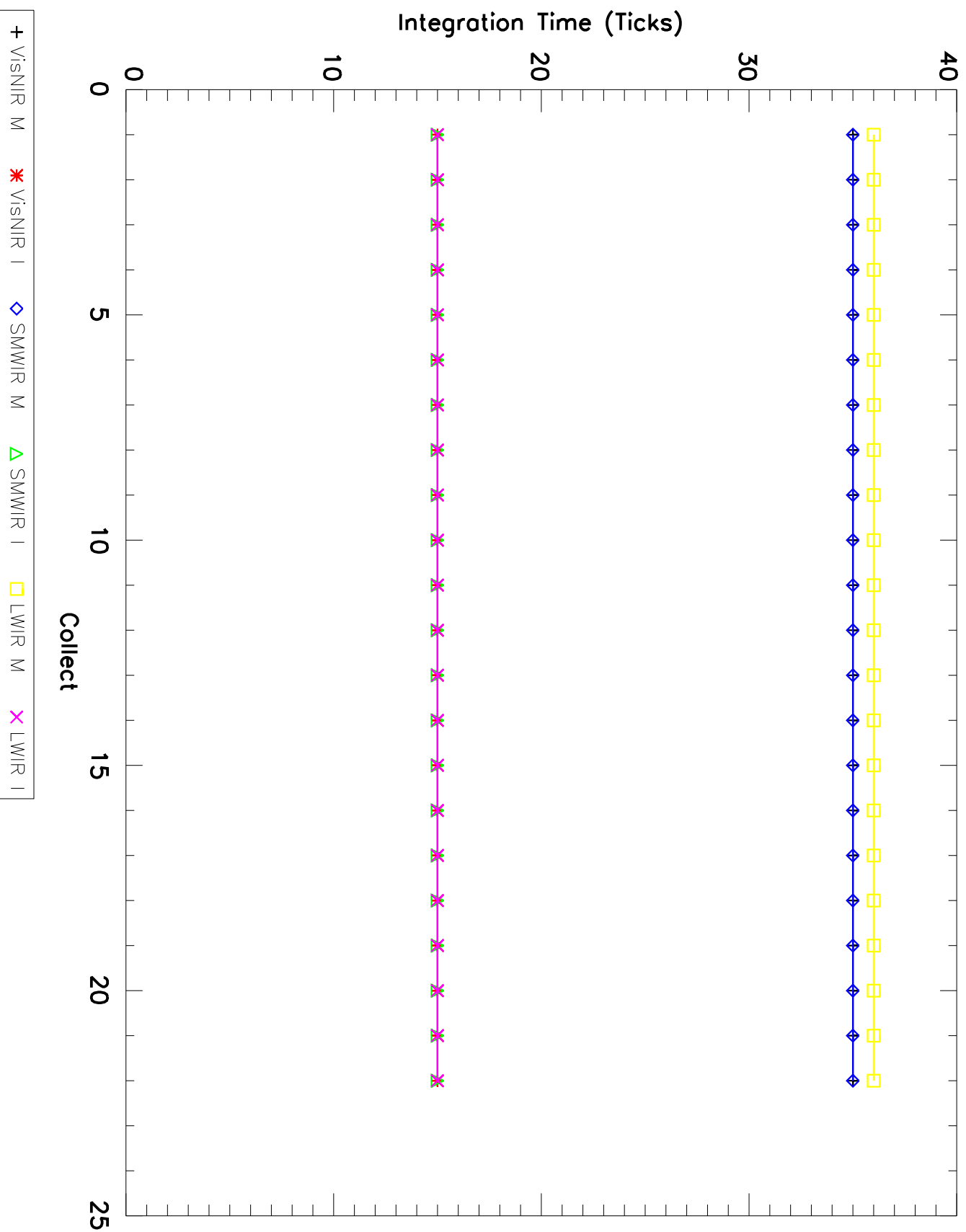
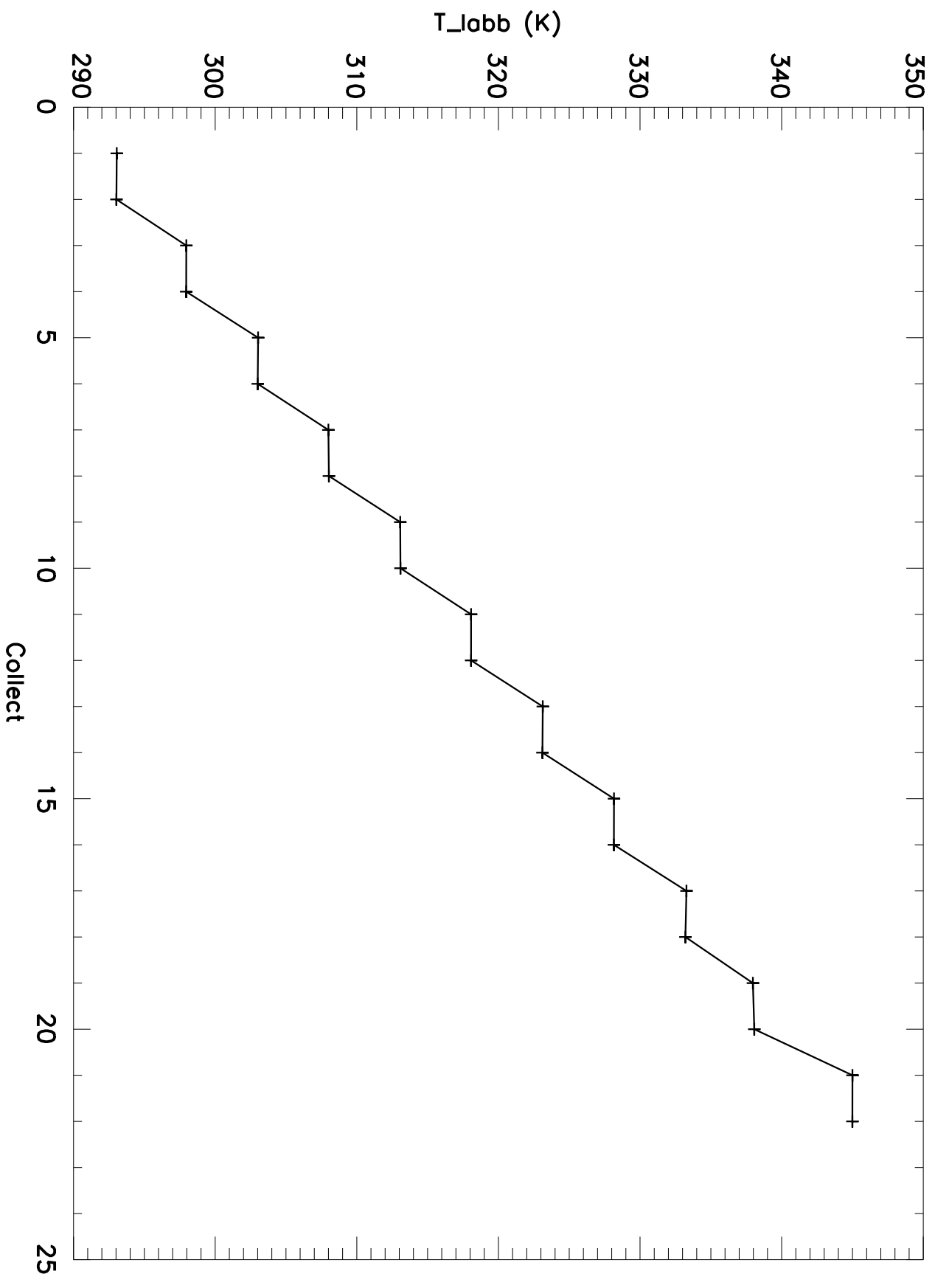


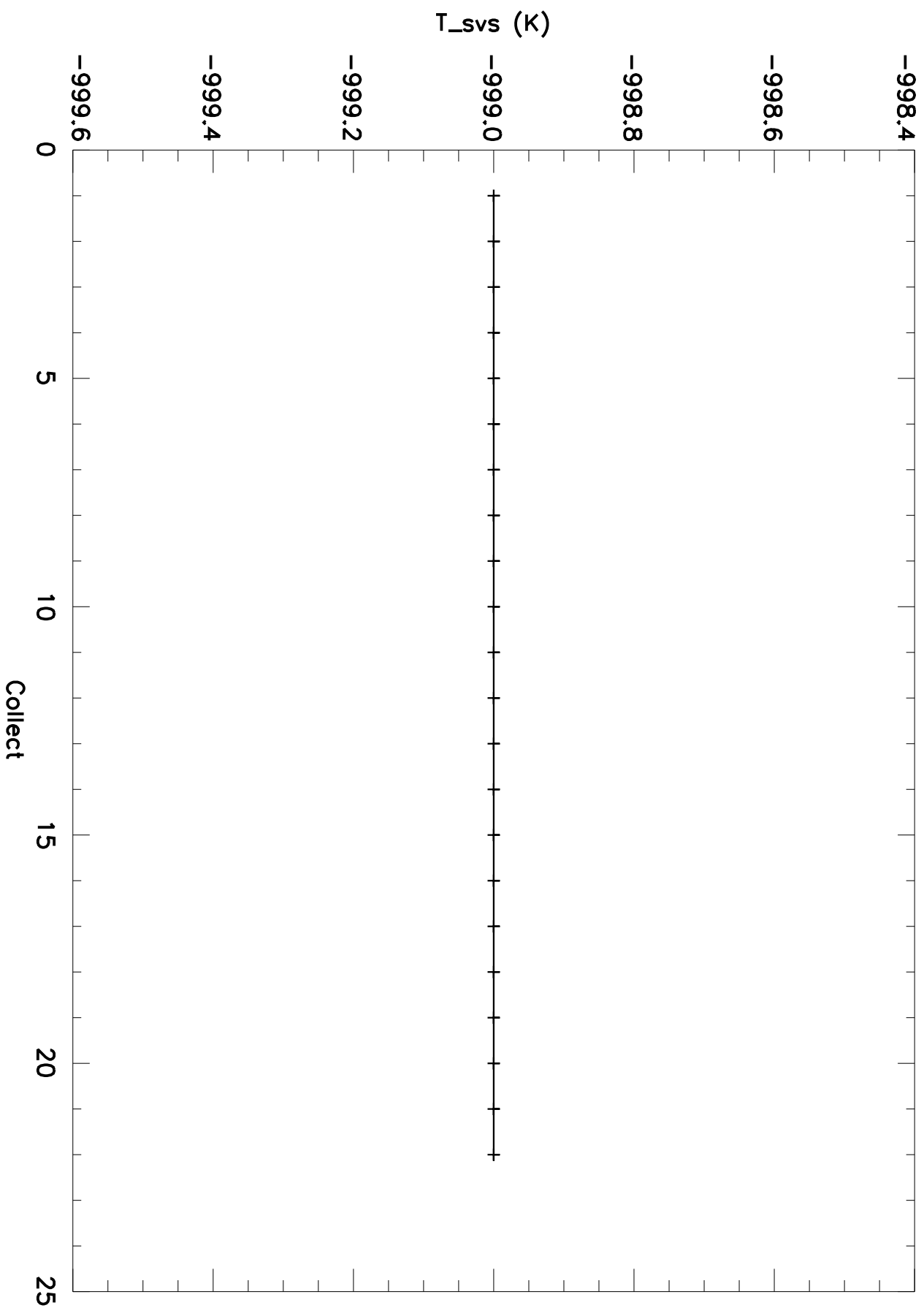
Integration Time vs Collect



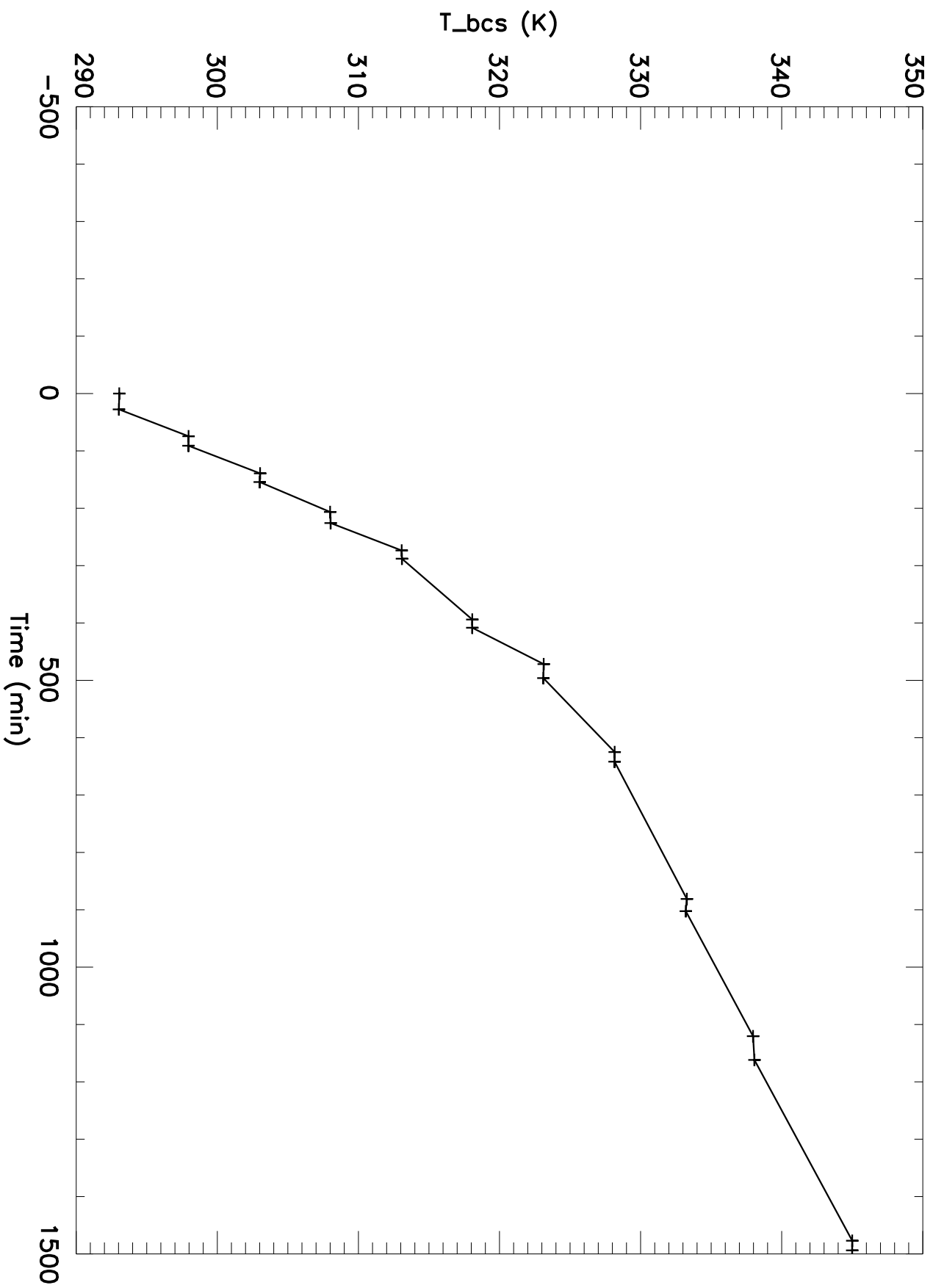
T_{labb} vs Collect



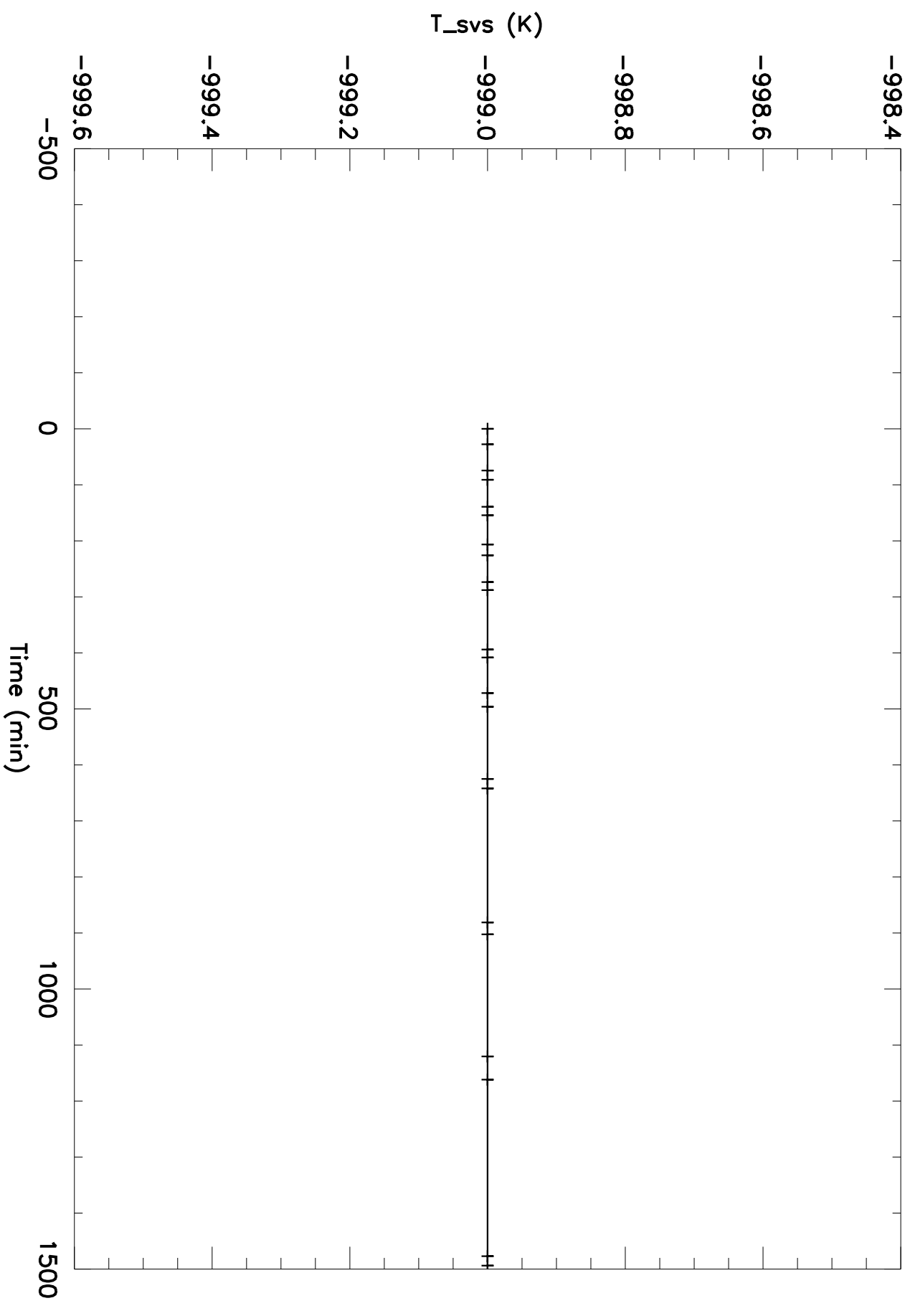
T_svs vs Collect



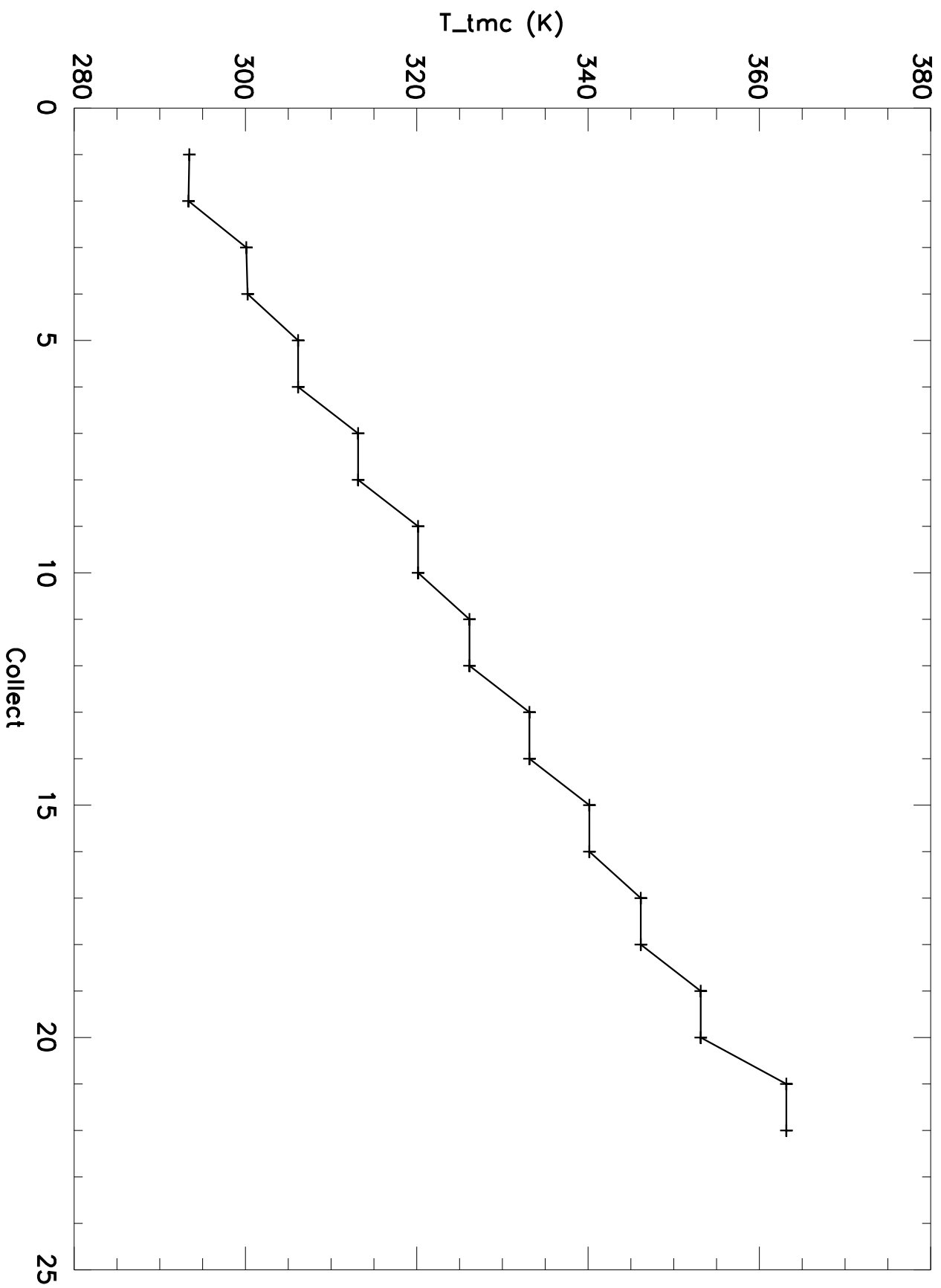
T_{labb} vs Time



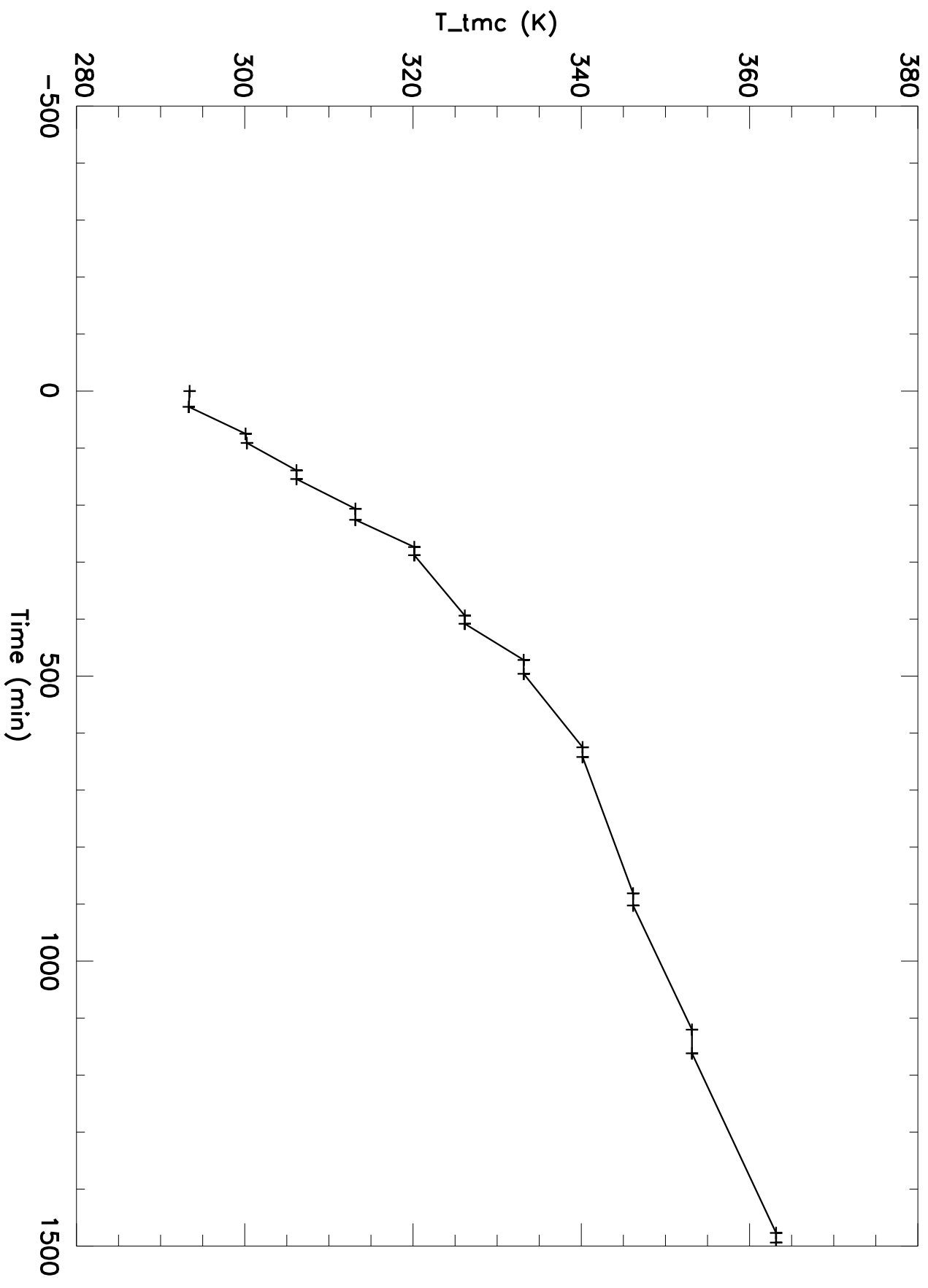
T_svs vs Time



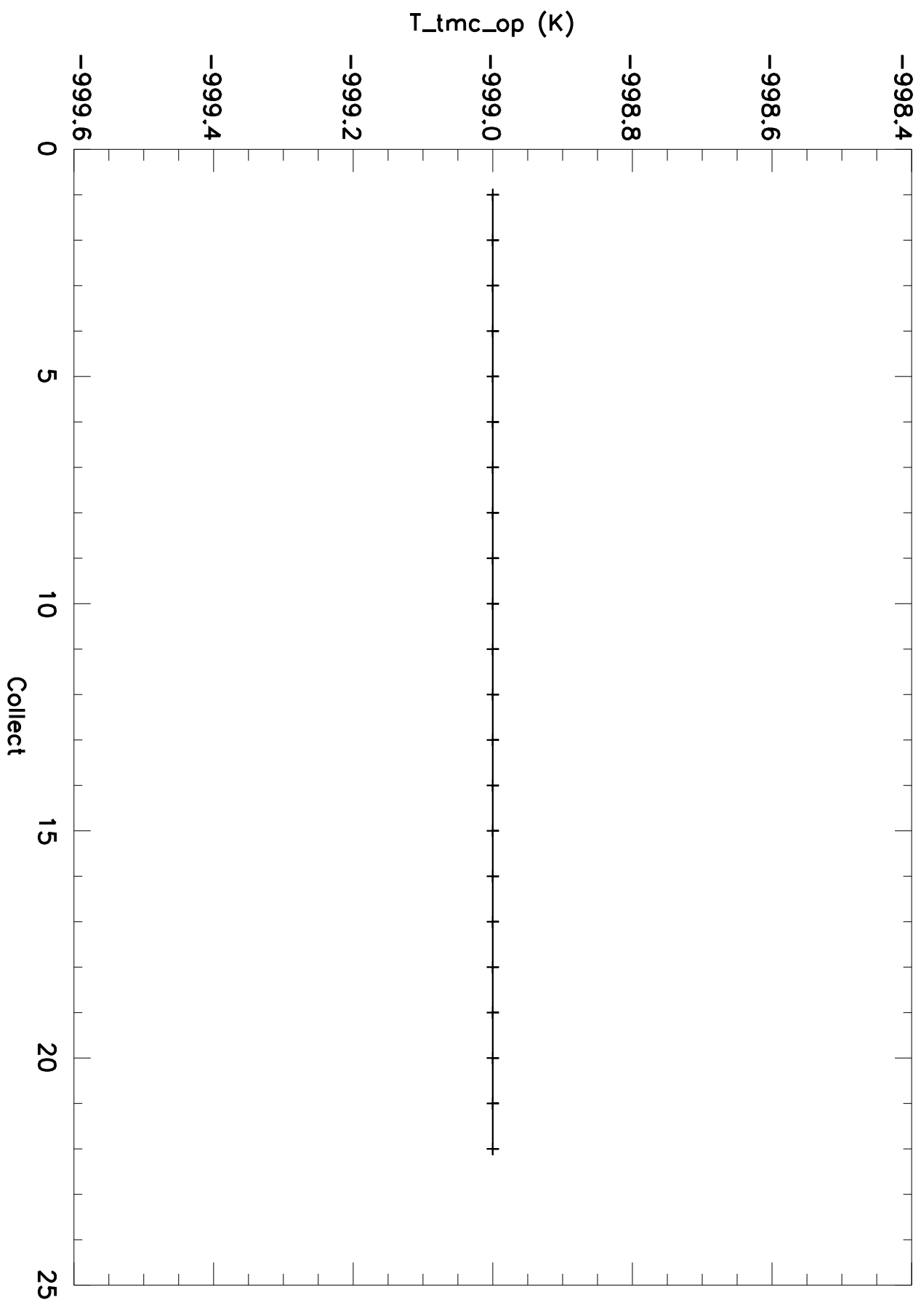
T_tmc vs Collect



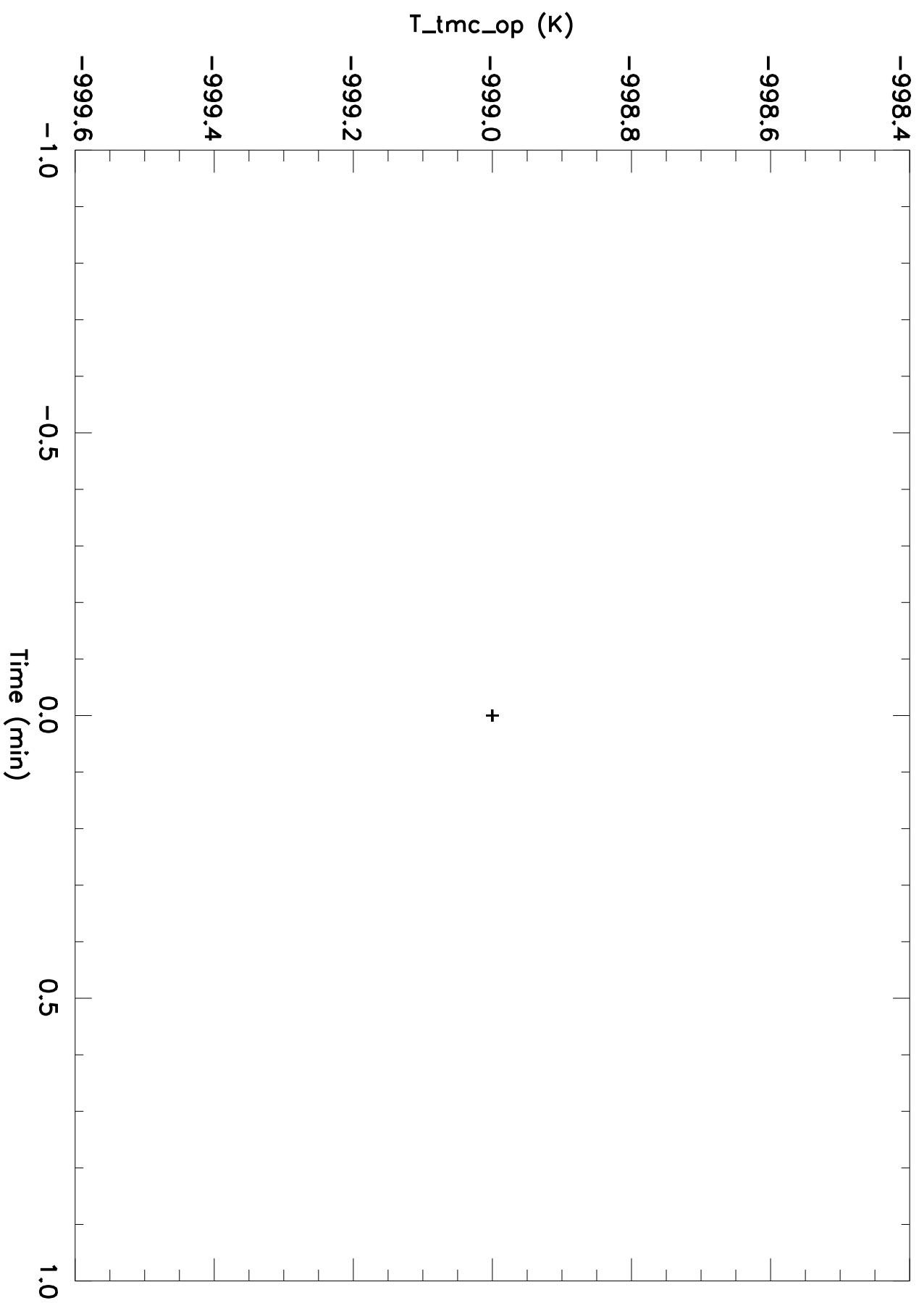
T_tmc vs Time



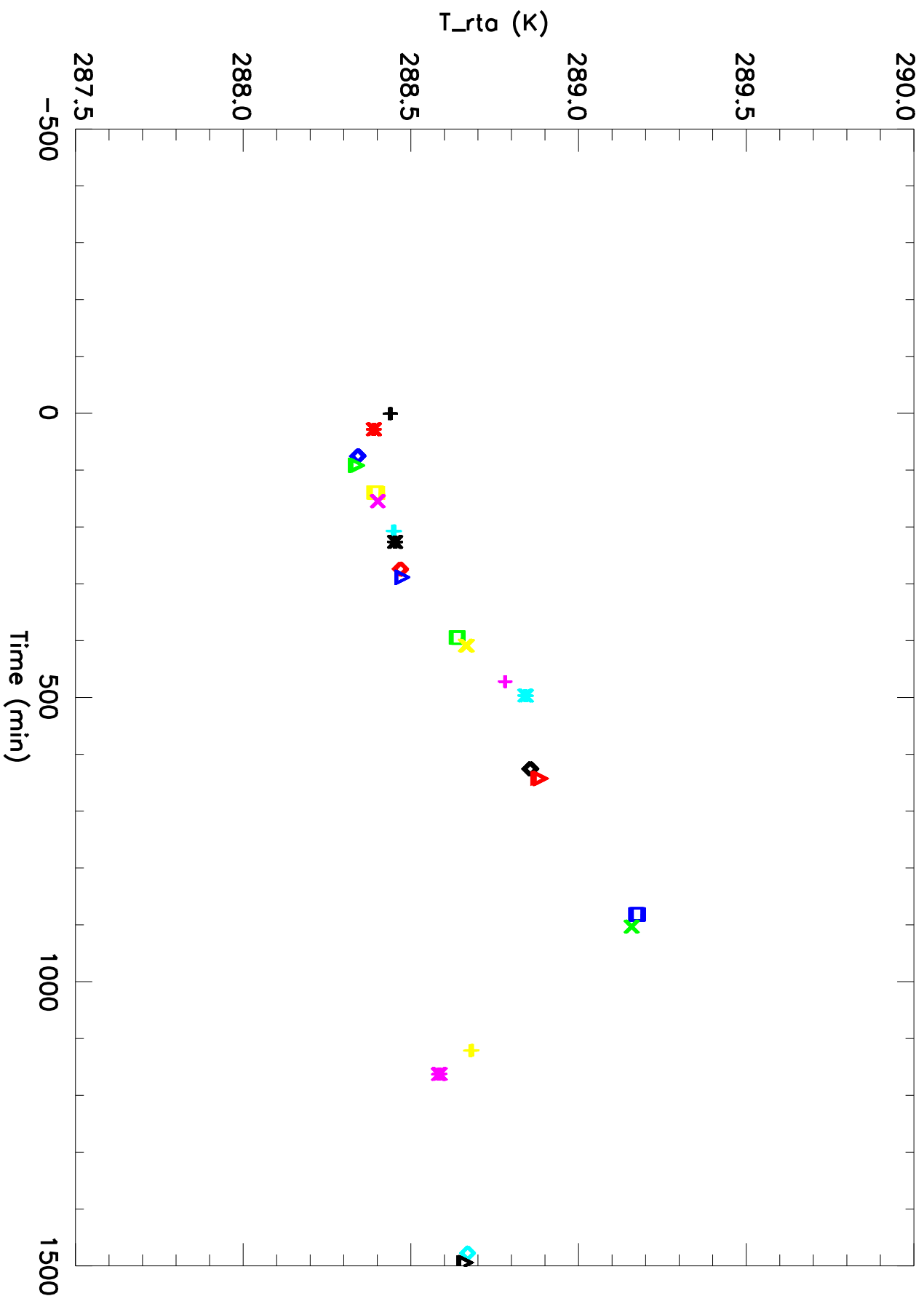
T_tmc_op vs Collect



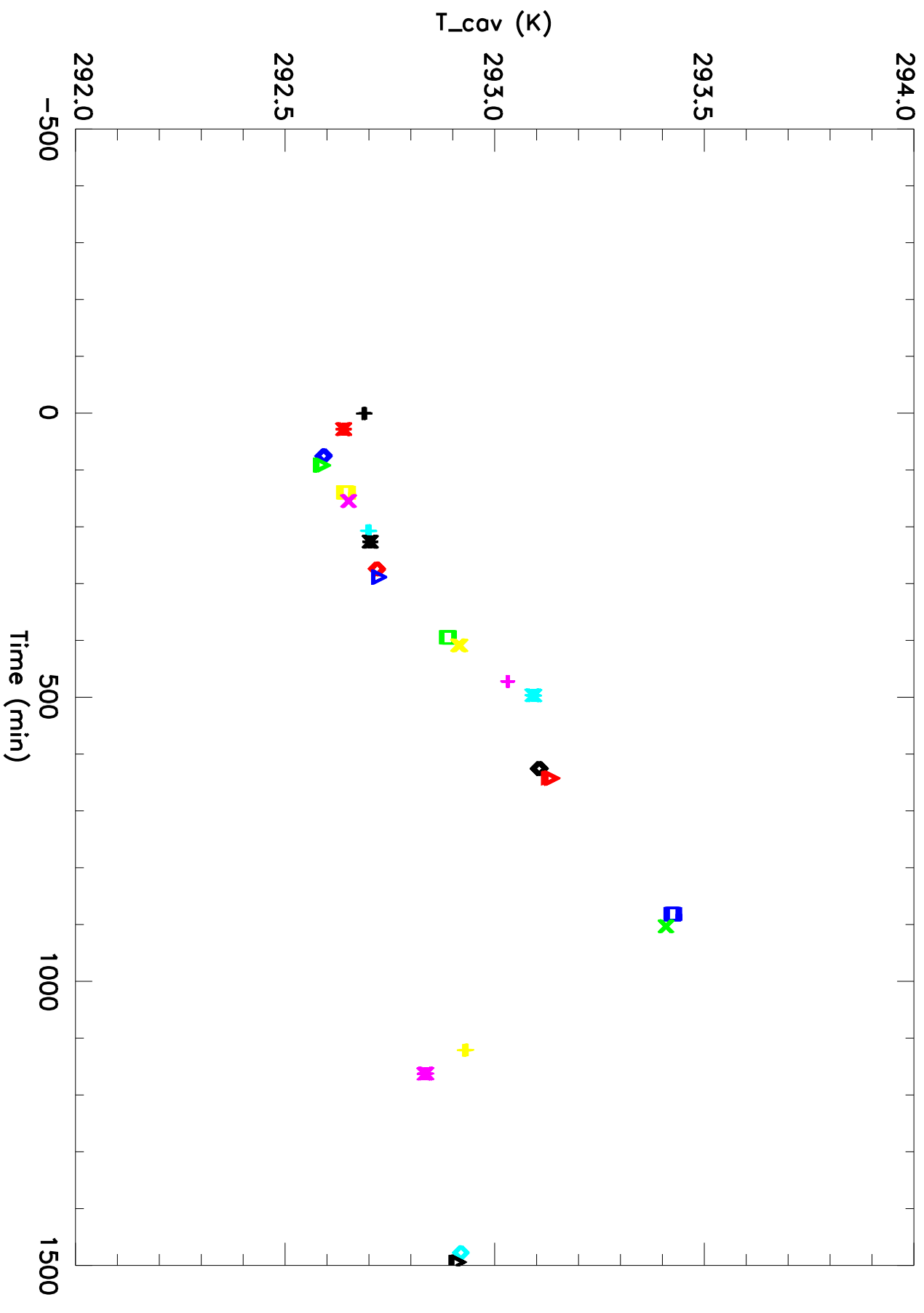
T_tmc_op vs Time



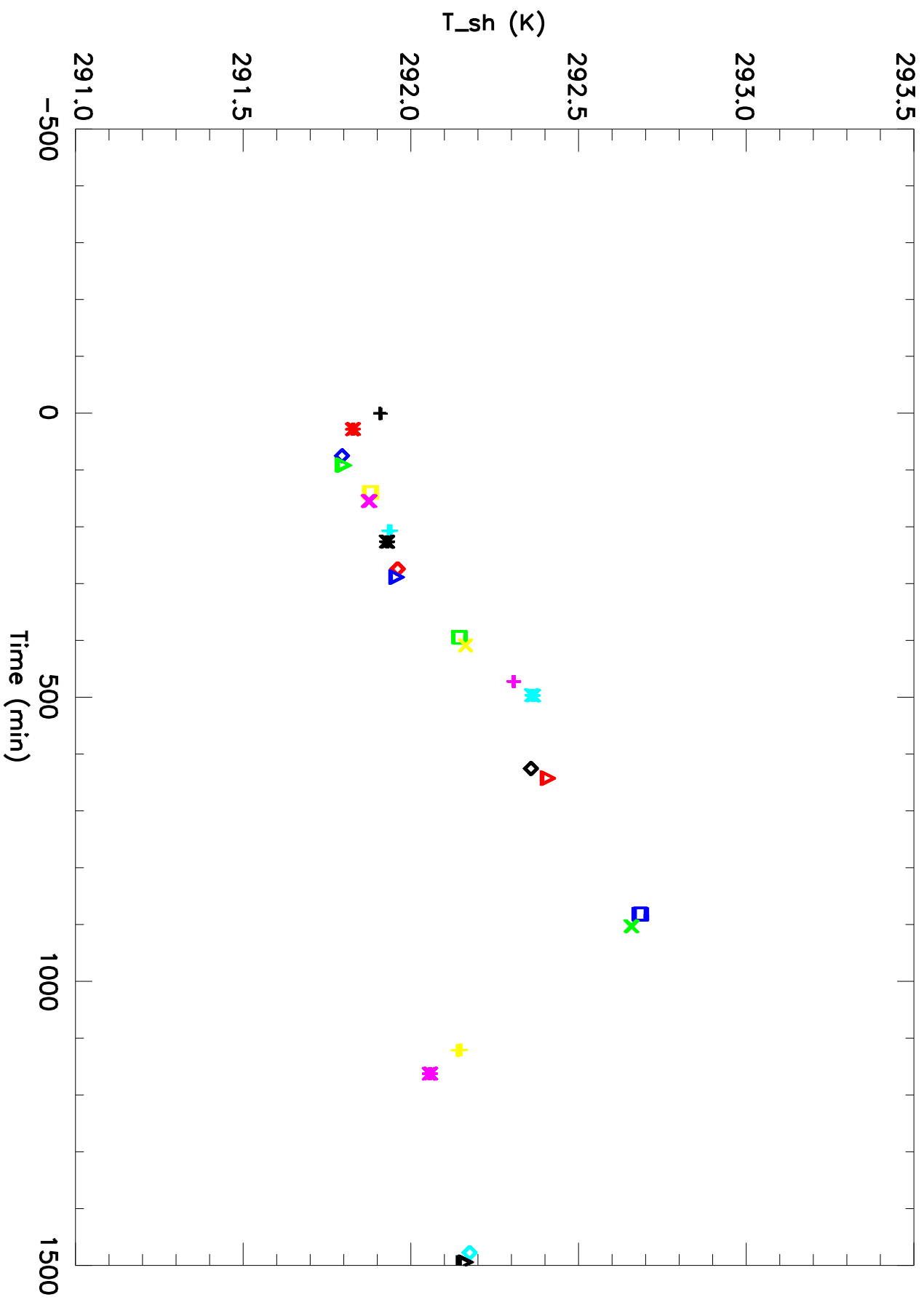
T_{rta} vs Time



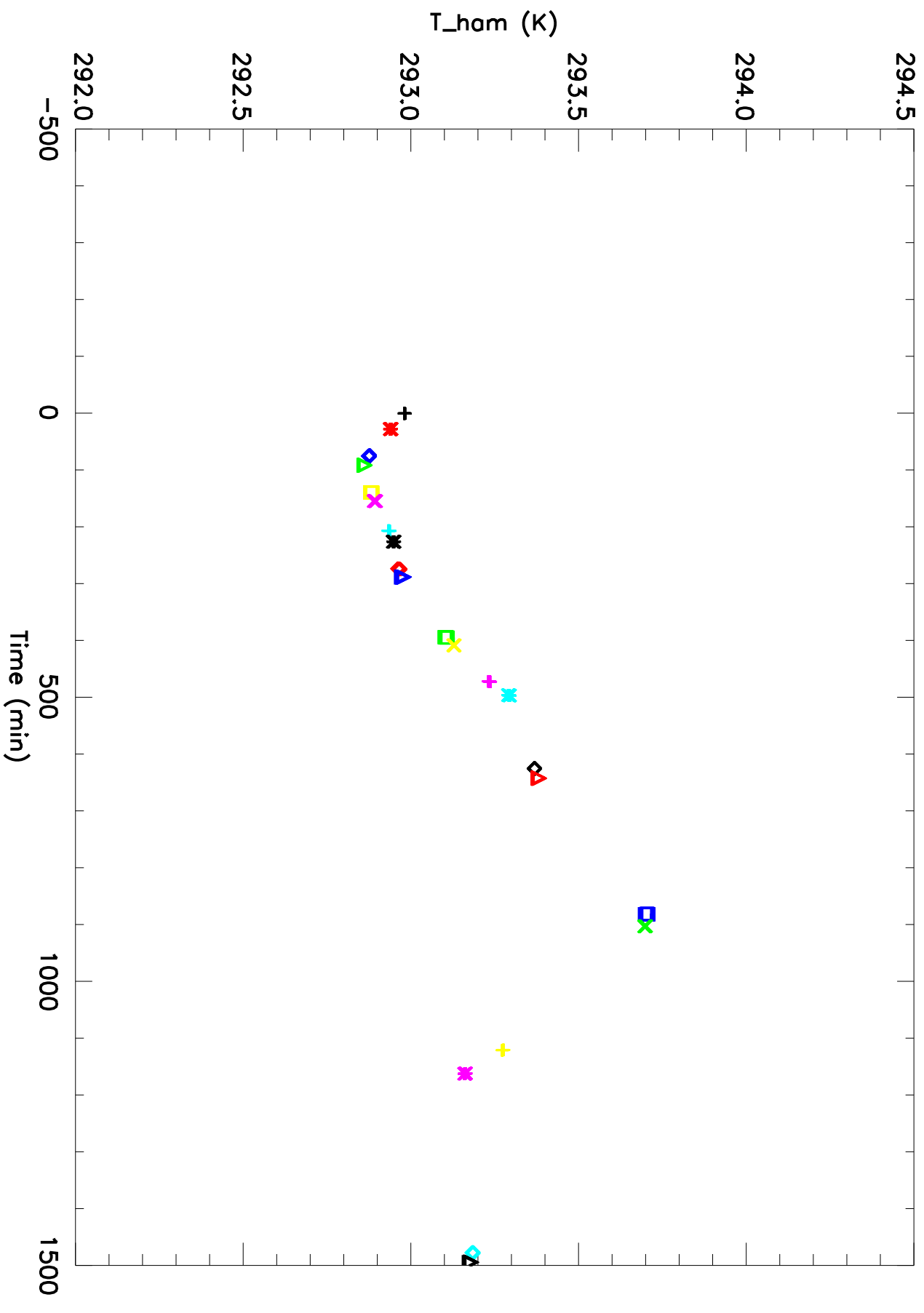
T_cav vs Time



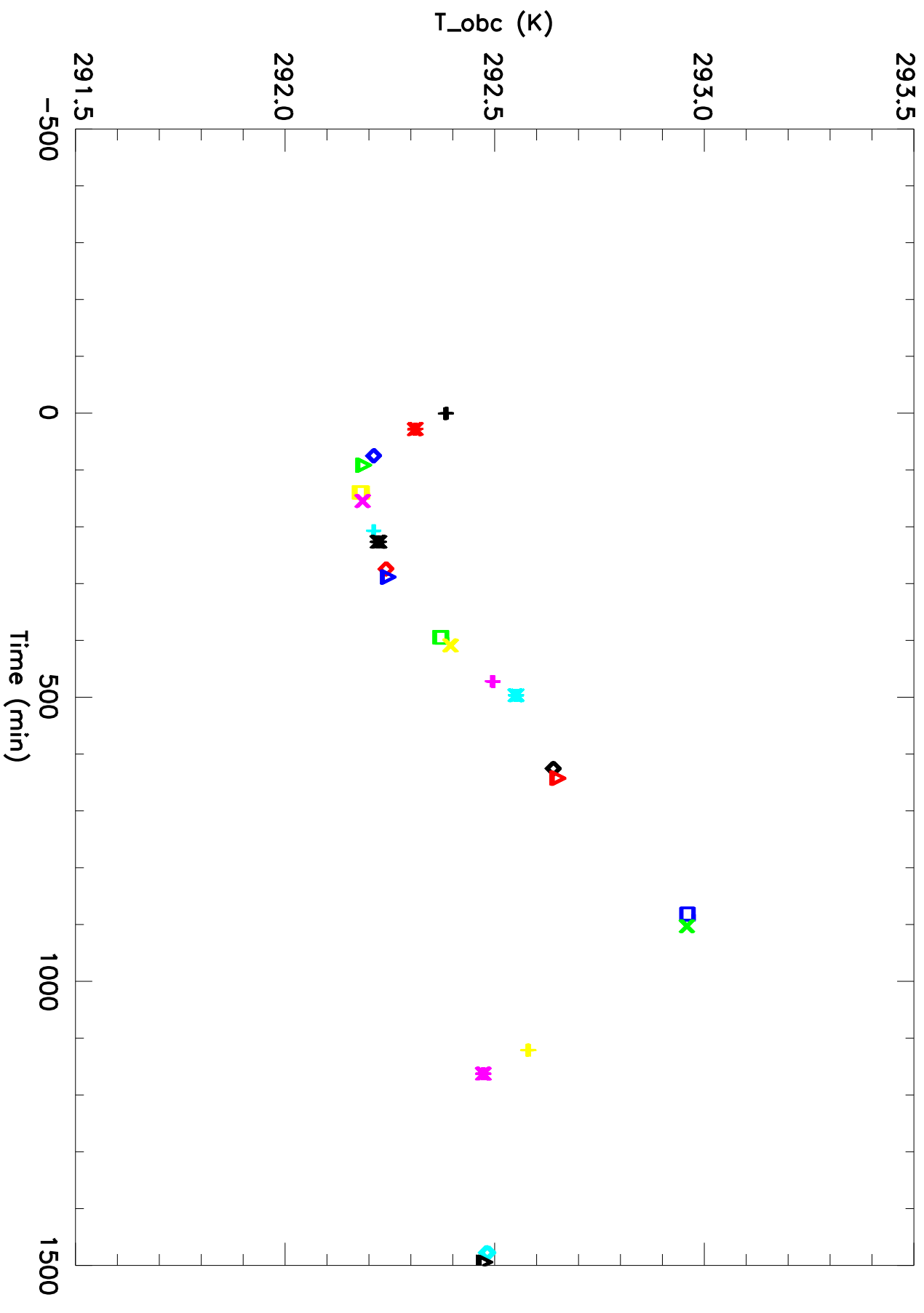
T_sh vs Time



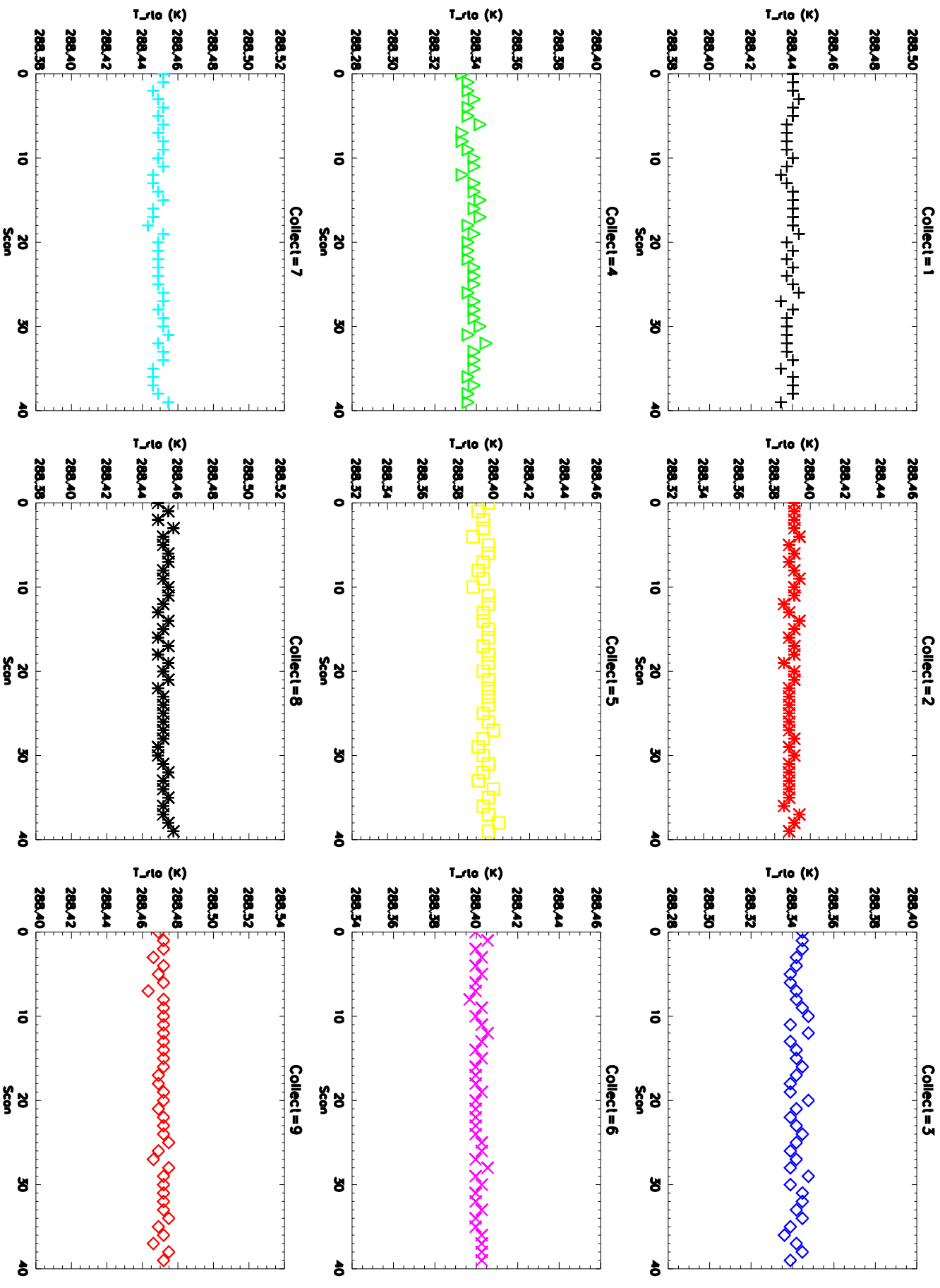
T_{hom} vs Time



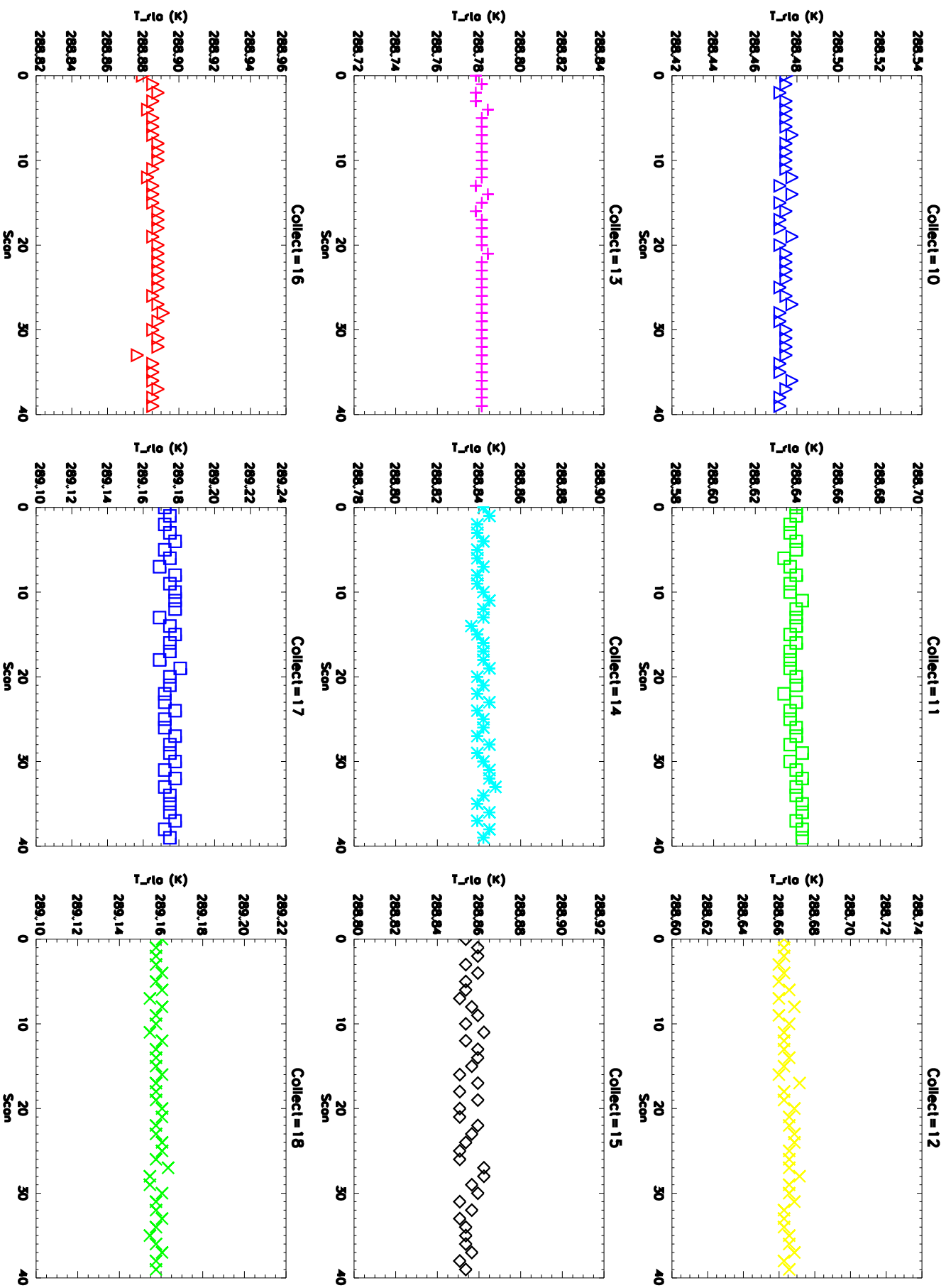
T_abc vs Time



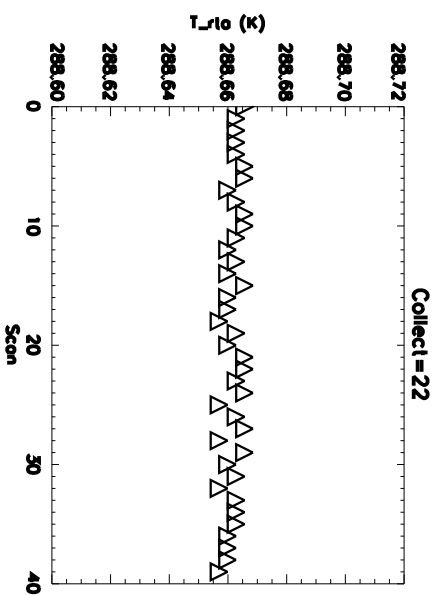
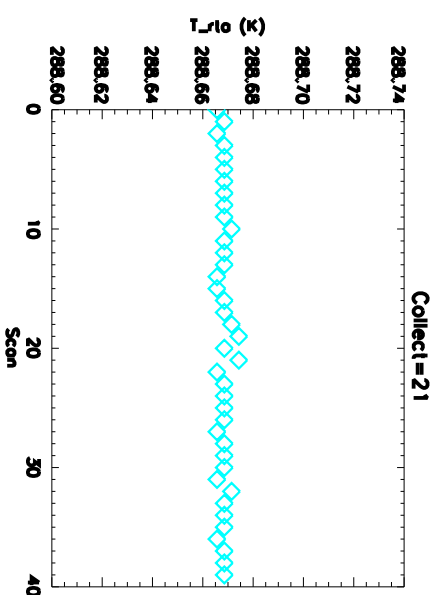
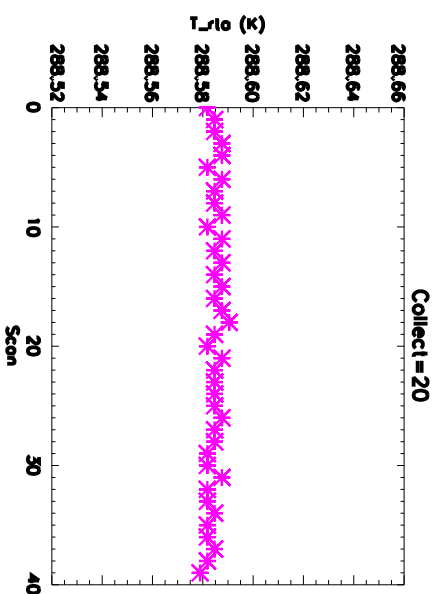
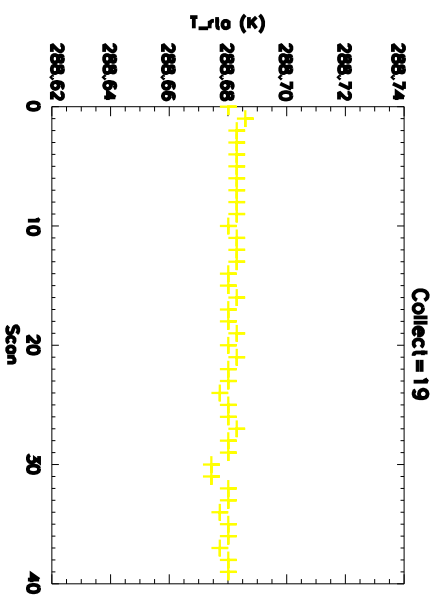
T_rta vs Scan



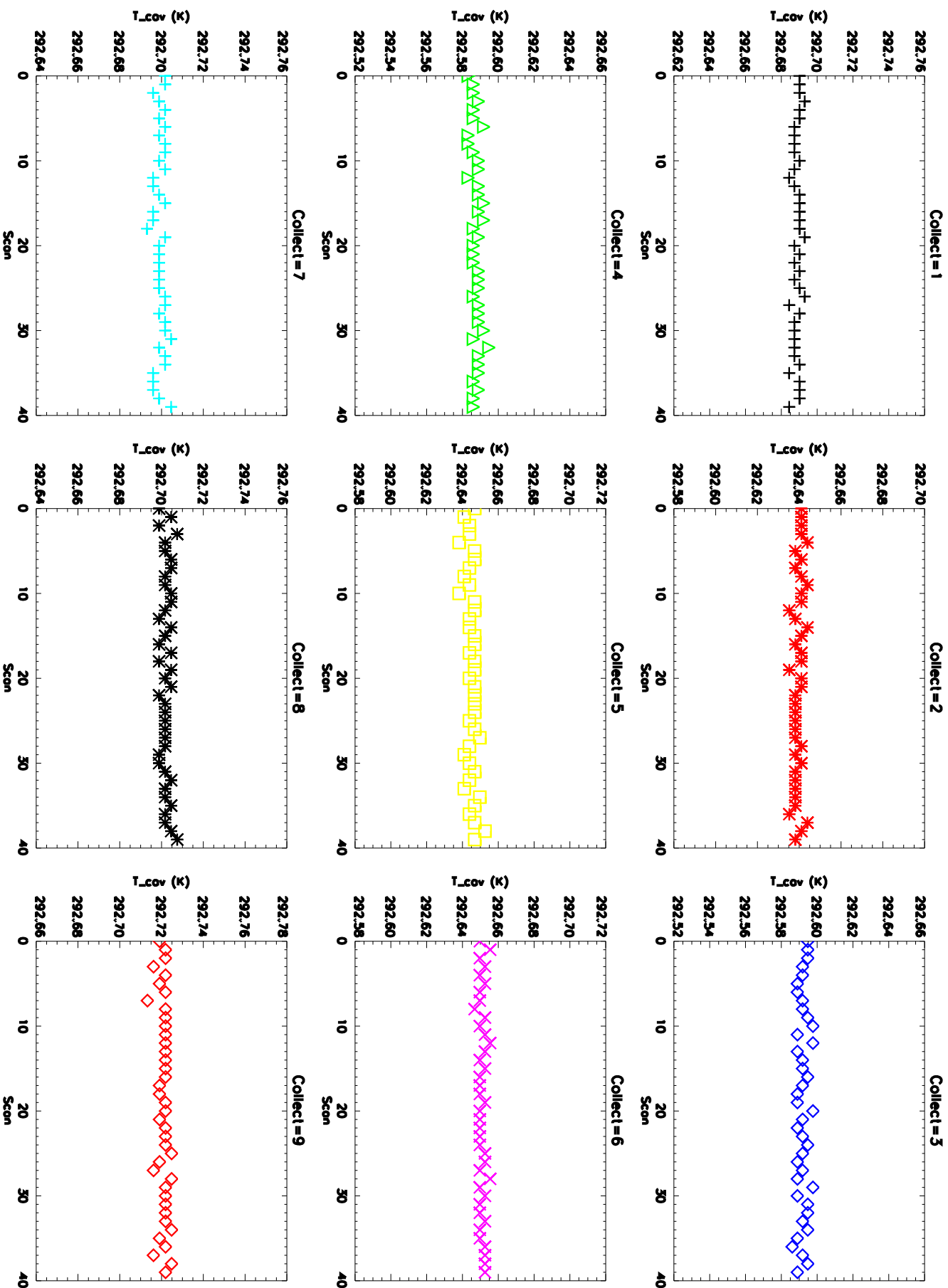
T_rta vs Scan



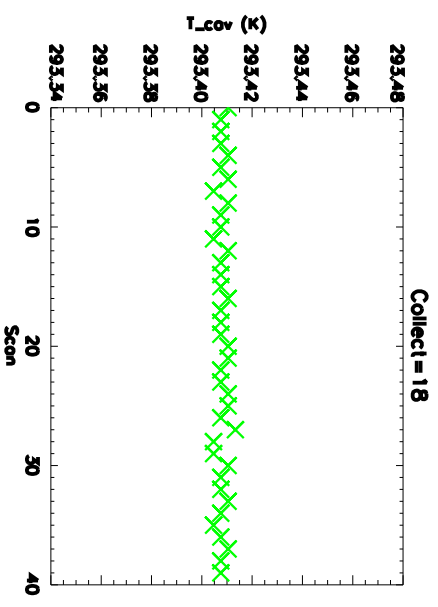
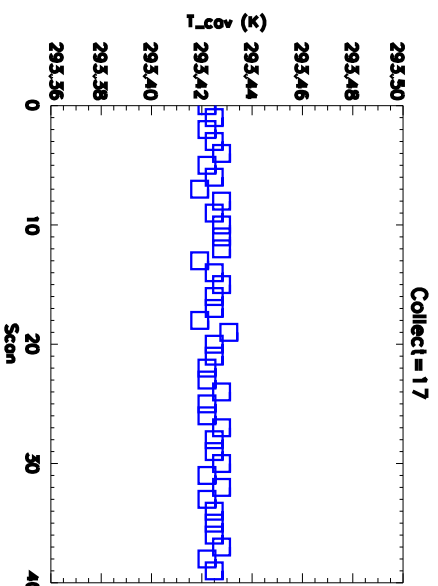
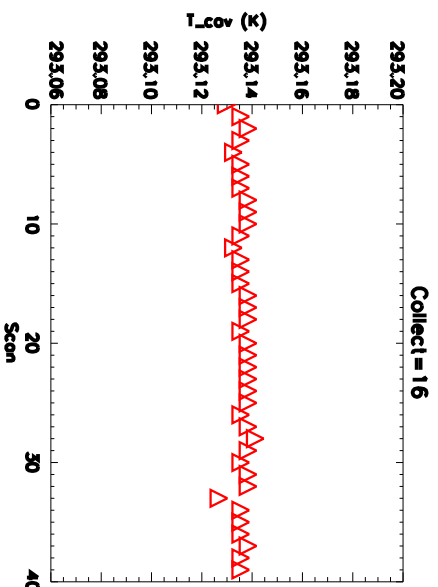
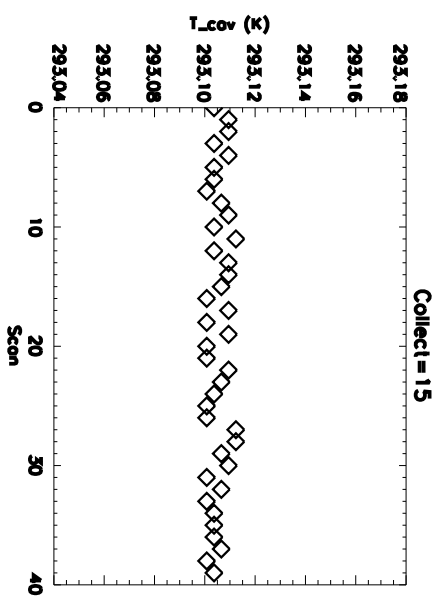
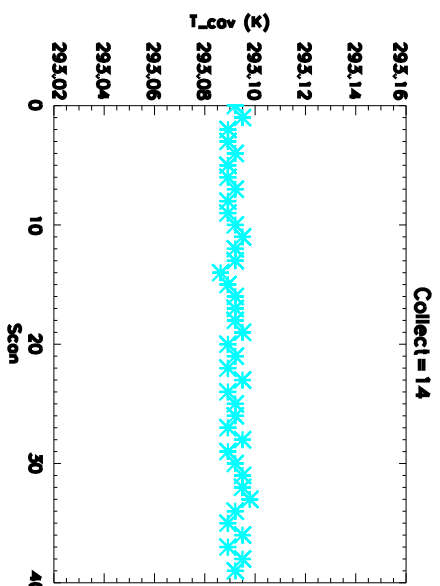
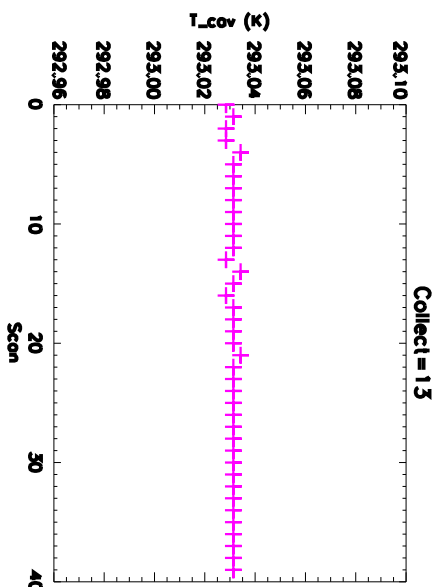
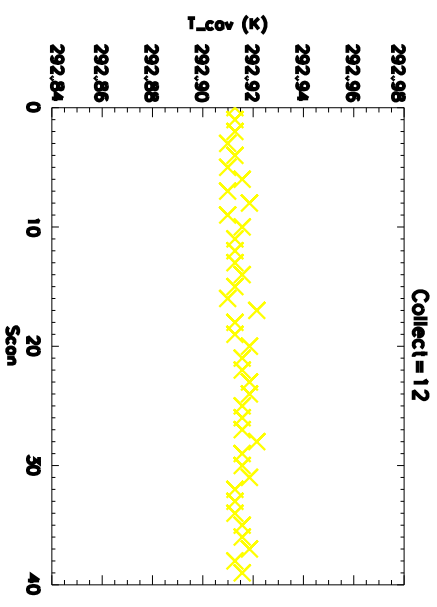
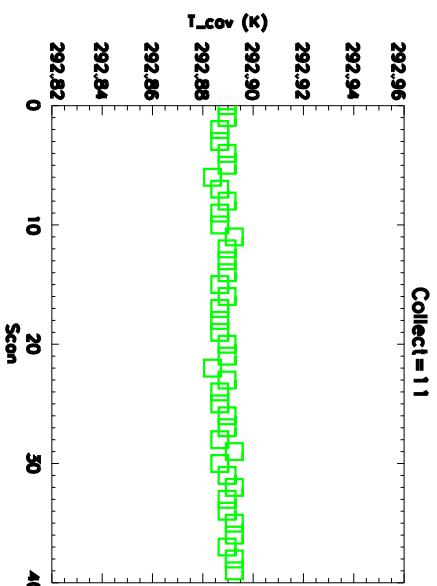
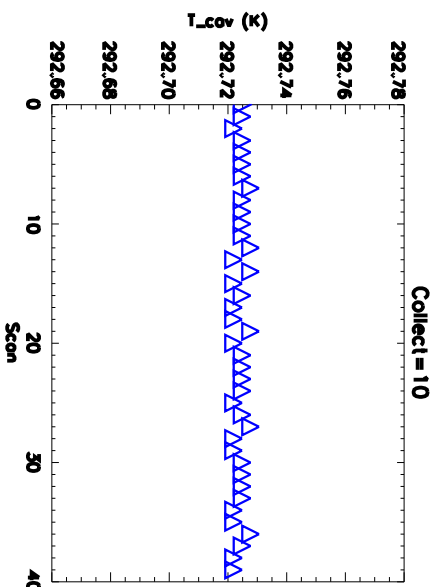
T_rta vs Scan



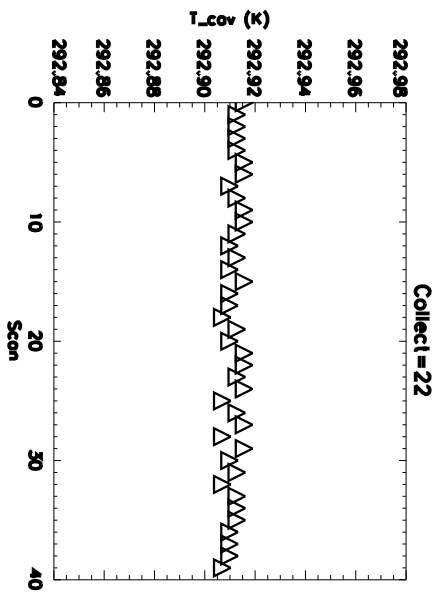
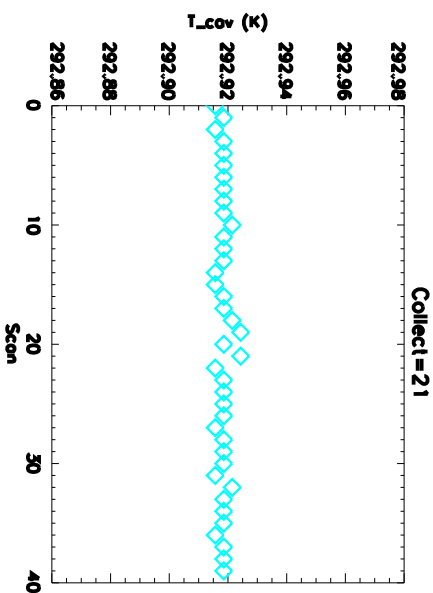
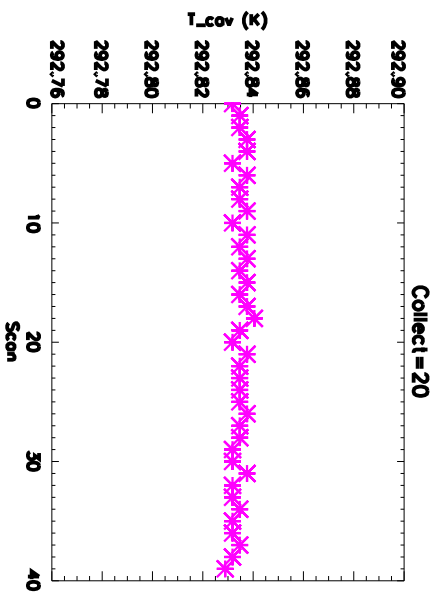
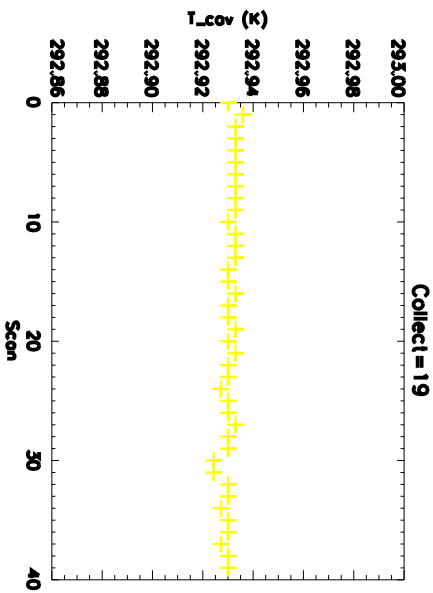
T_COV VS Scon



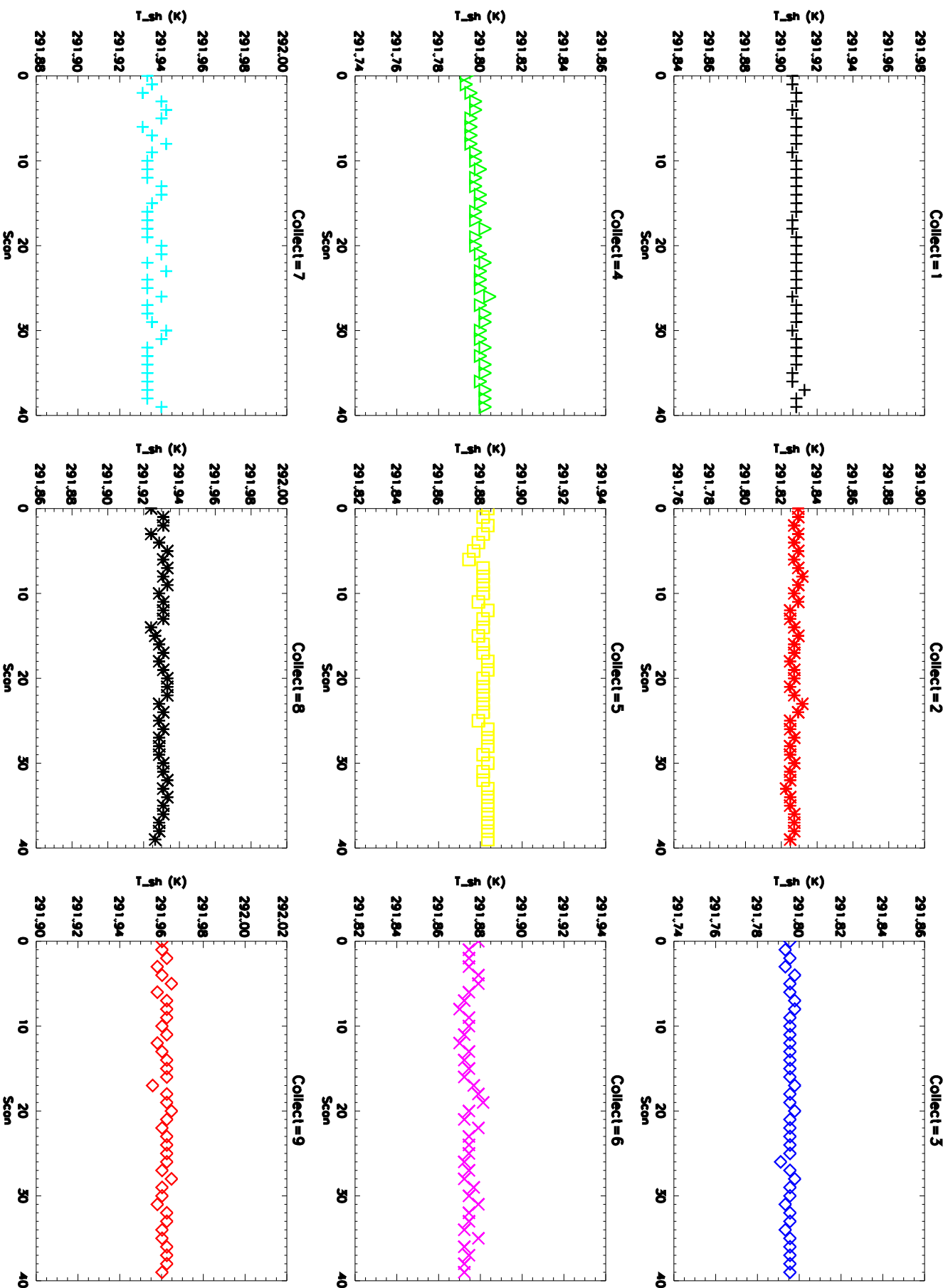
T_COV vs Scon



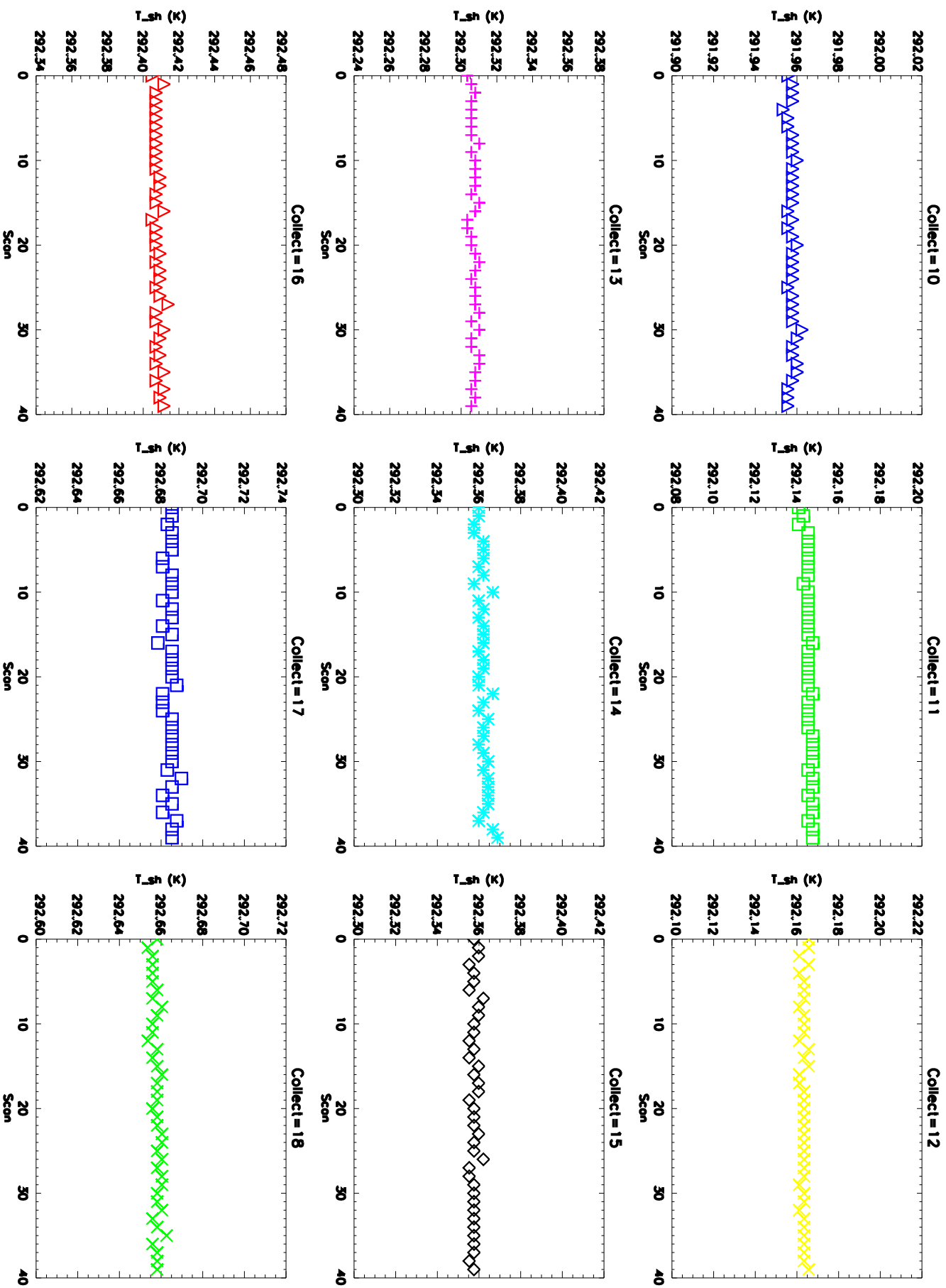
T_COV vs Scan



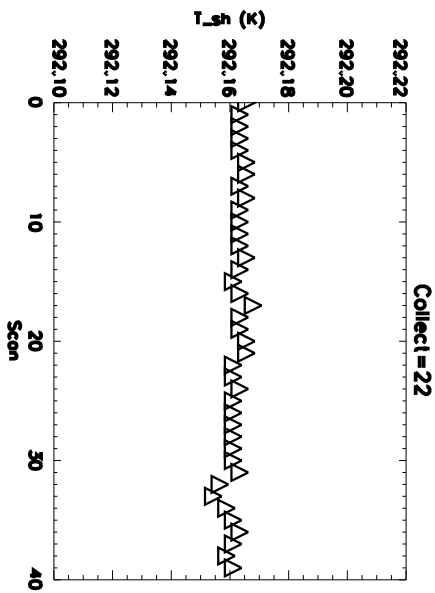
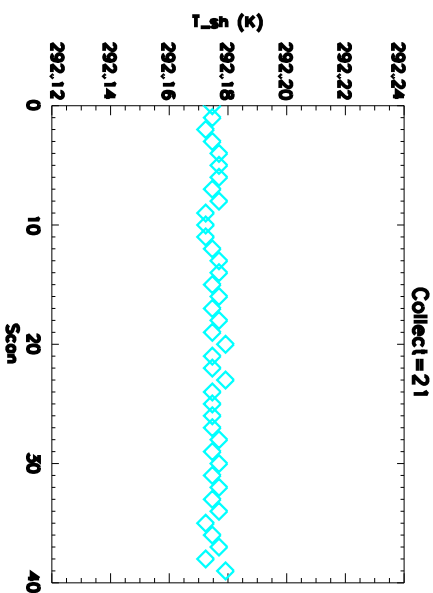
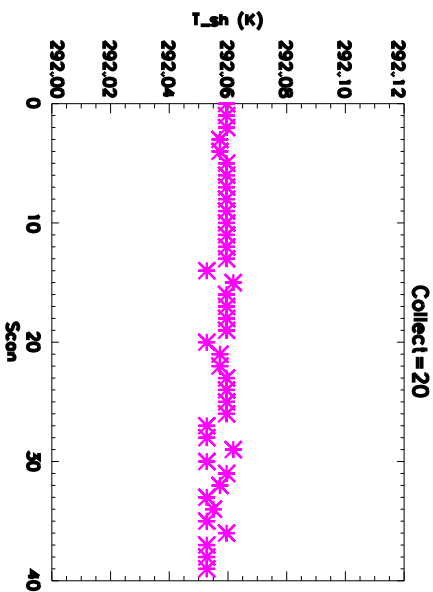
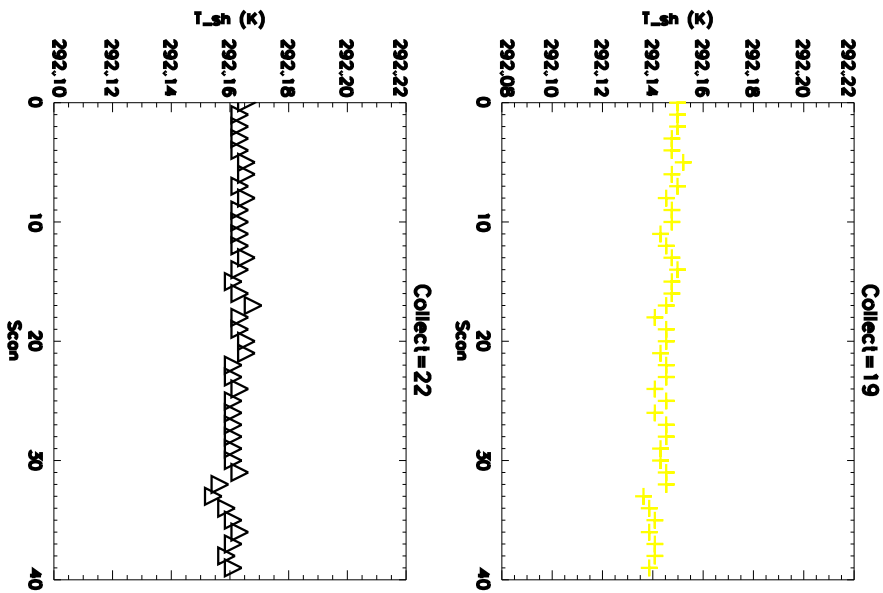
T_sh vs Scan



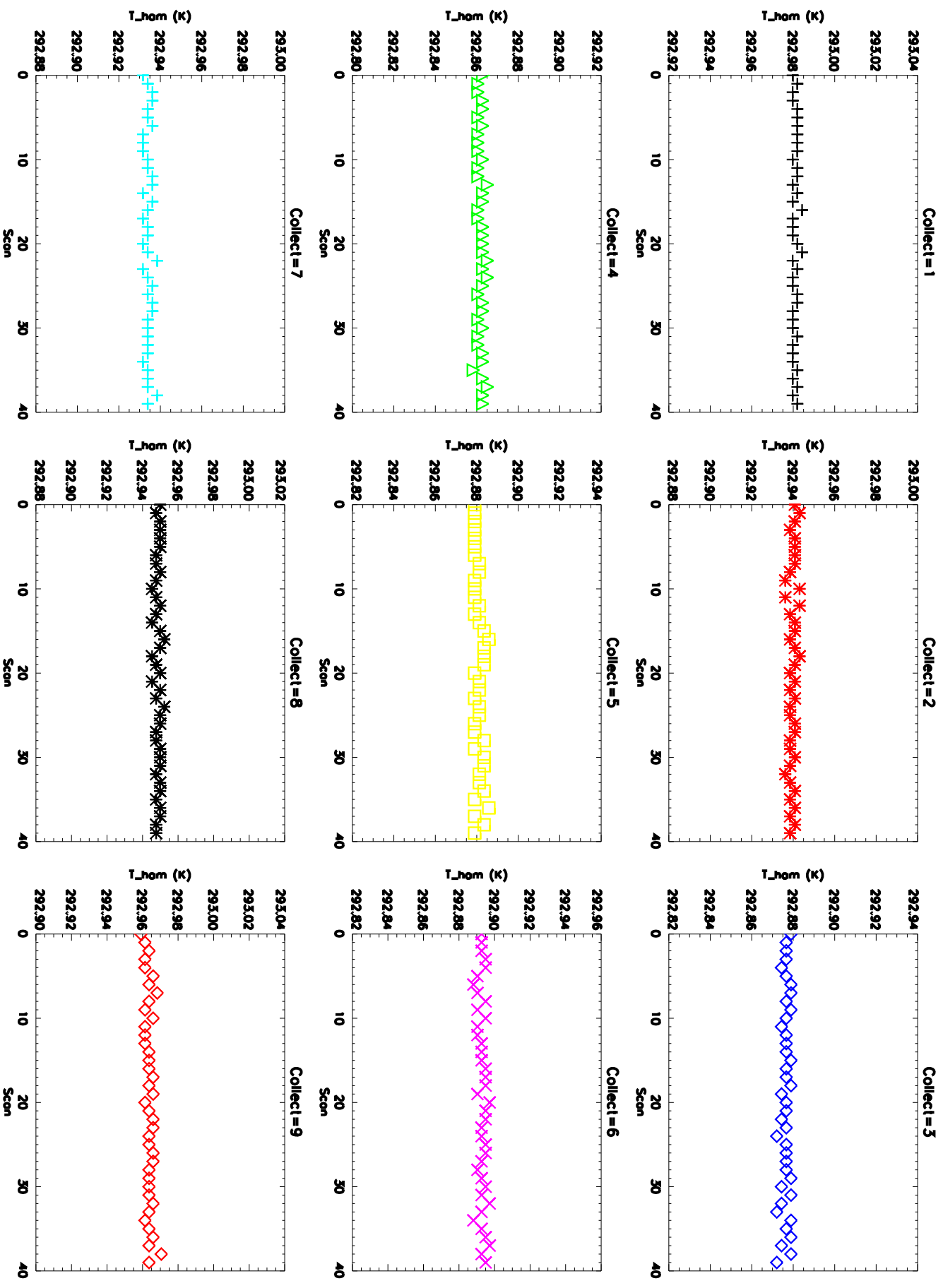
T_sh vs Scon



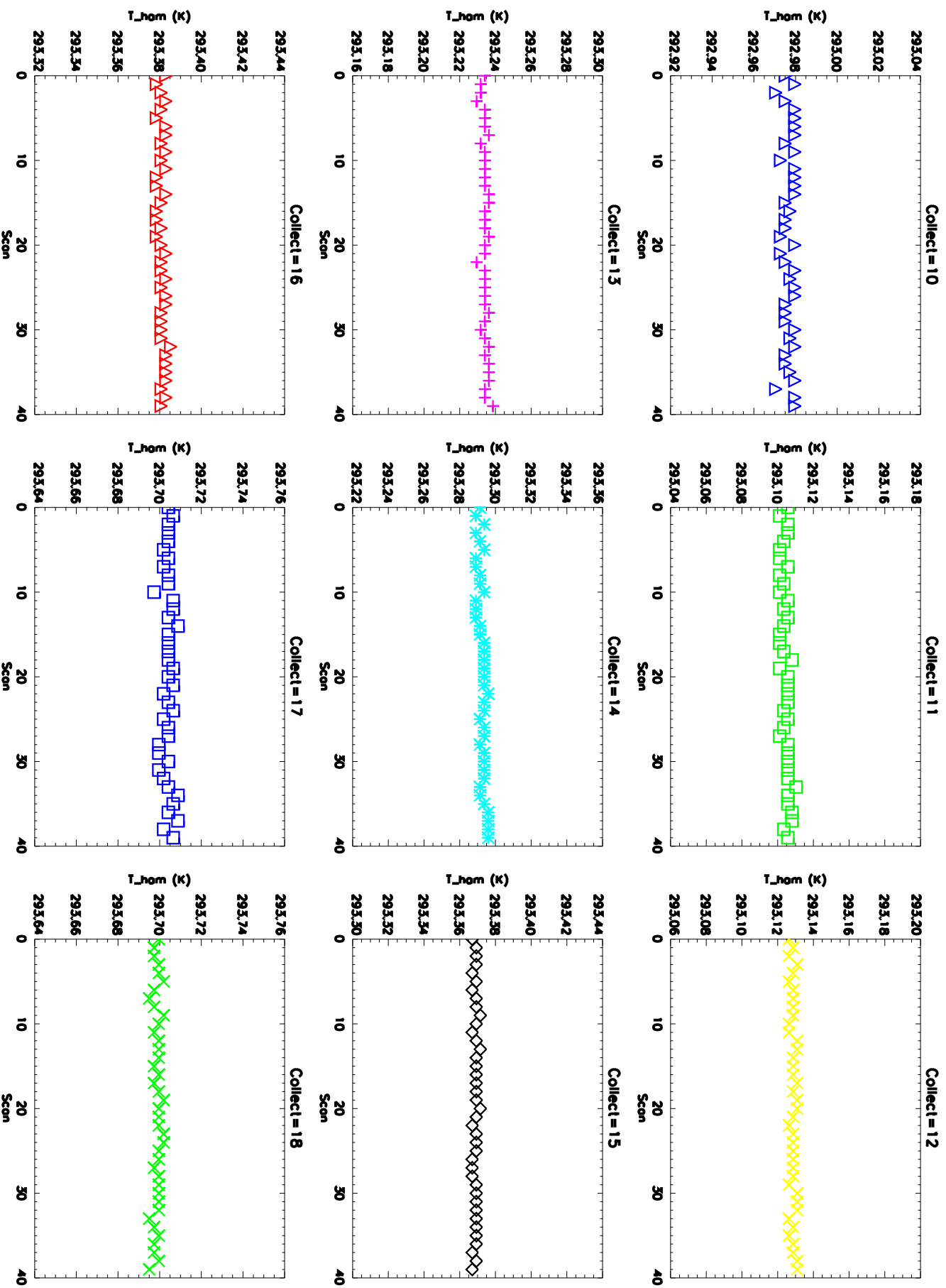
T_sh vs Scan



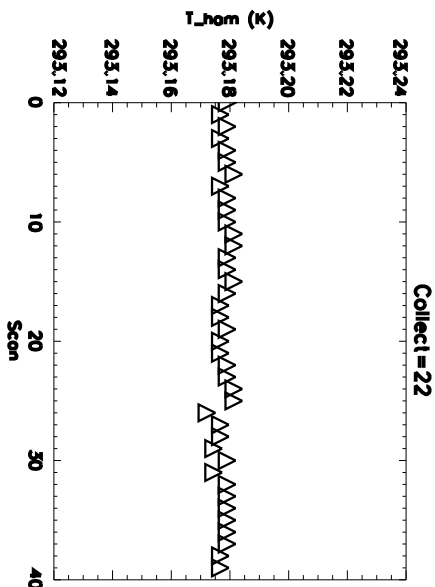
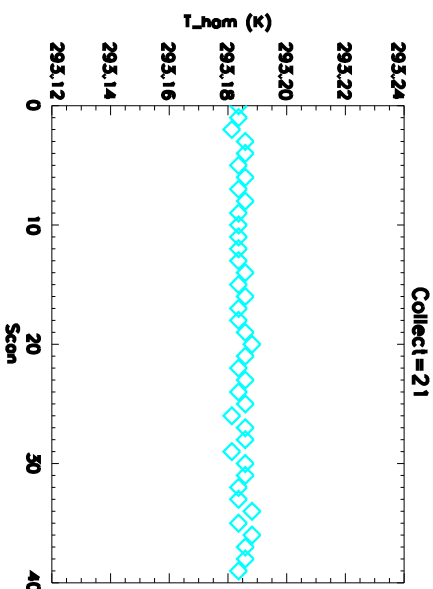
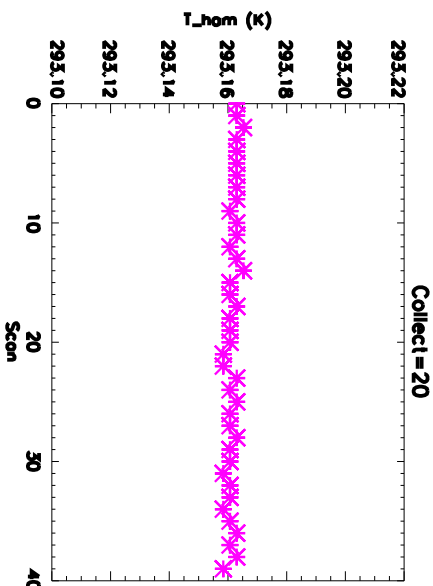
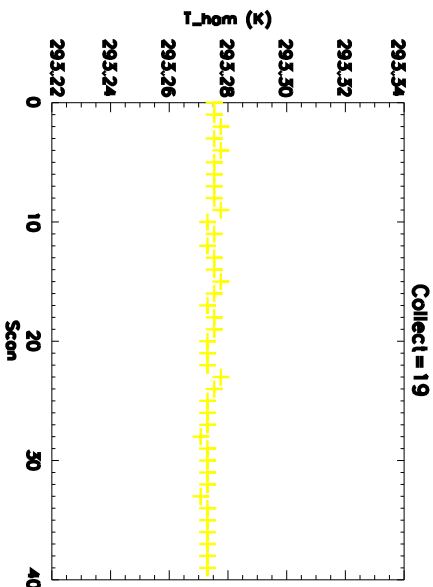
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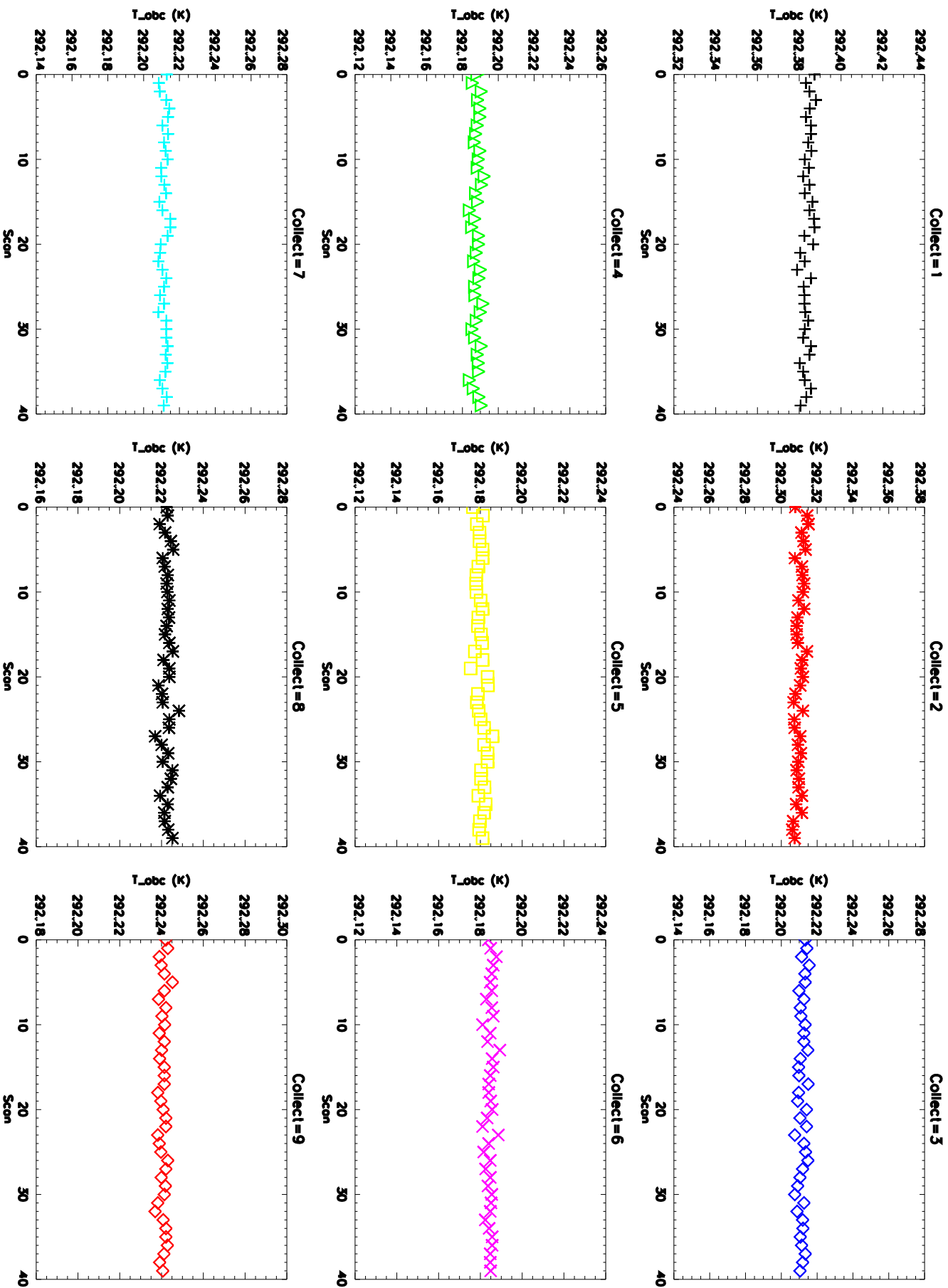
T_ham vs Scan



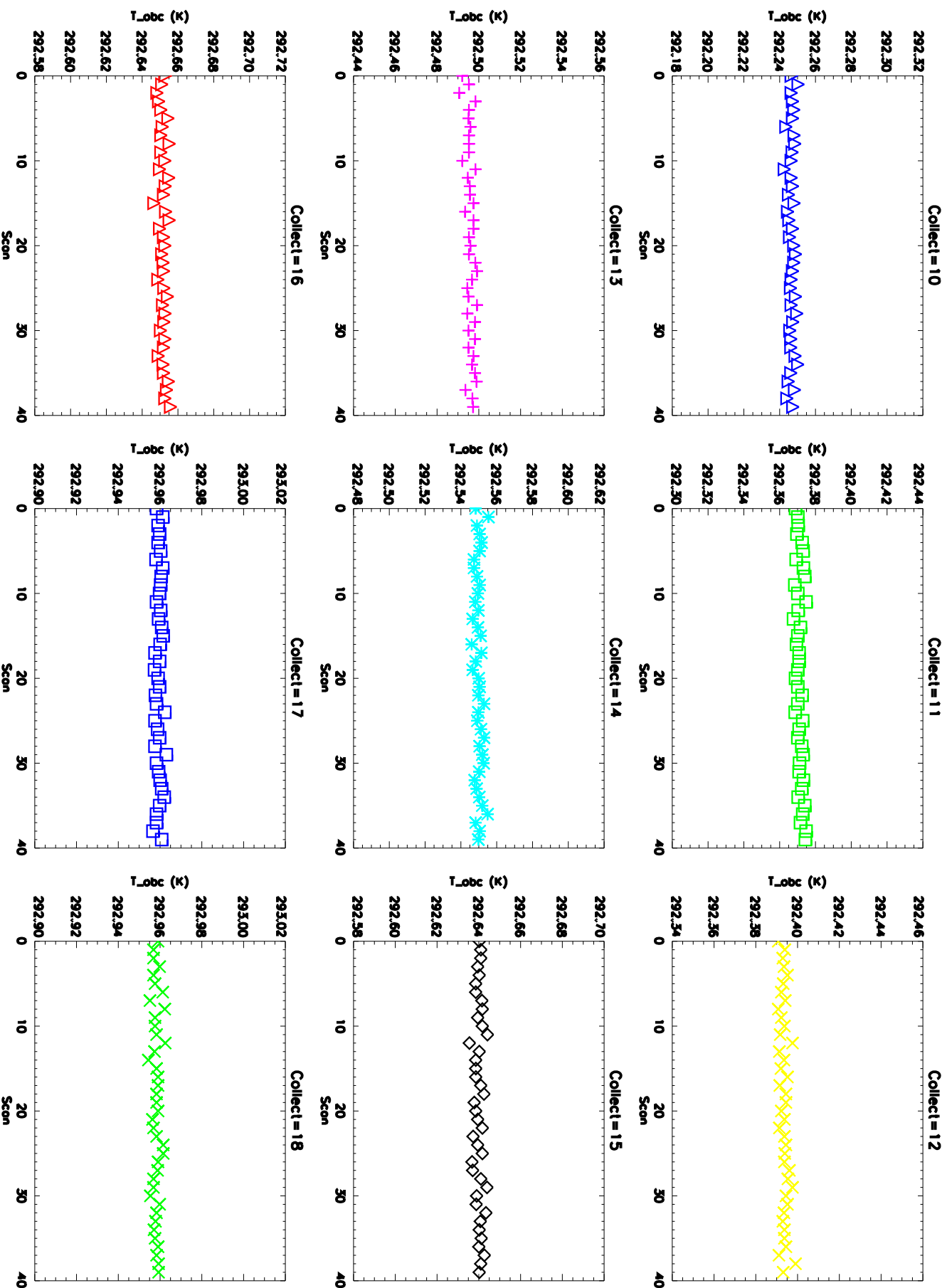
T_ham vs Scan



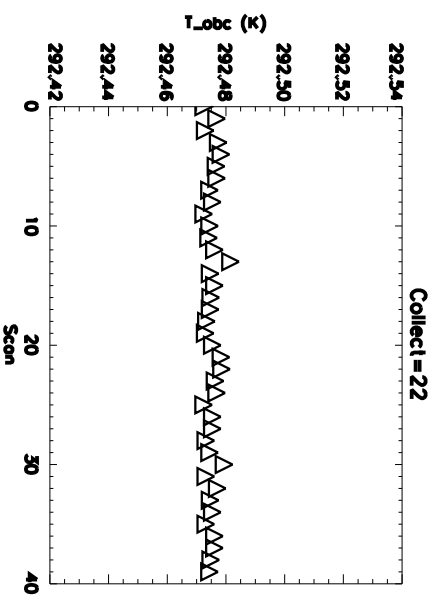
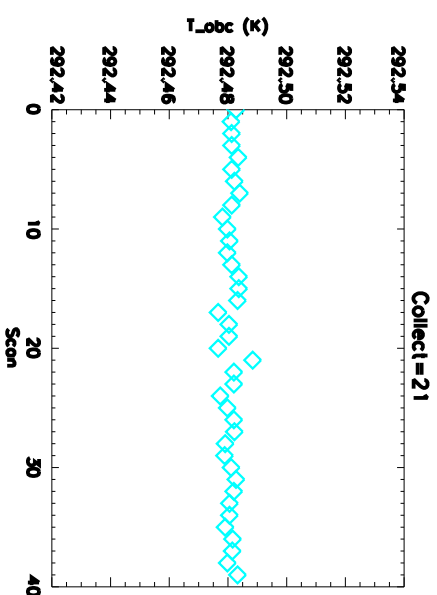
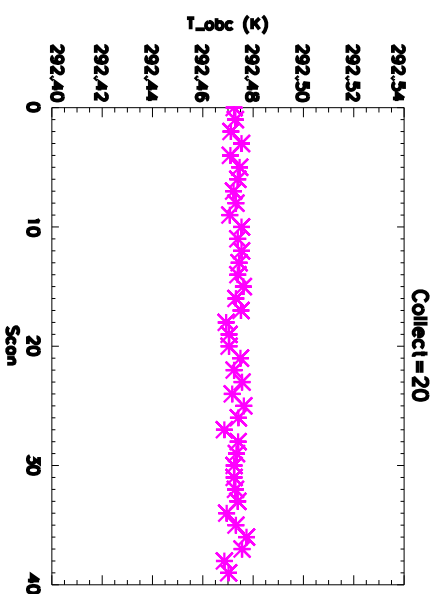
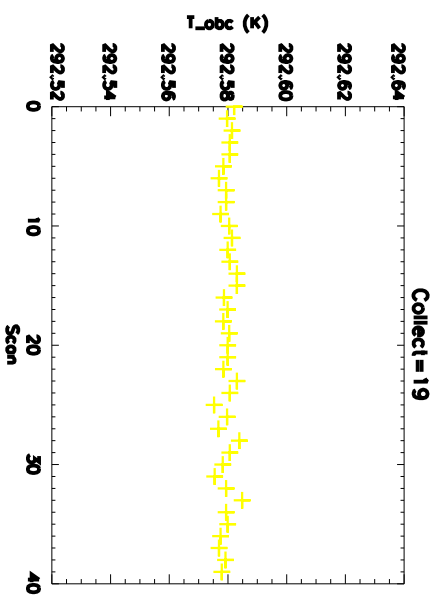
T_obc vs Scan



T_obc vs Scan

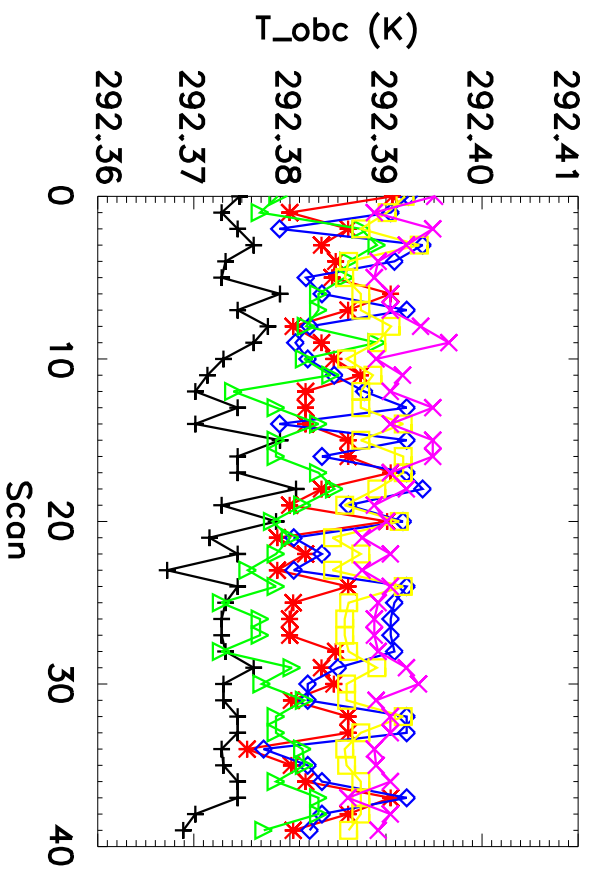


T_obc vs Scan

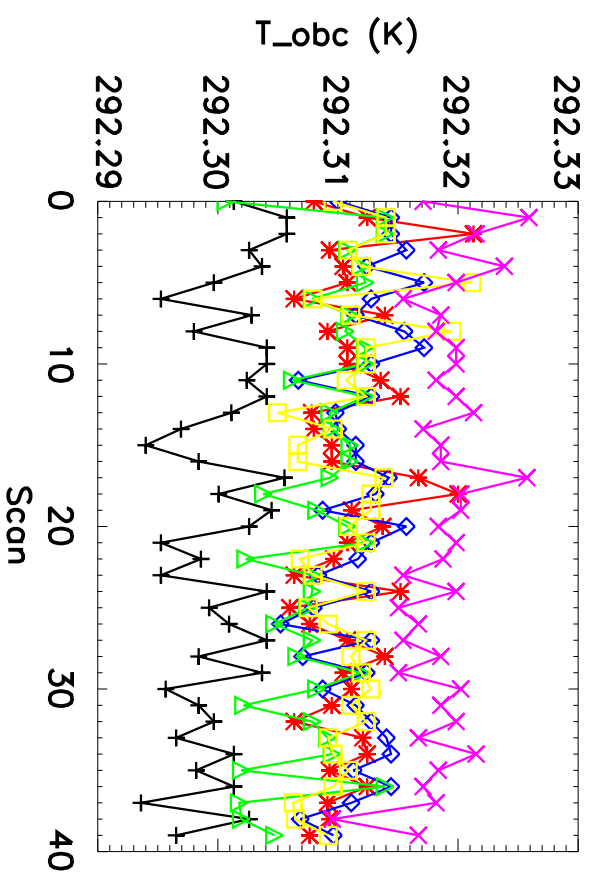


T_OBC per thermistors vs Scan

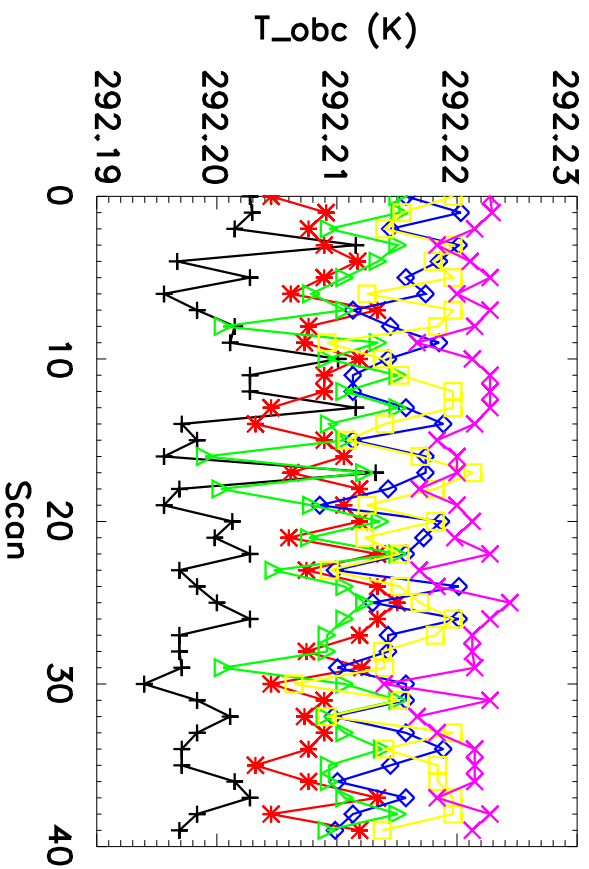
Collect=1



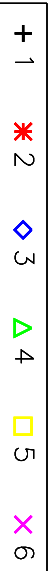
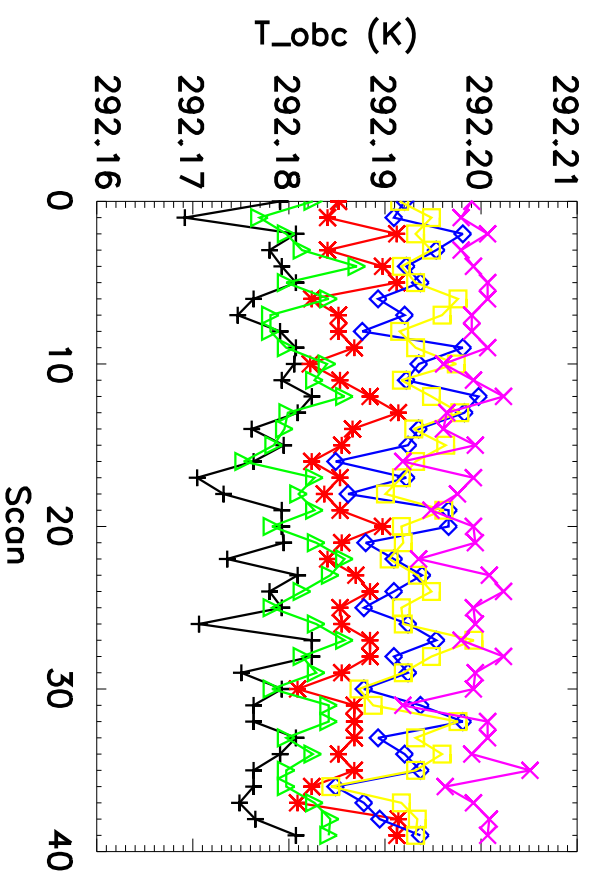
Collect=2



Collect=3

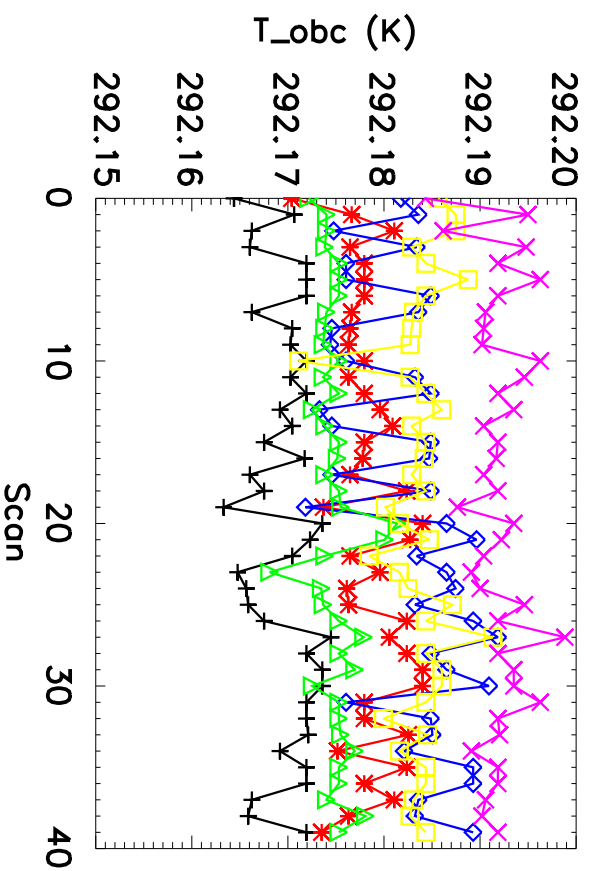


Collect=4

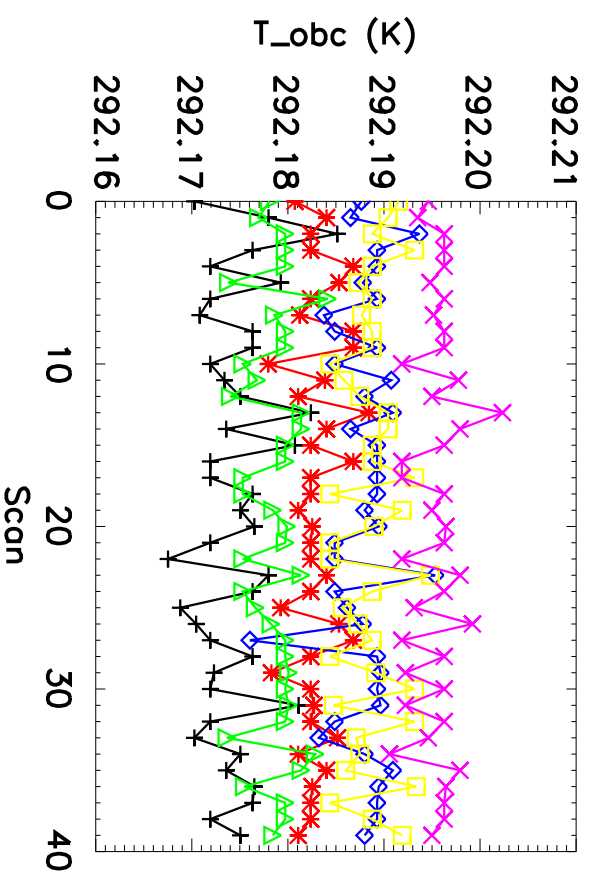


T_OBC per thermistors vs Scan

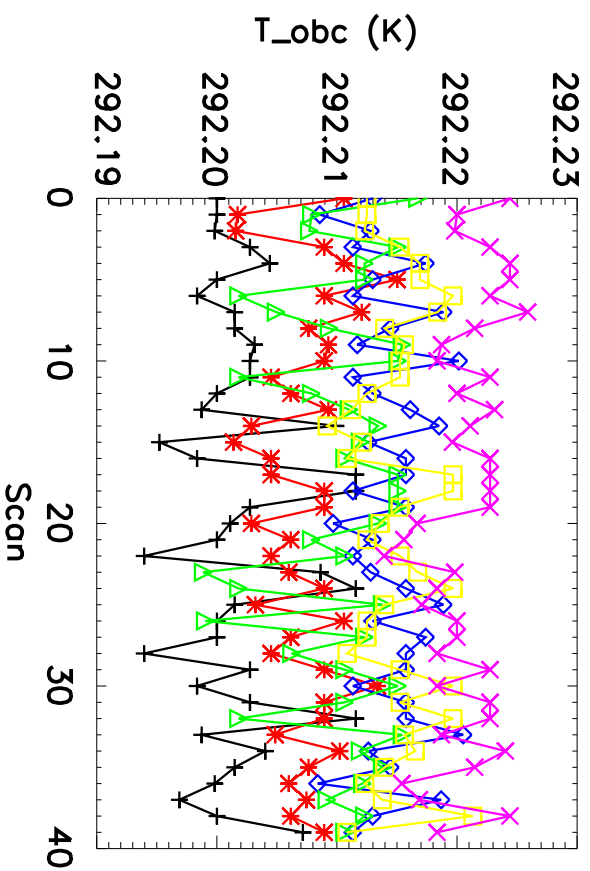
Collect=5



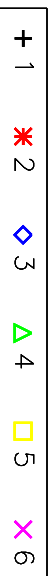
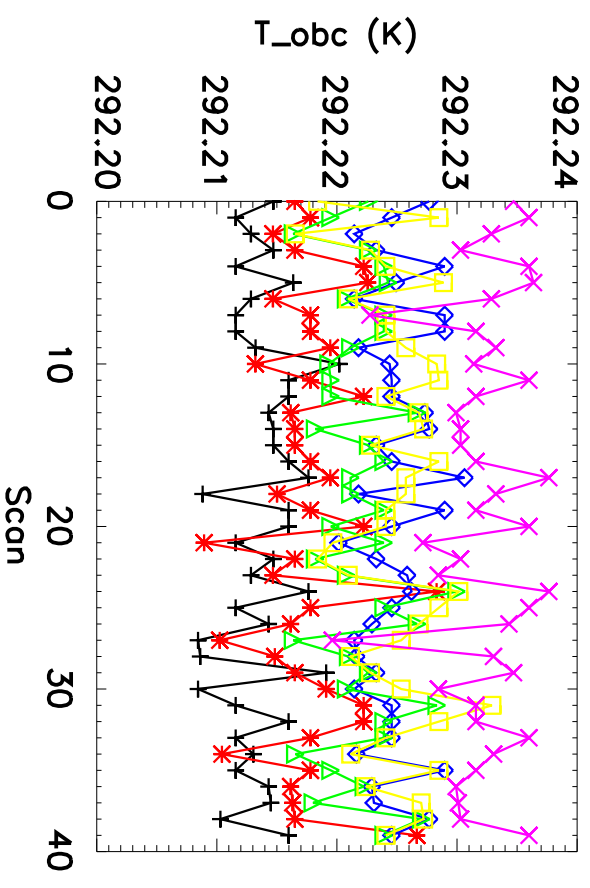
Collect=6



Collect=7

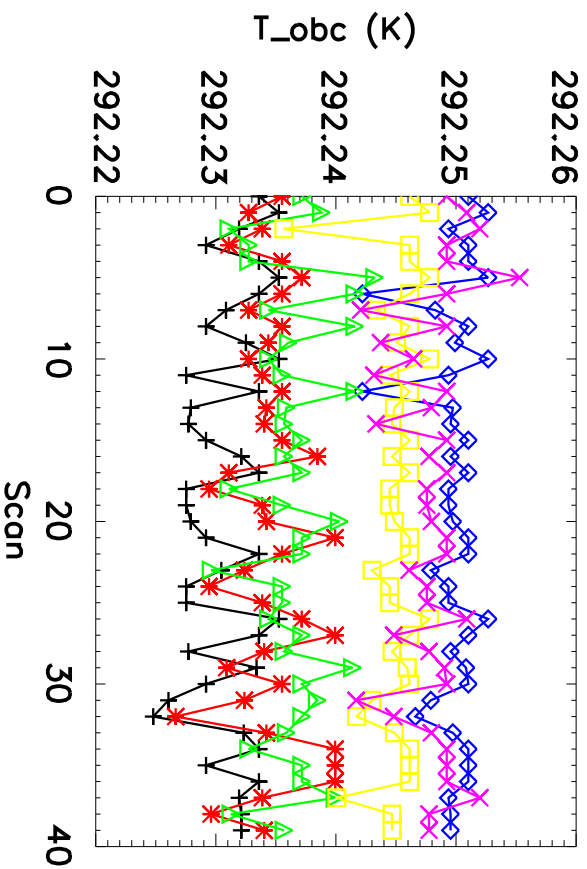


Collect=8

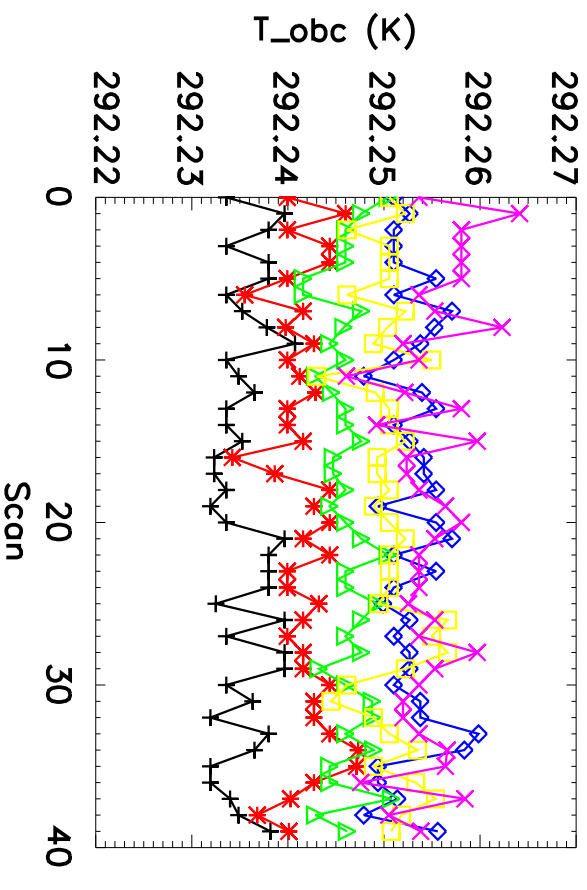


T_OBC per thermistors vs Scan

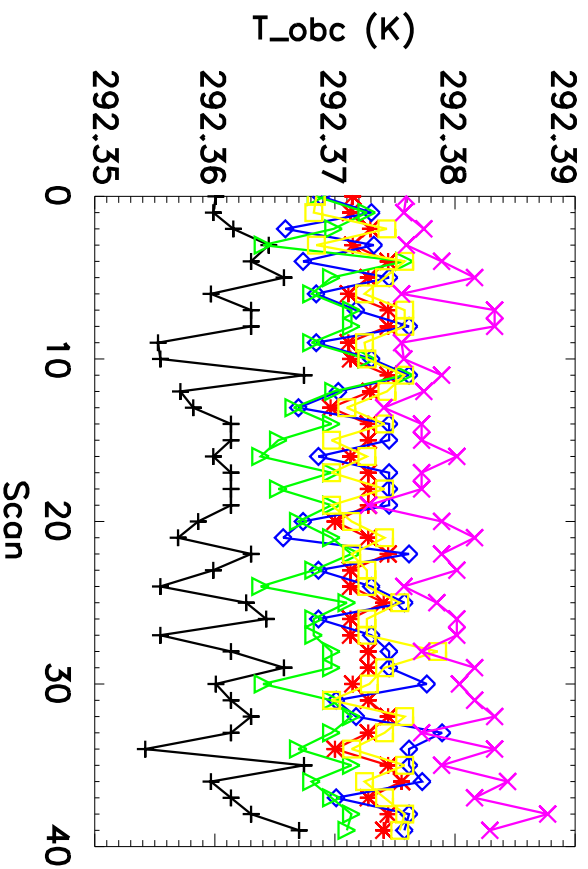
Collect=9



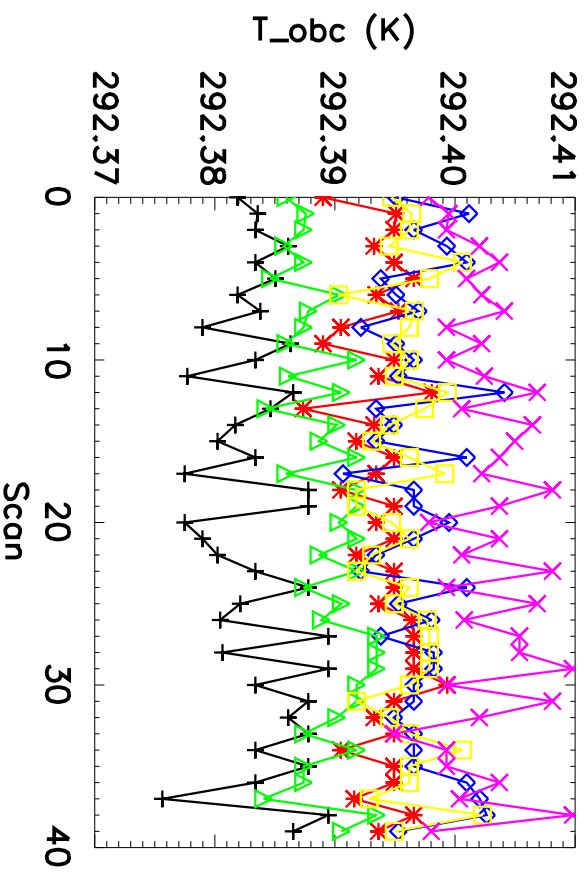
Collect=10



Collect=11

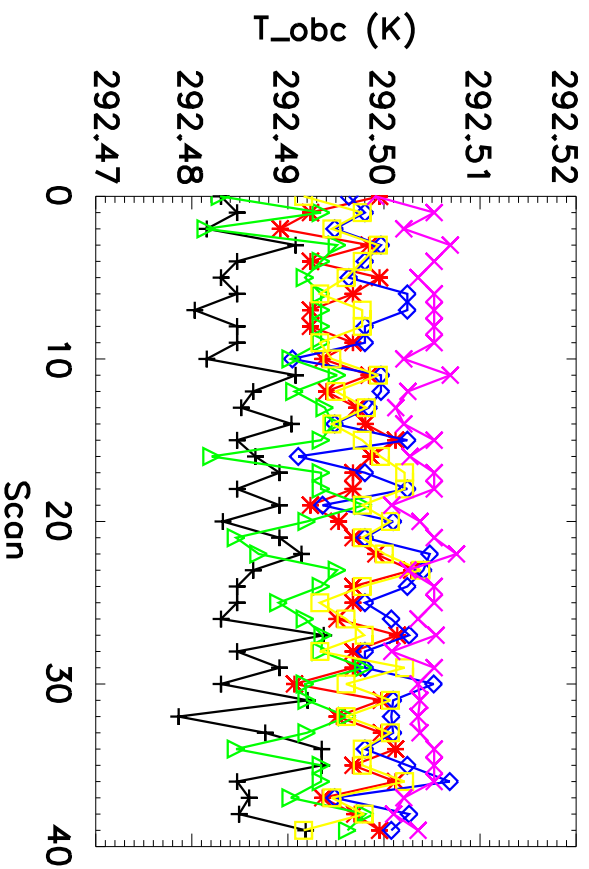


Collect=12

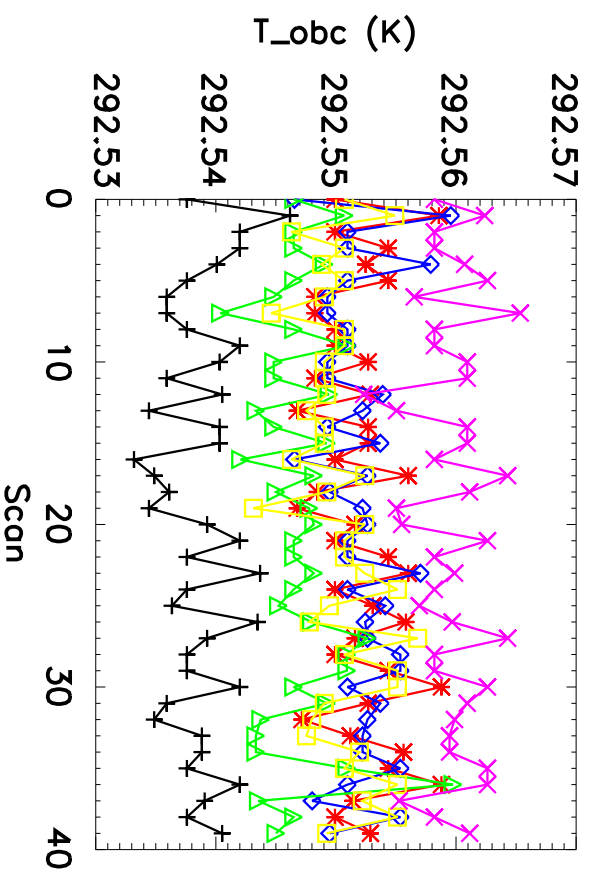


T_OBC per thermistors vs Scan

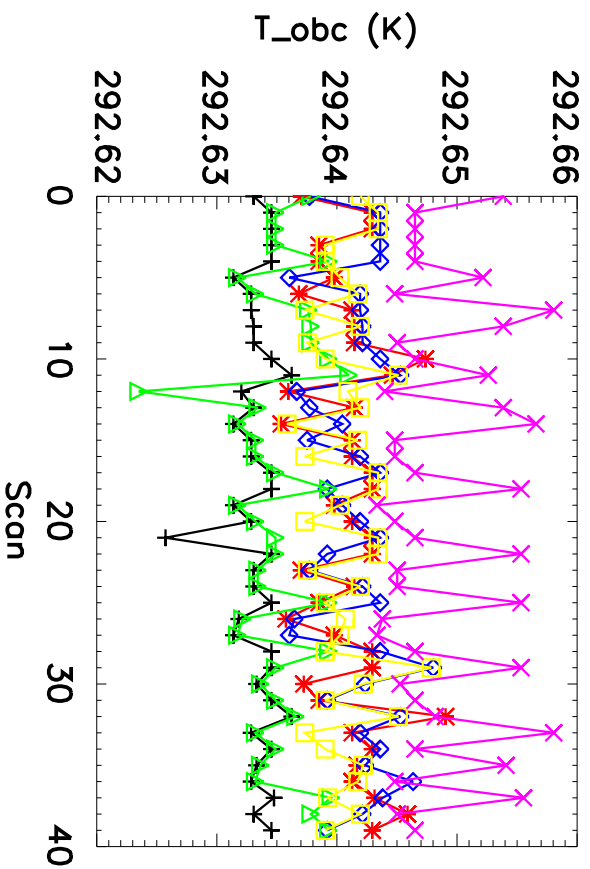
Collect=13



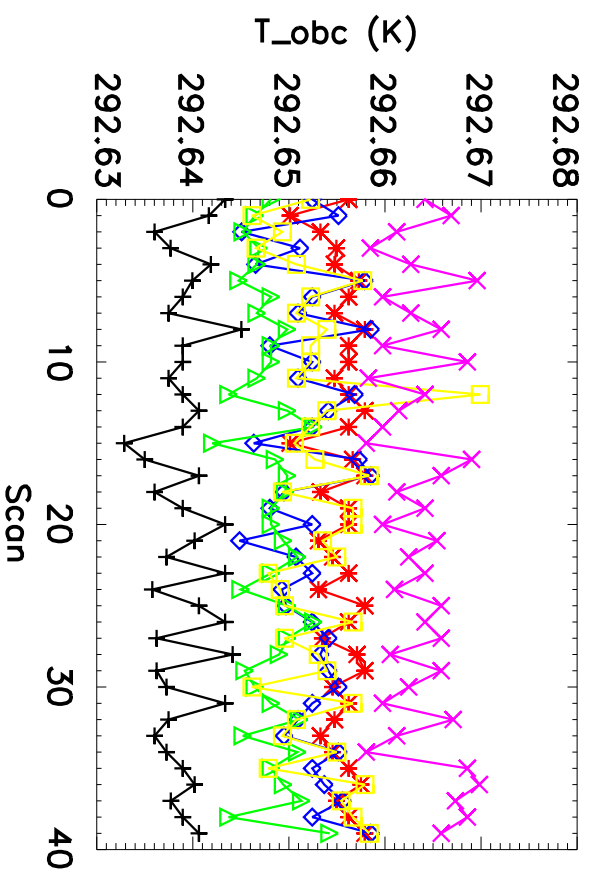
Collect=14



Collect=15



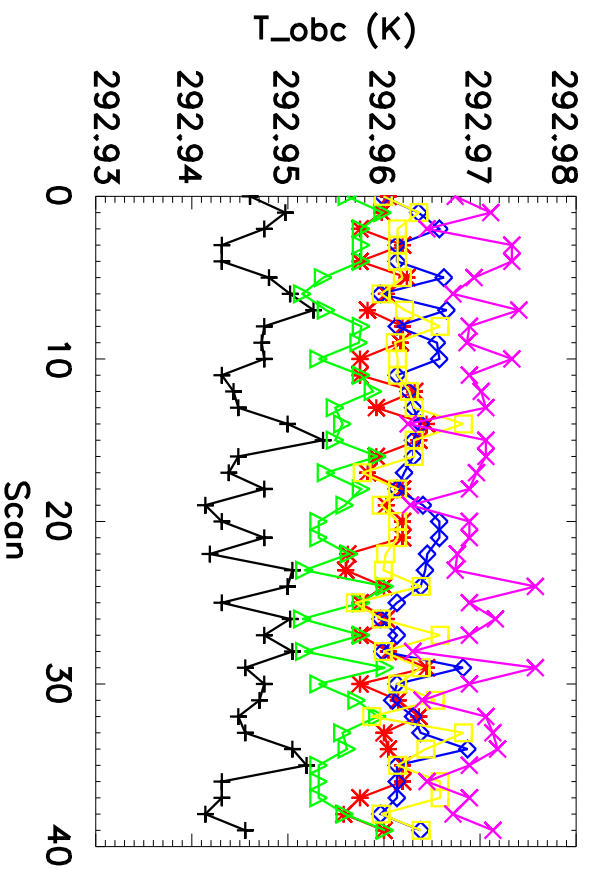
Collect=16



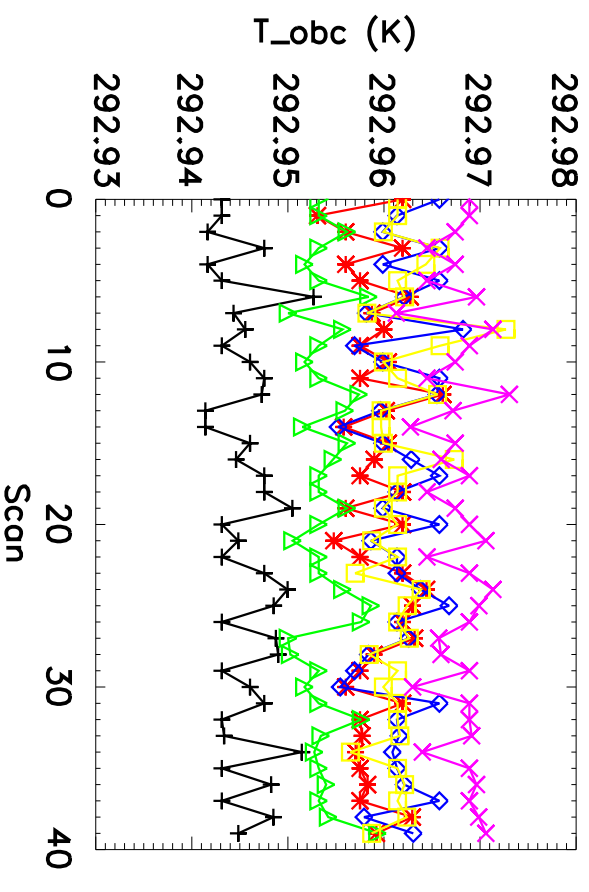
+ 1 * 2 ◇ 3 ▲ 4 □ 5 × 6

T_OBC per thermistors vs Scan

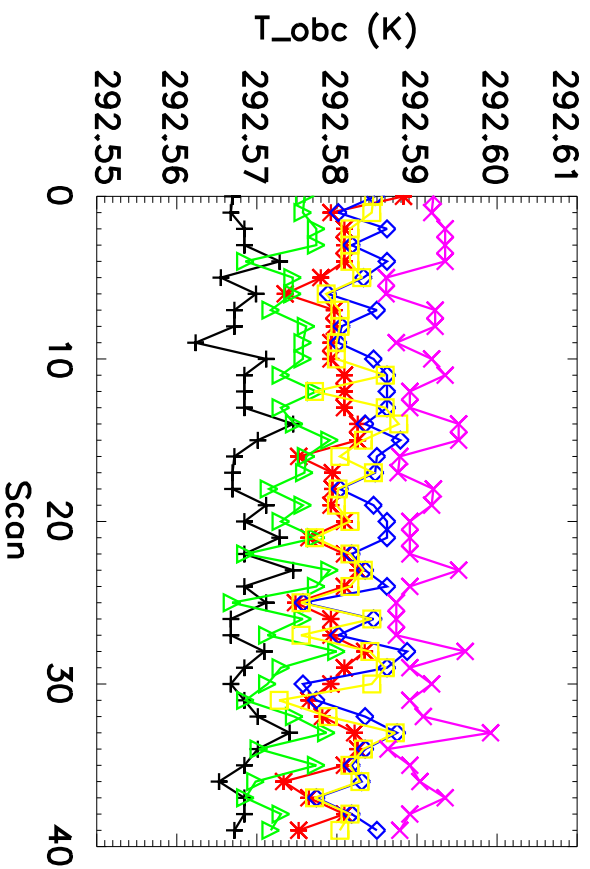
Collect=17



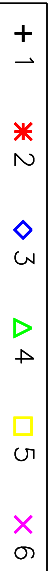
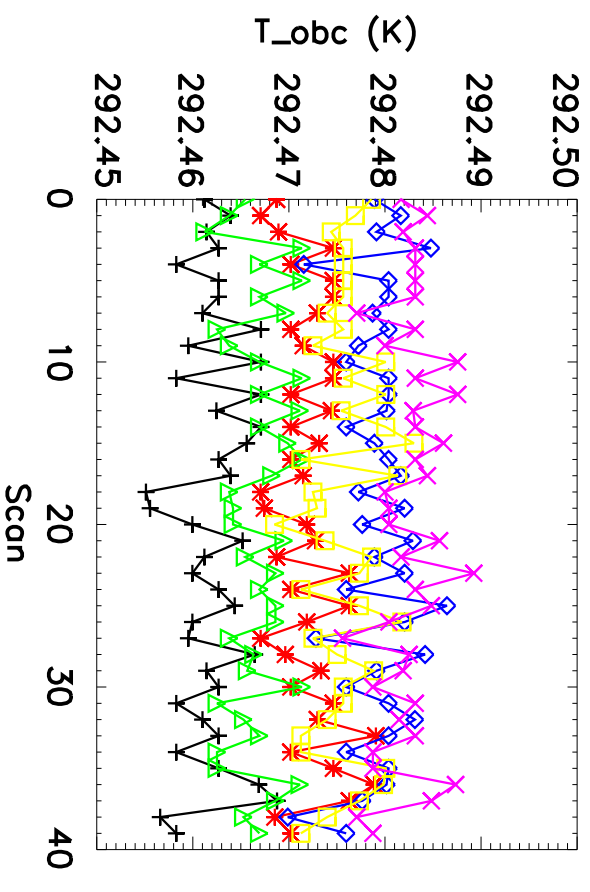
Collect=18



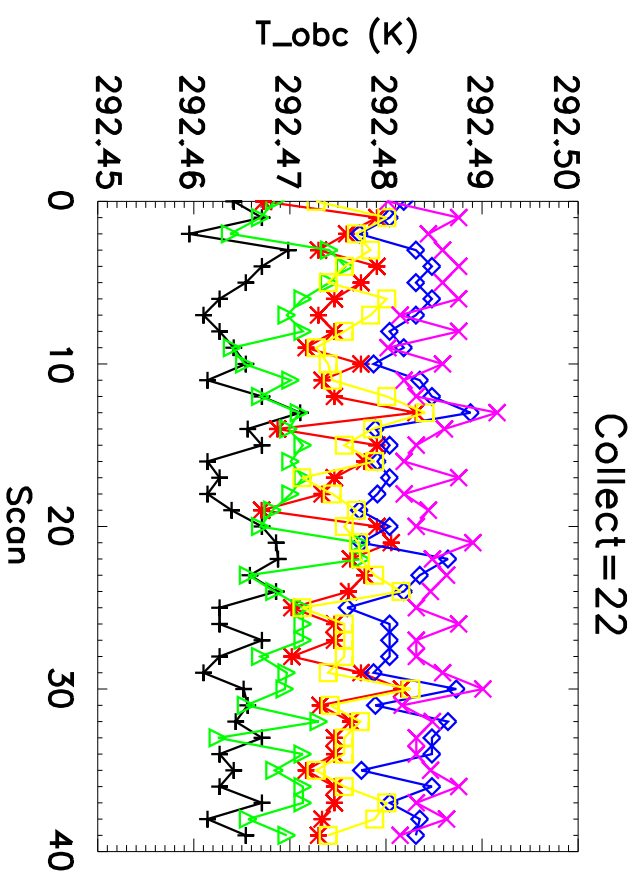
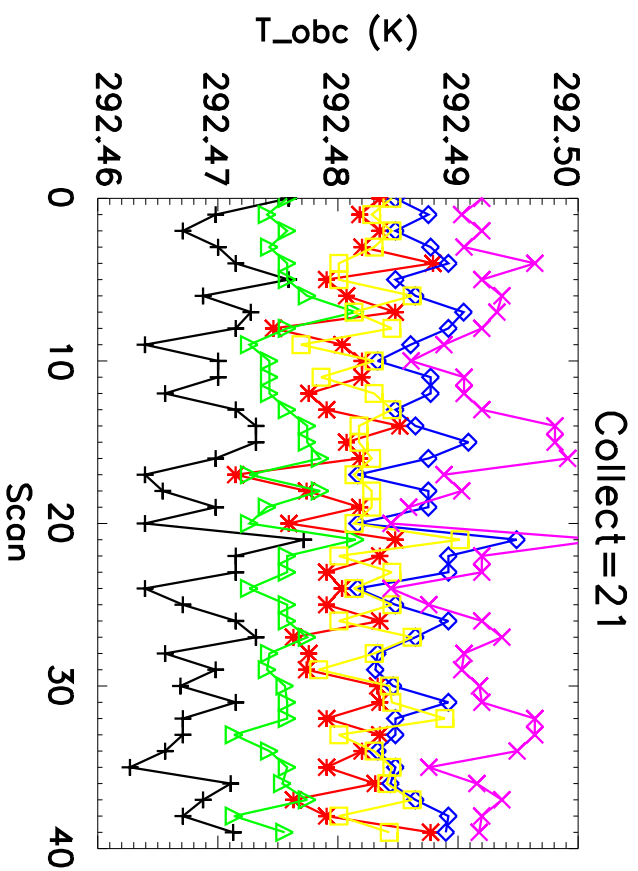
Collect=19



Collect=20

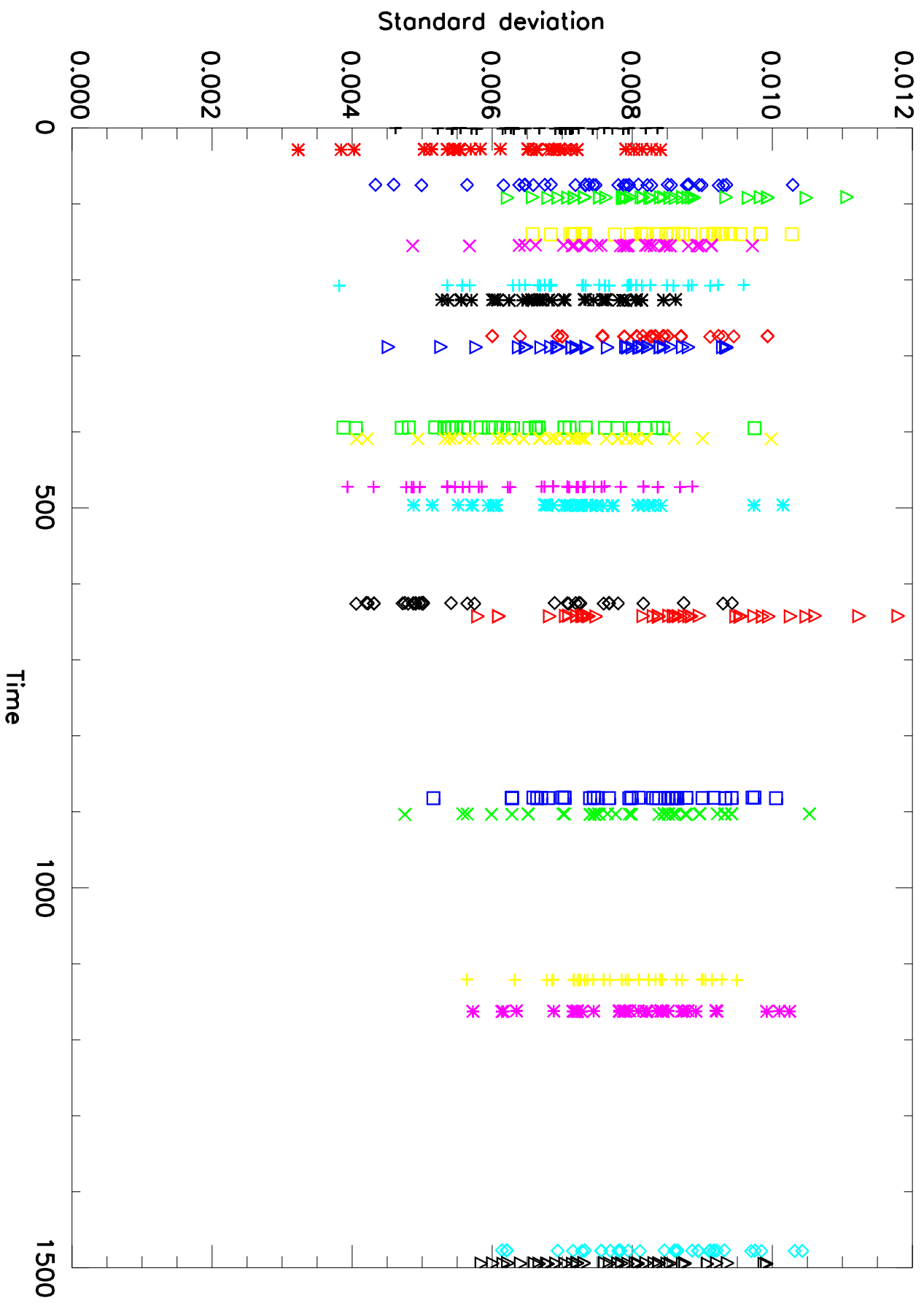


T_OBC per thermistors vs Scan

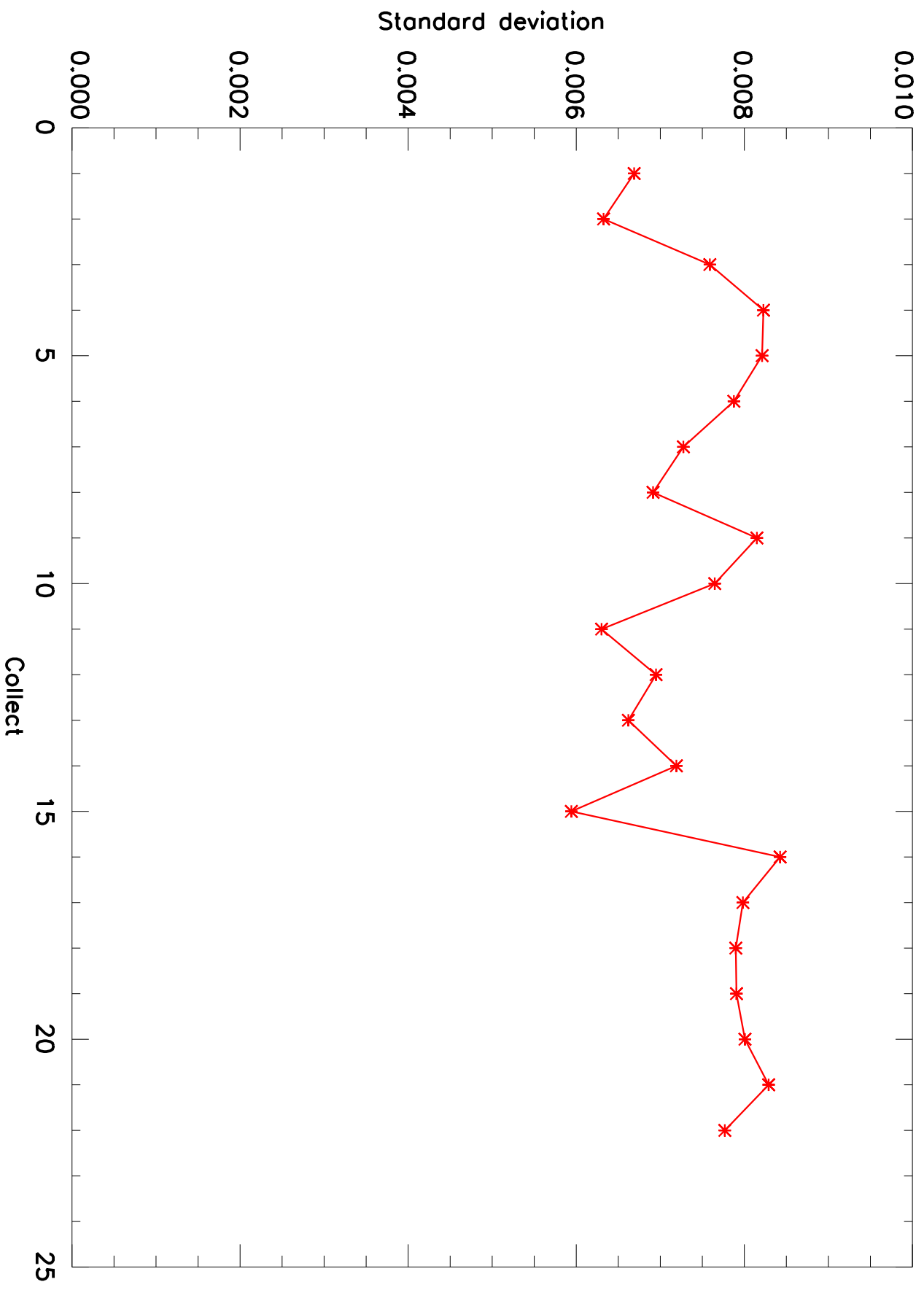


+ 1 * 2 ◇ 3 △ 4 □ 5 × 6

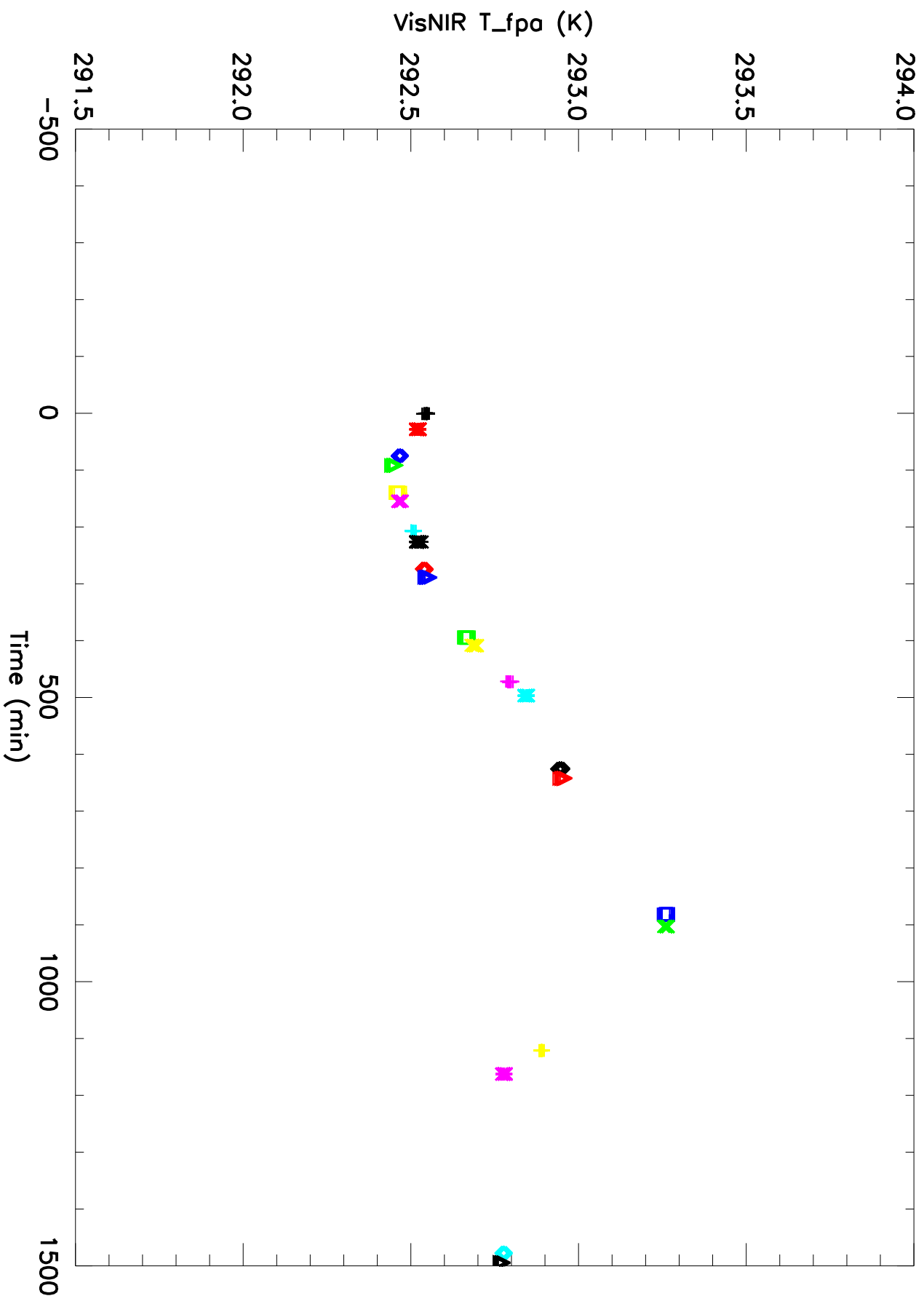
OBC thermistor standard deviation vs Time

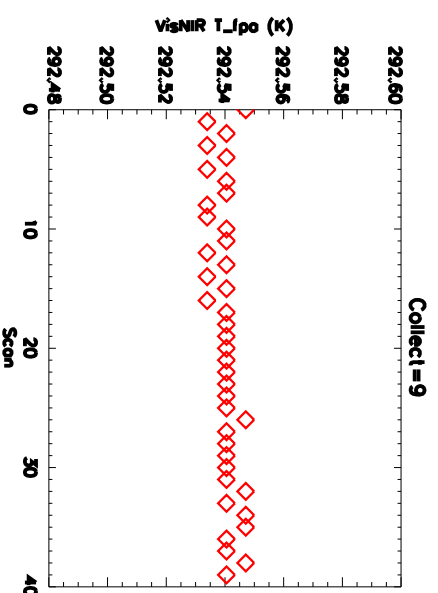
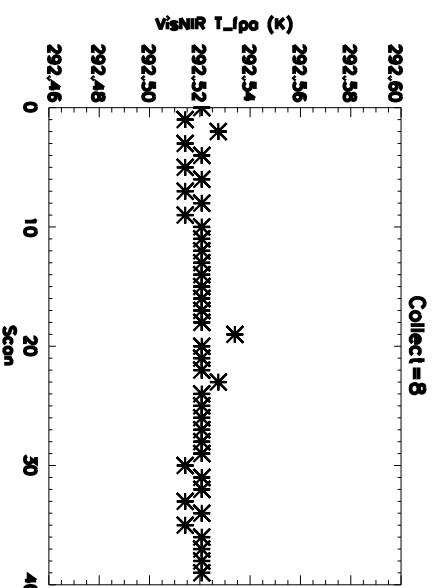
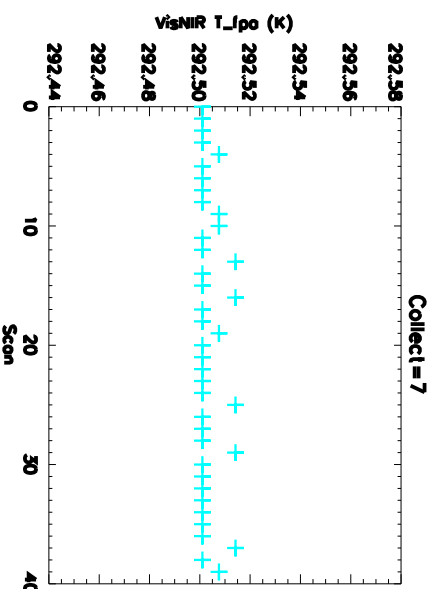
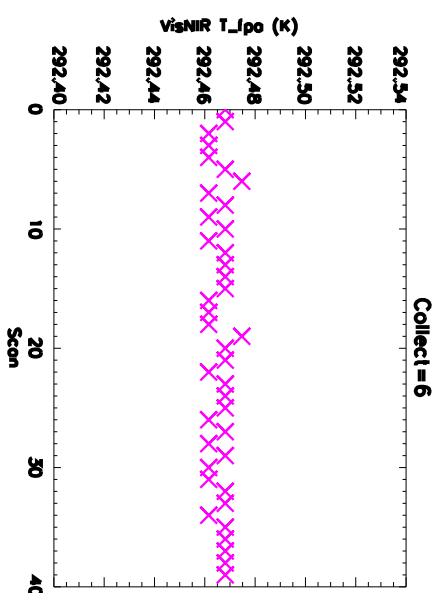
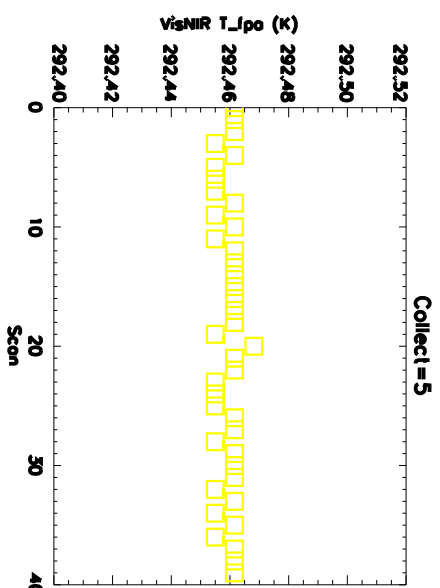
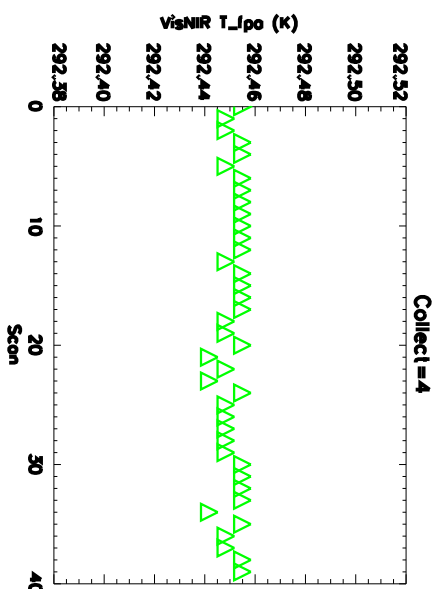
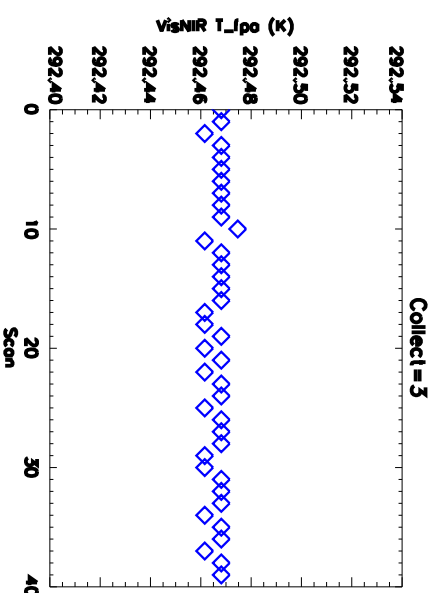
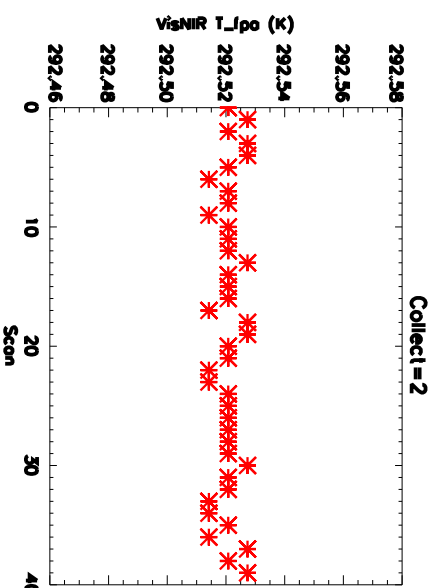
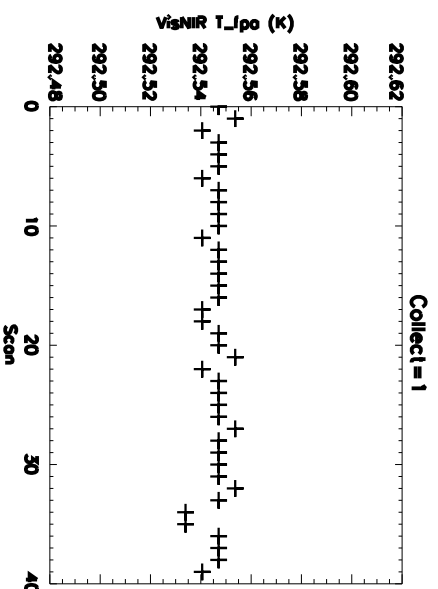


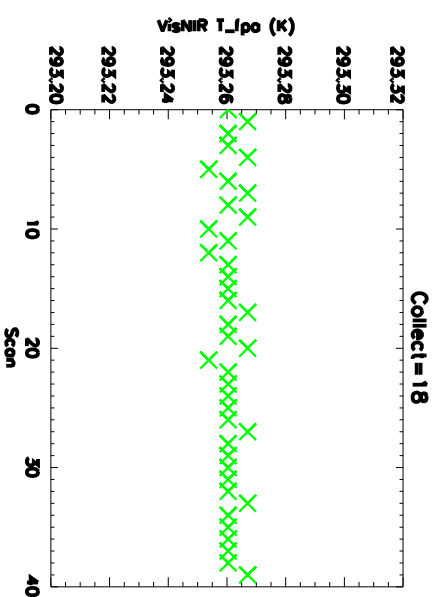
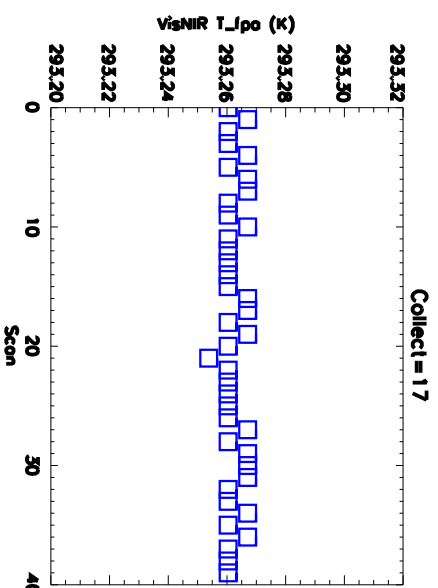
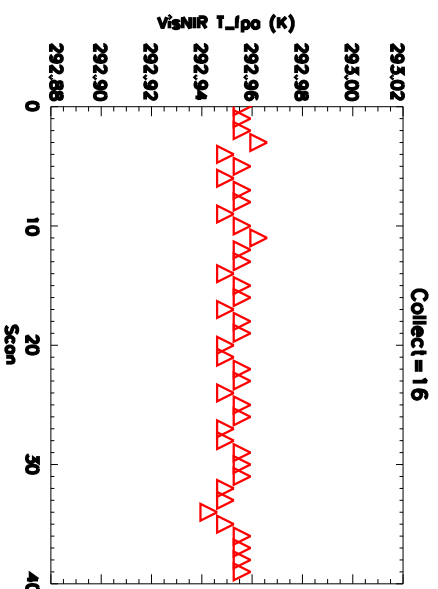
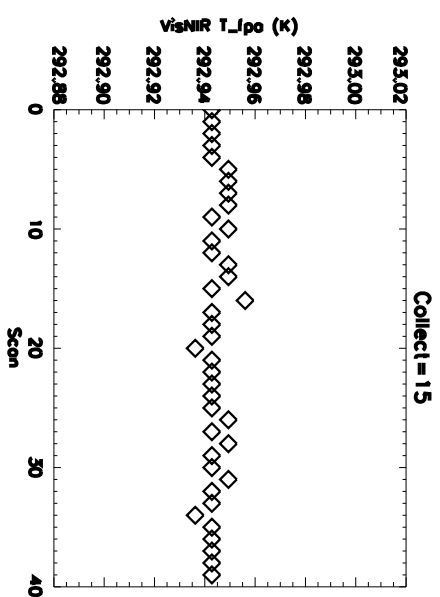
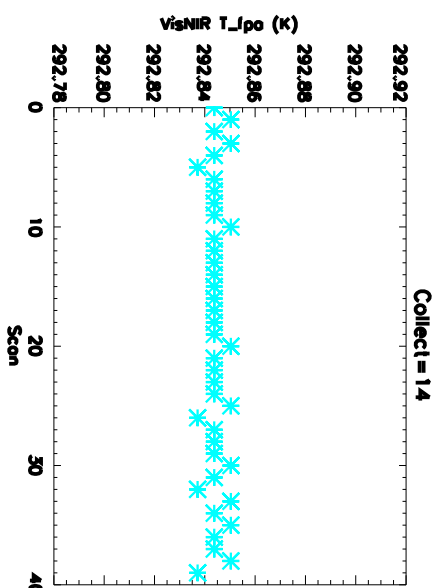
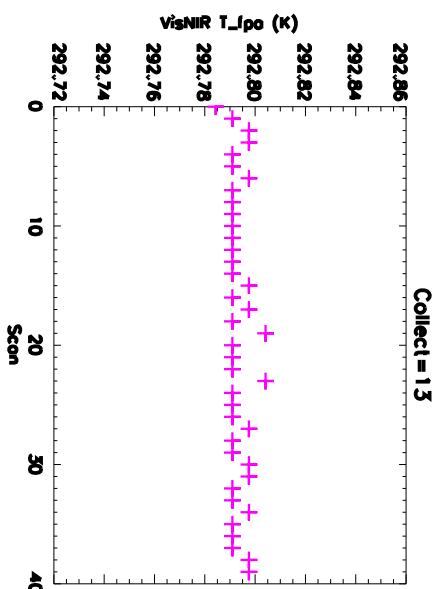
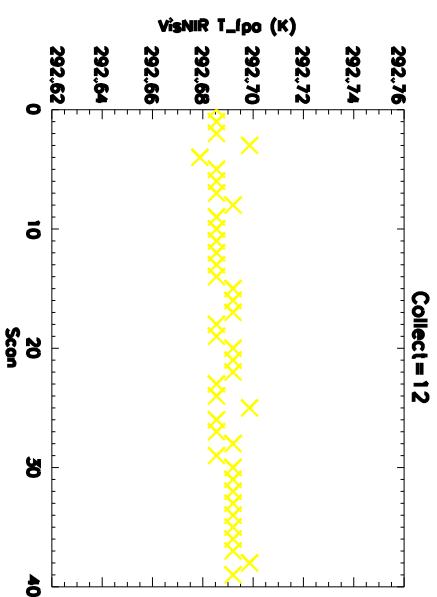
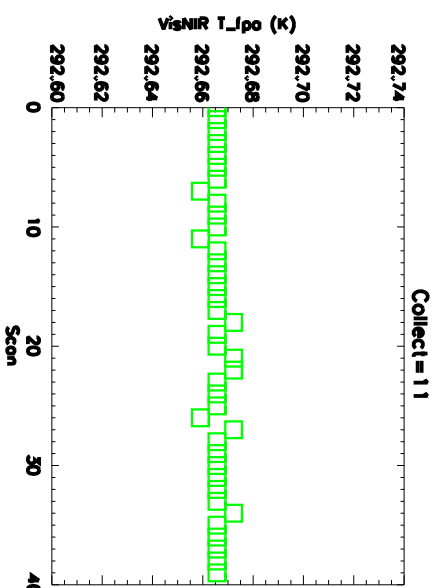
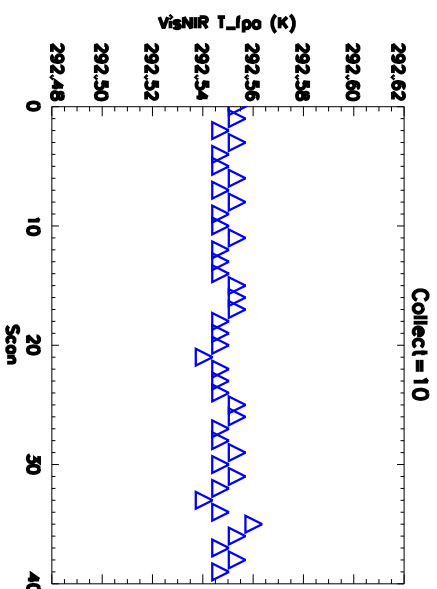
OBC thermistor standard deviation vs Collect



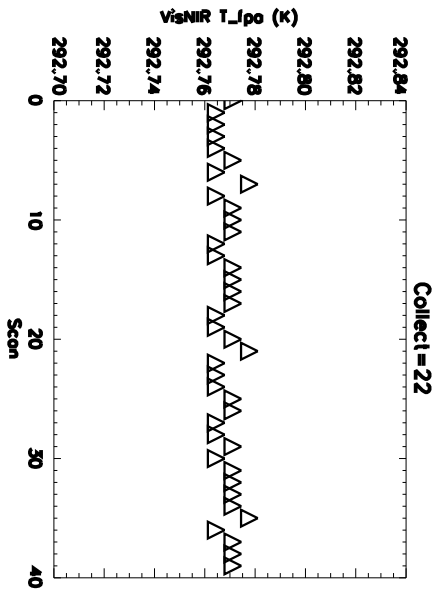
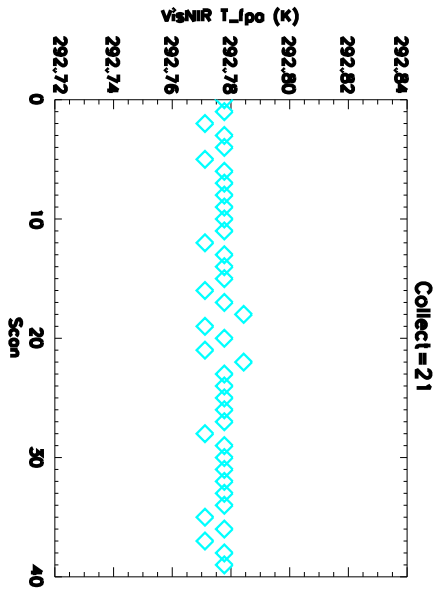
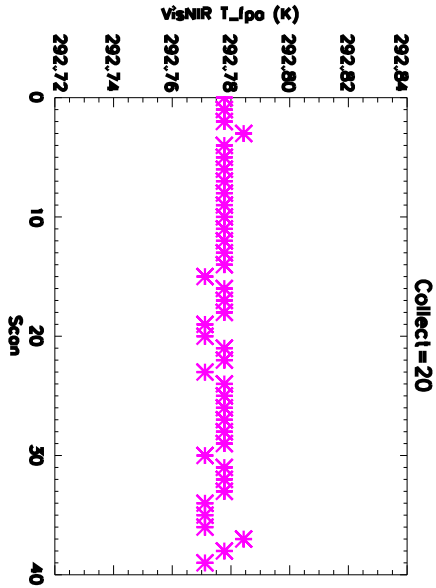
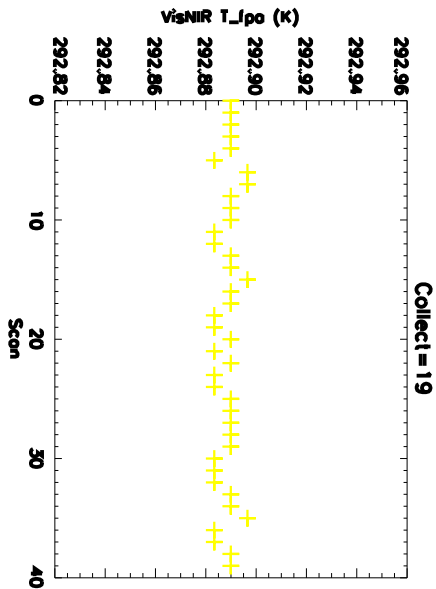
VisNIR T_{fpa} vs Time



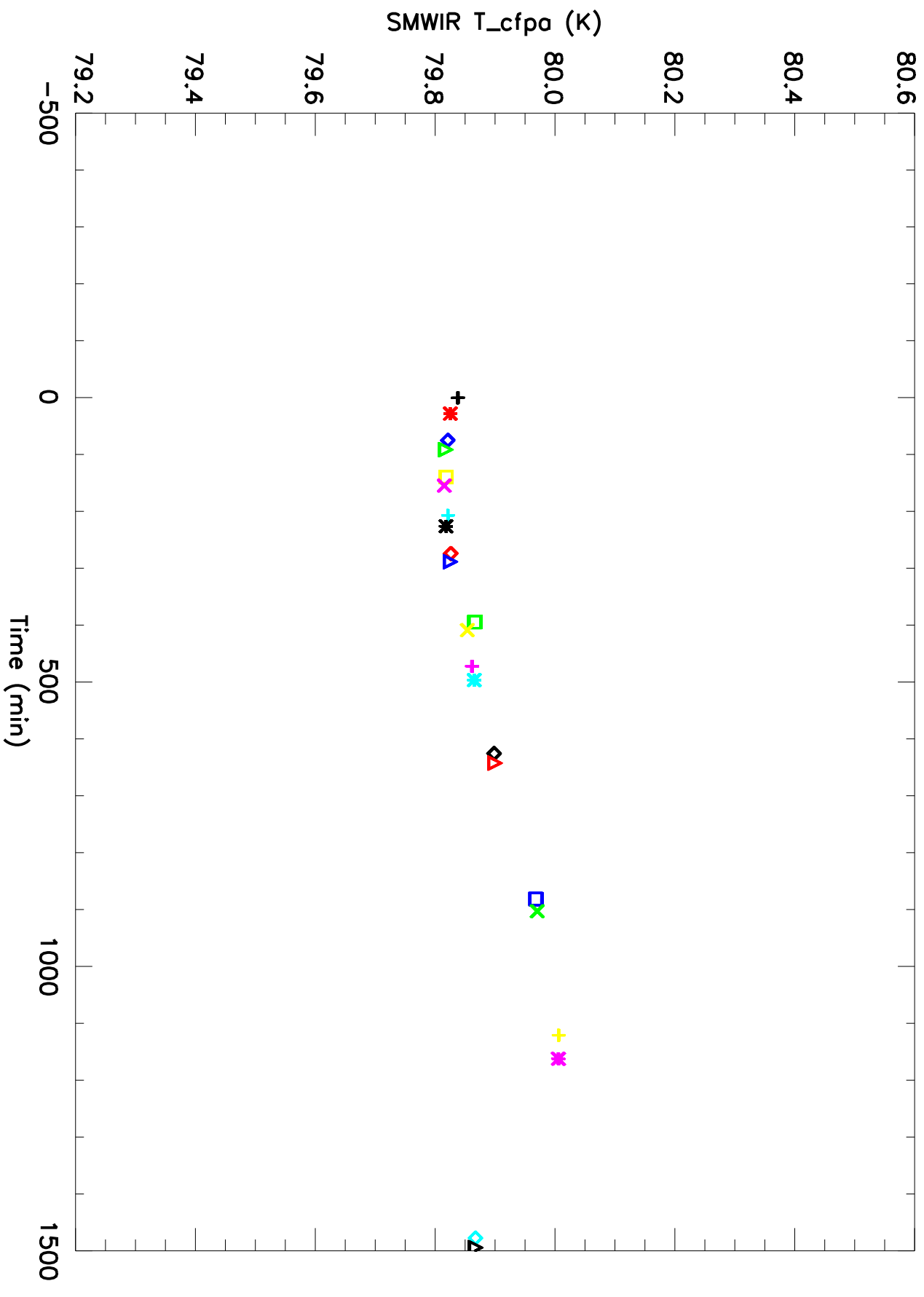


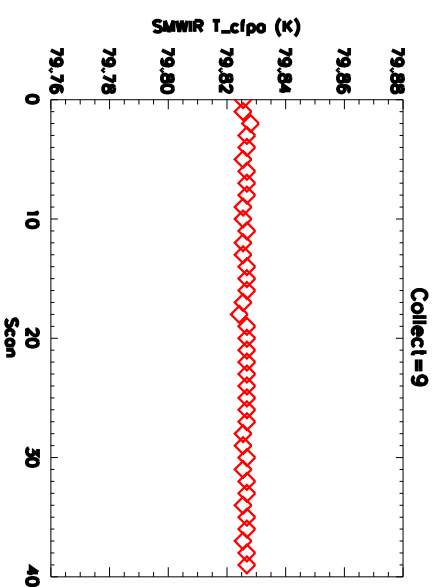
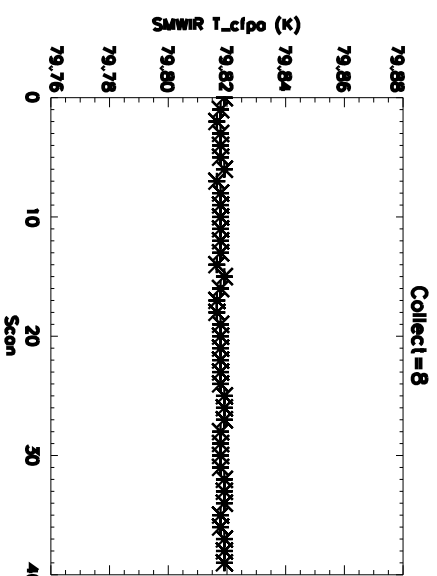
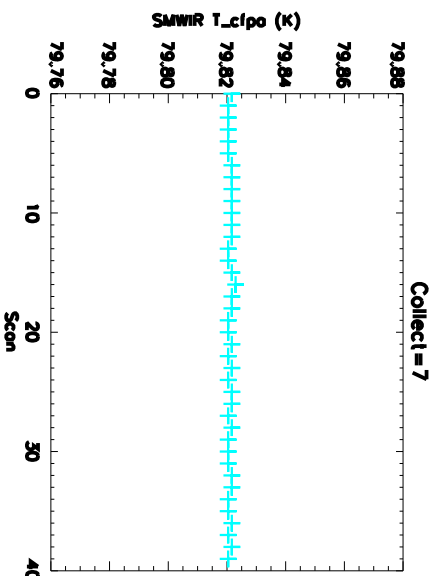
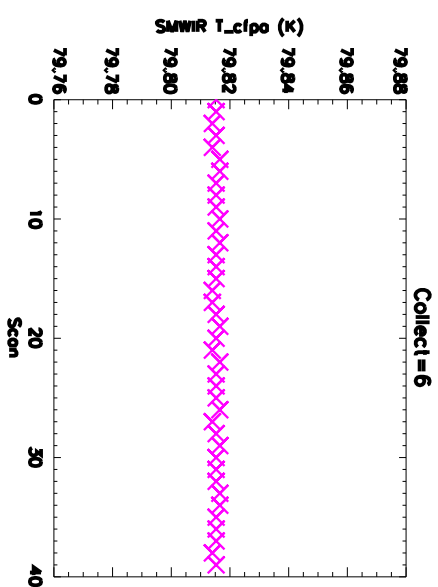
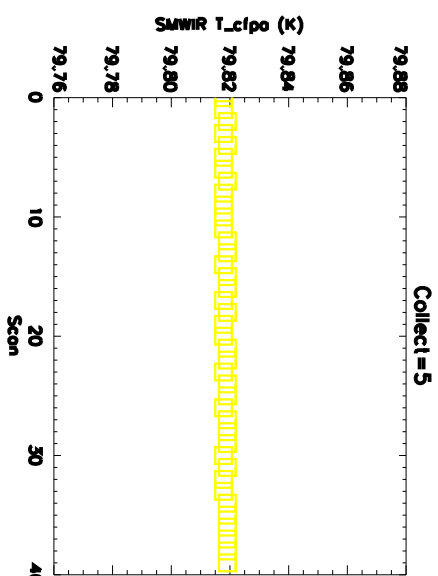
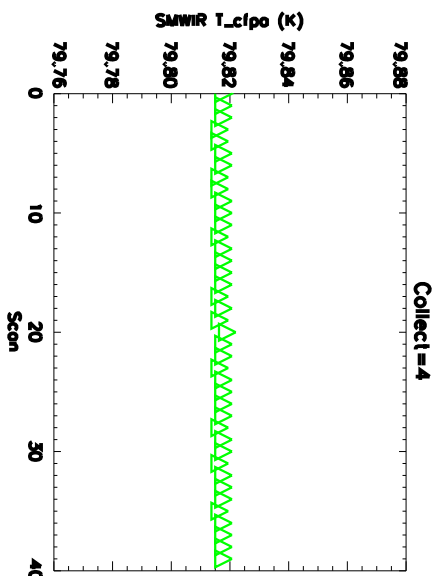
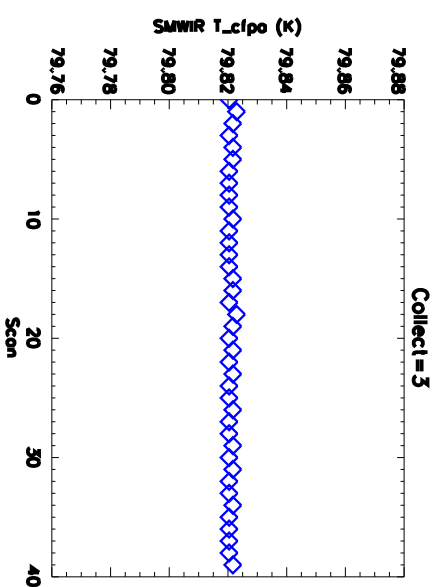
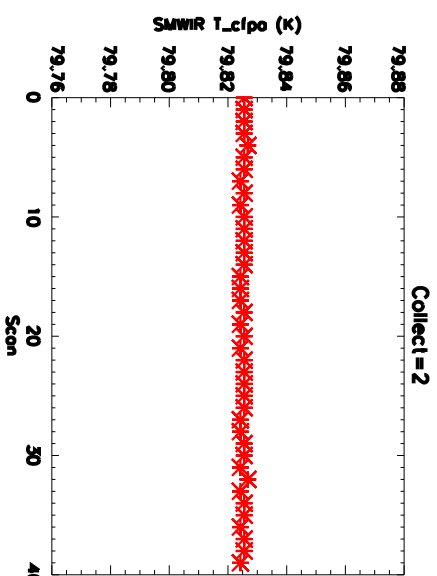
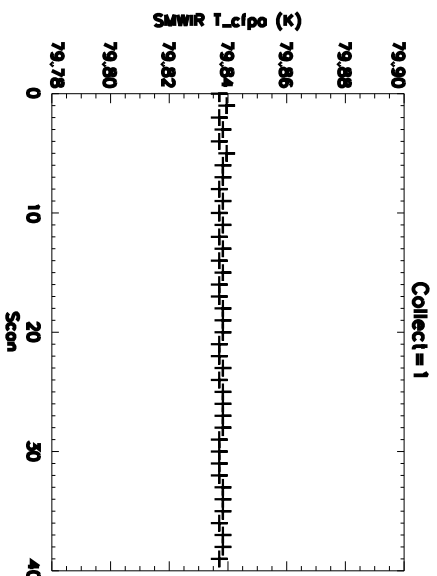


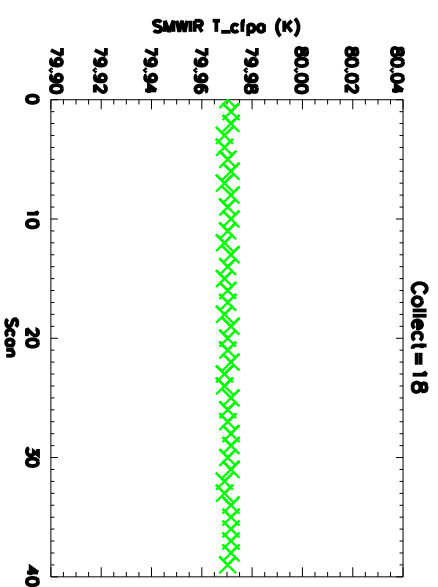
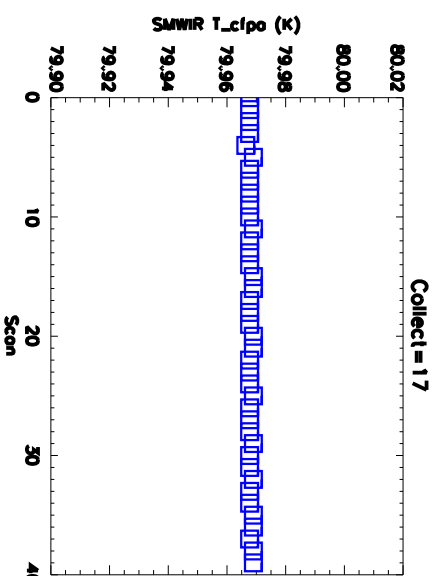
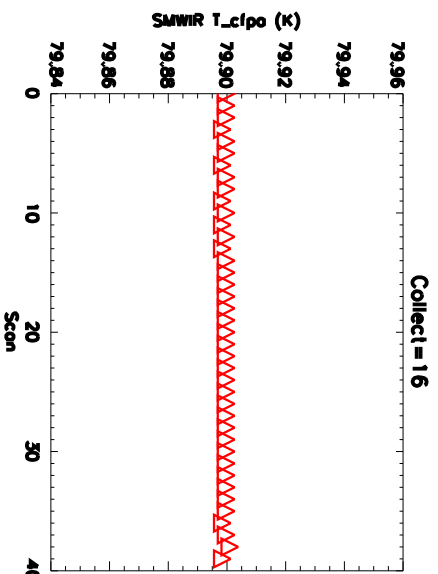
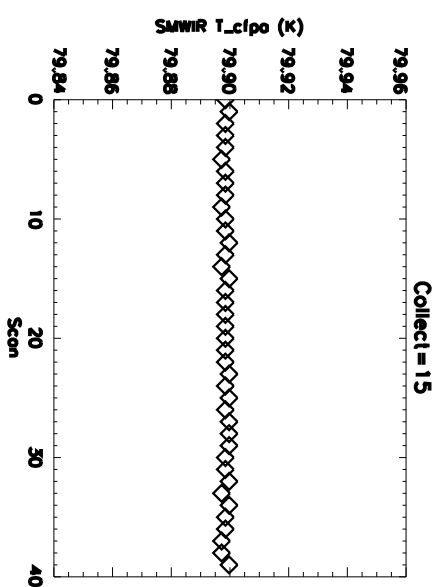
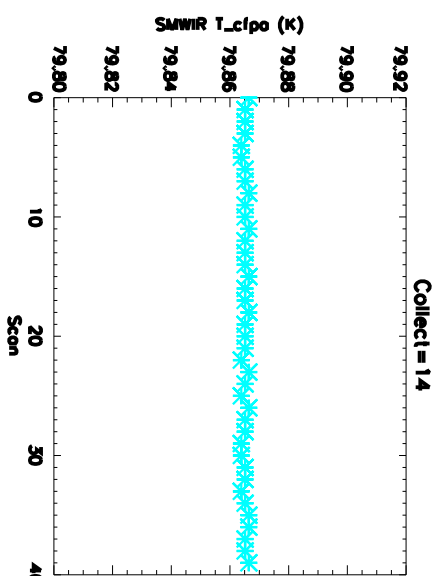
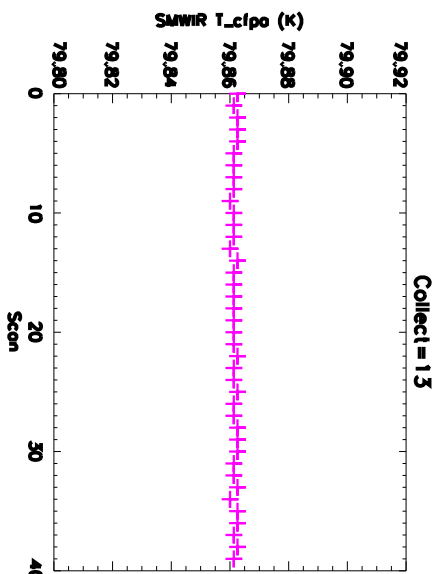
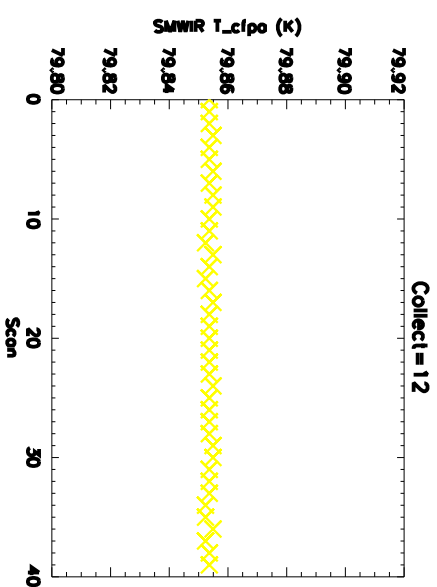
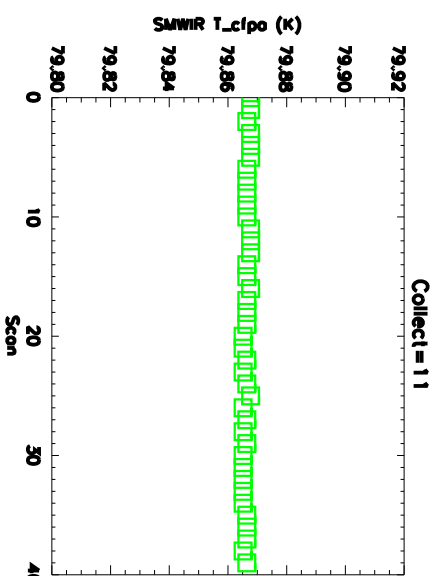
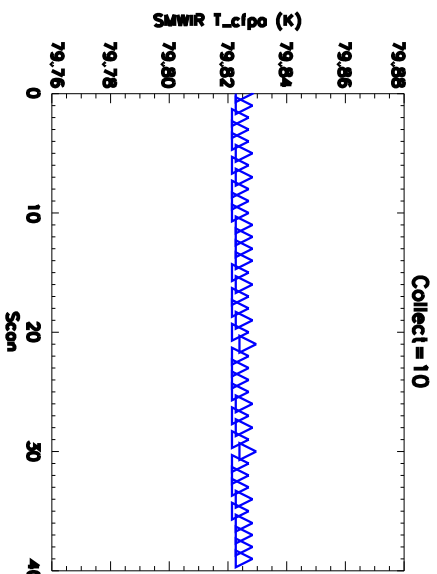
VisNIR T_fpo vs Scan



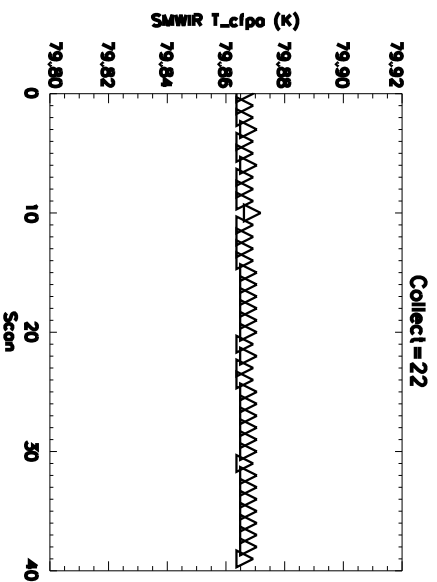
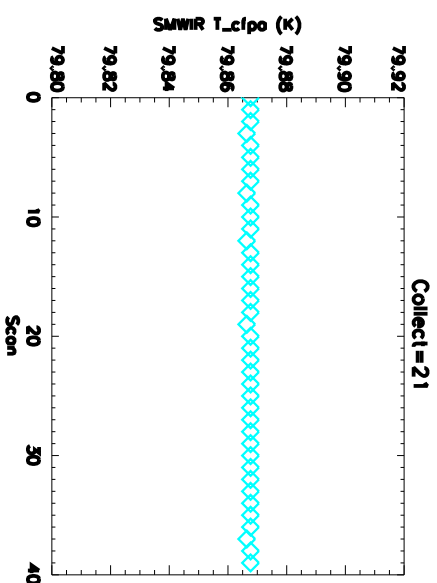
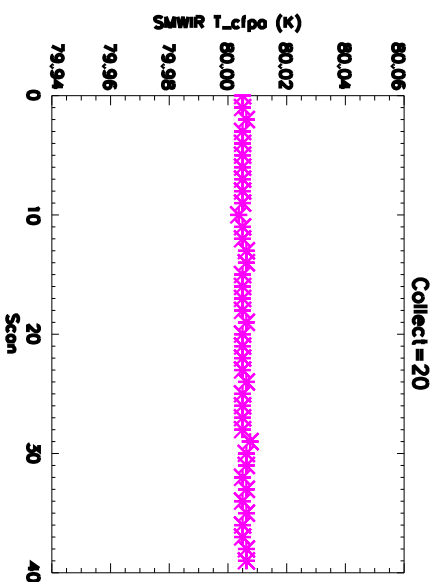
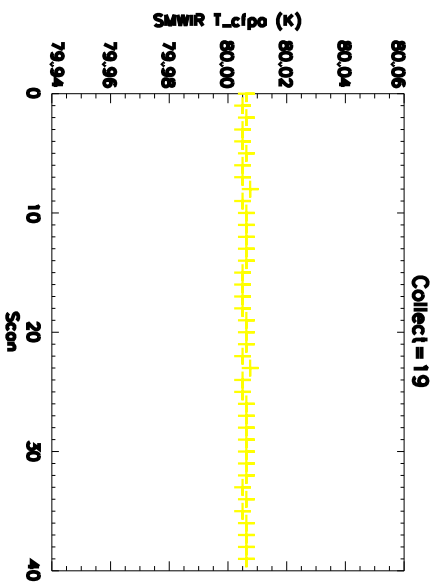
SMWIR T_{cfpa} vs Time



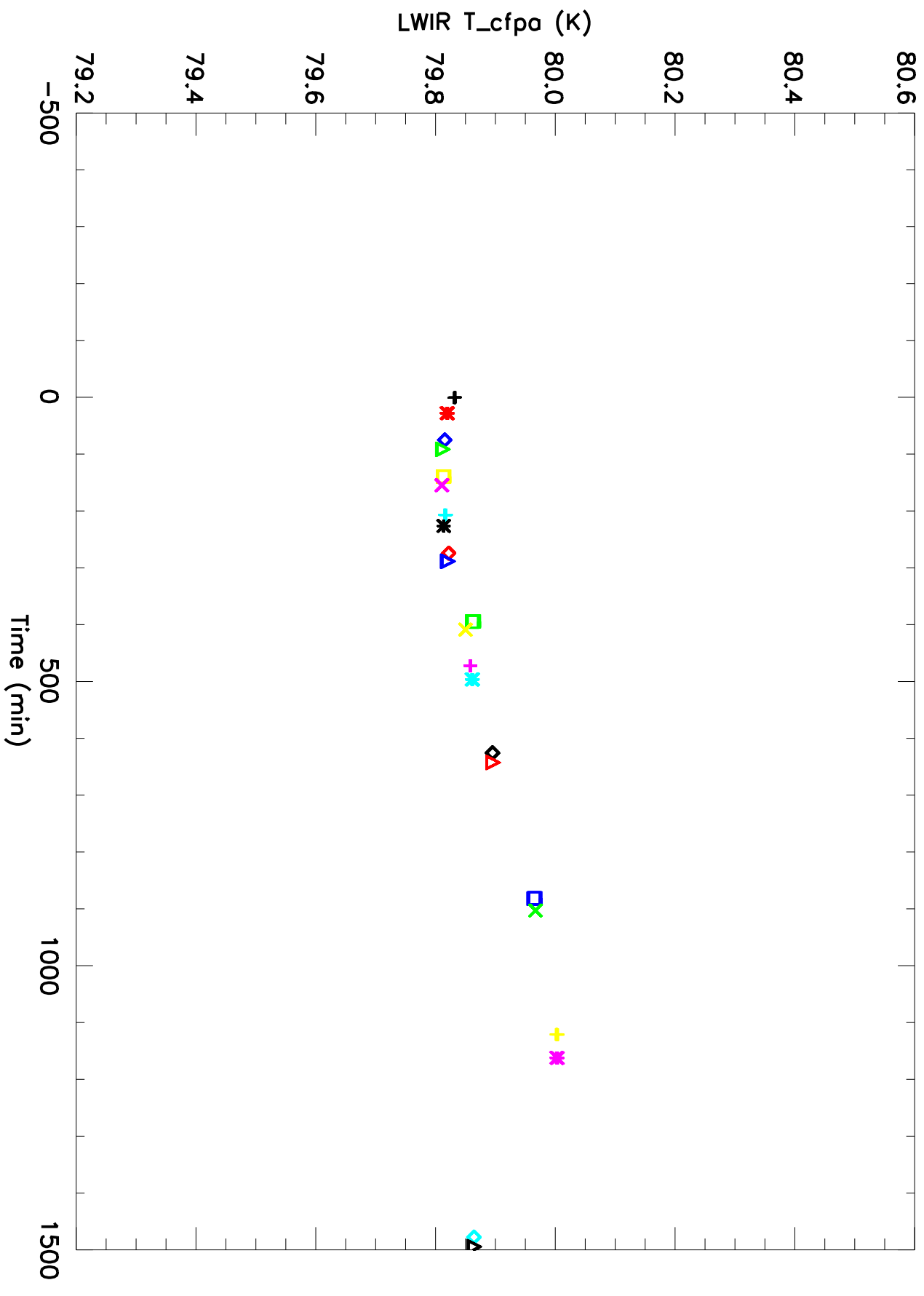


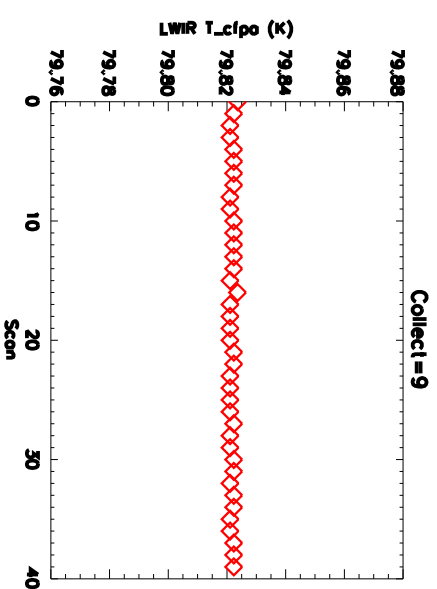
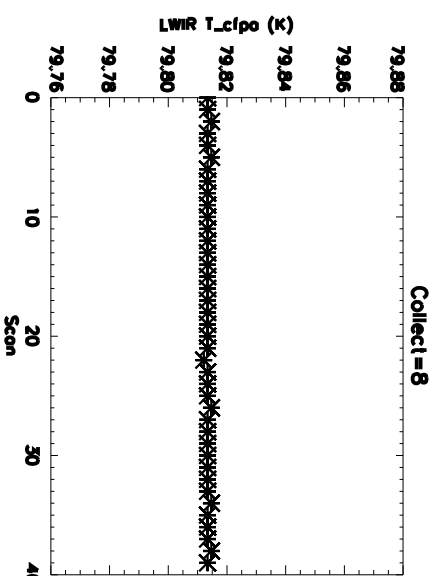
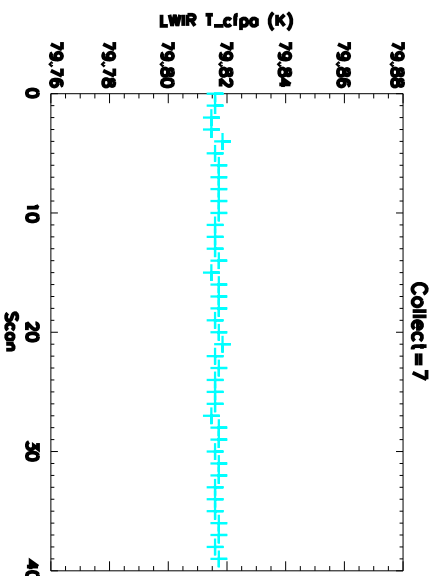
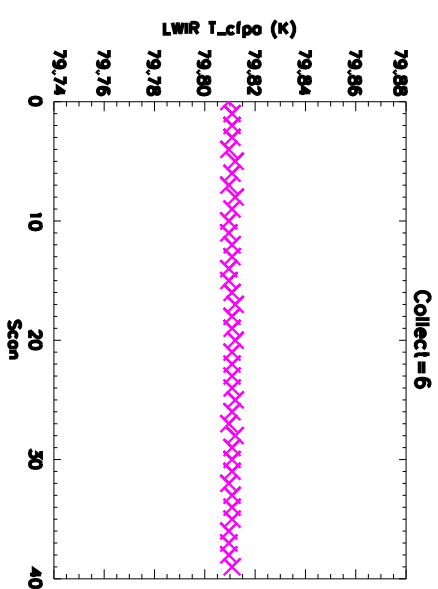
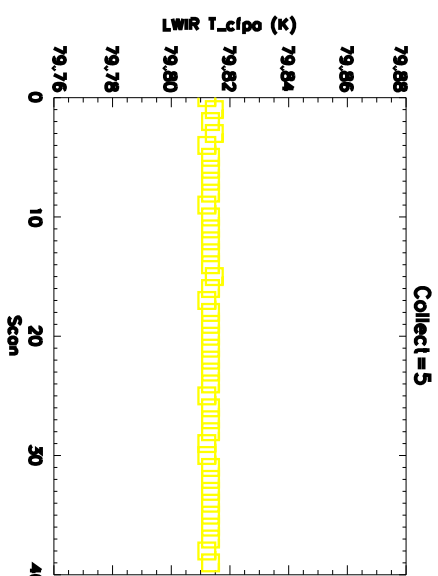
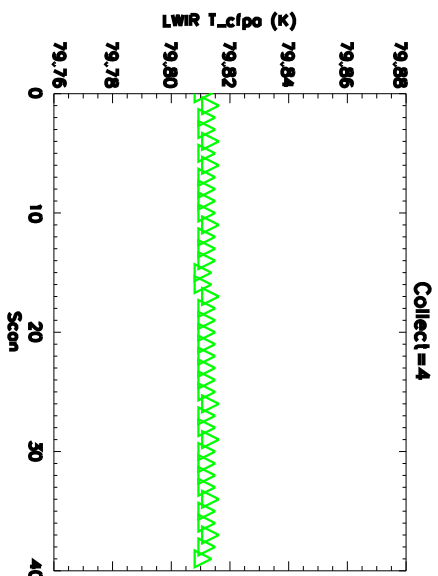
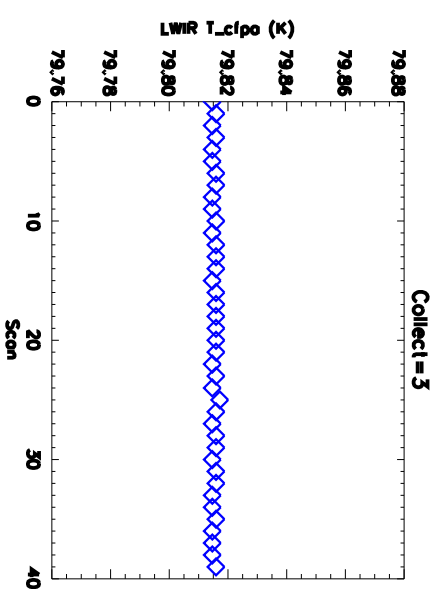
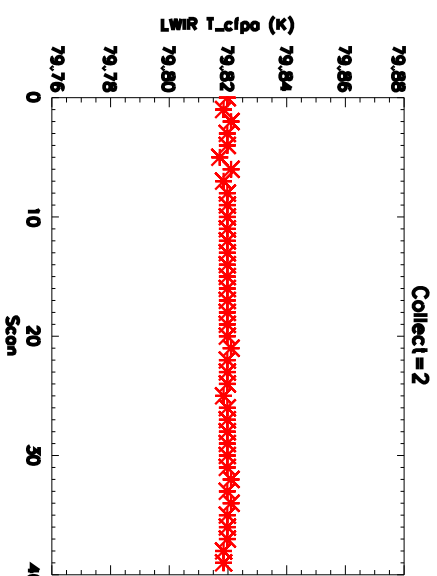
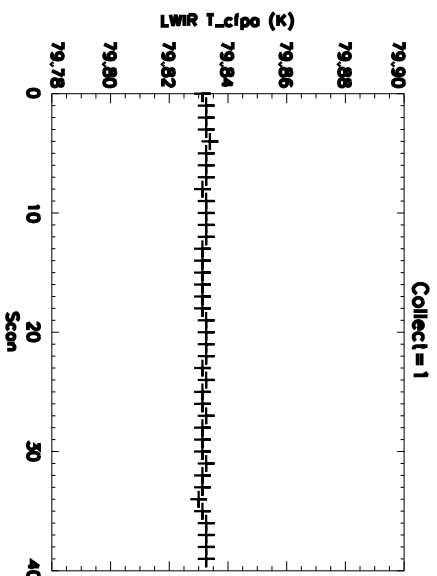


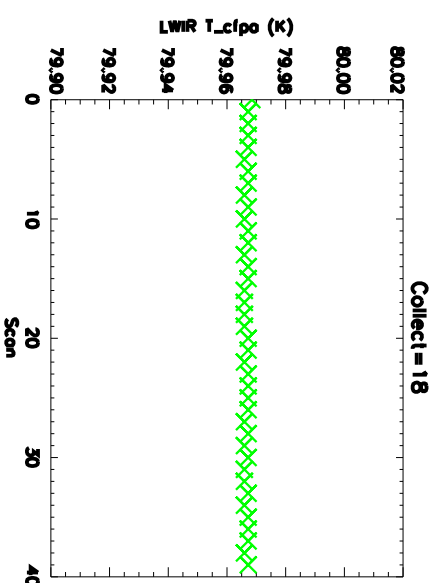
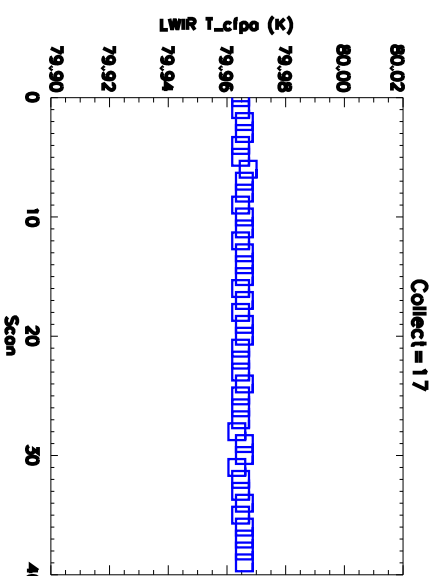
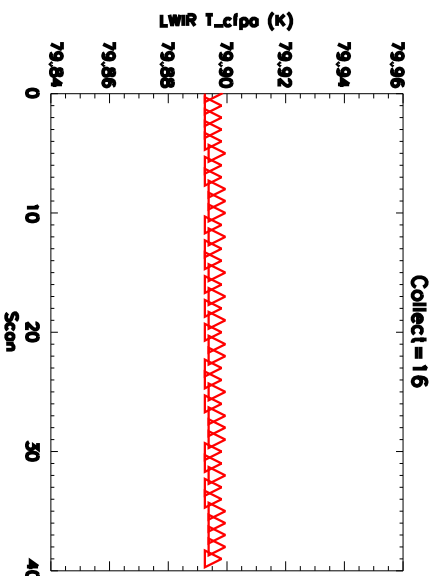
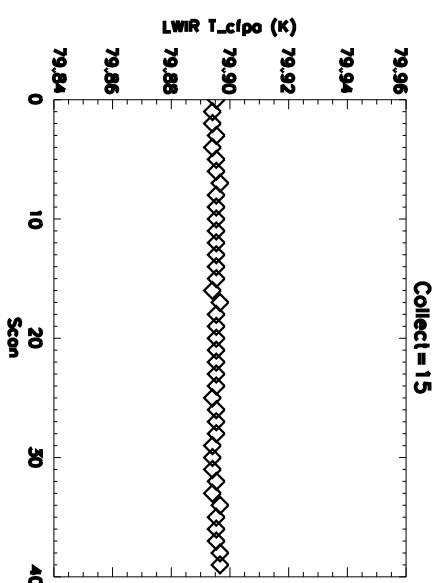
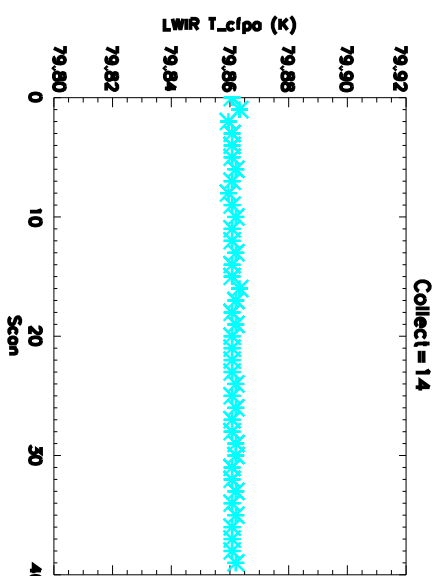
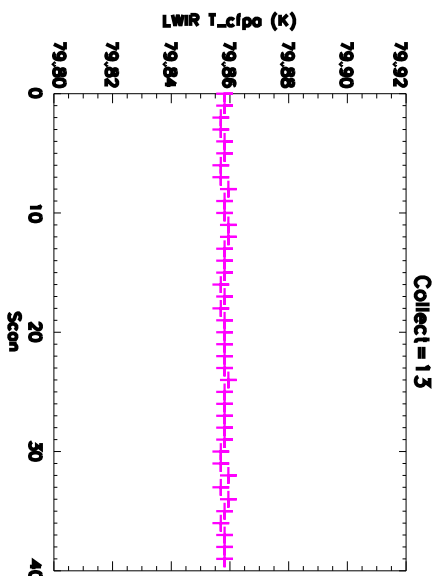
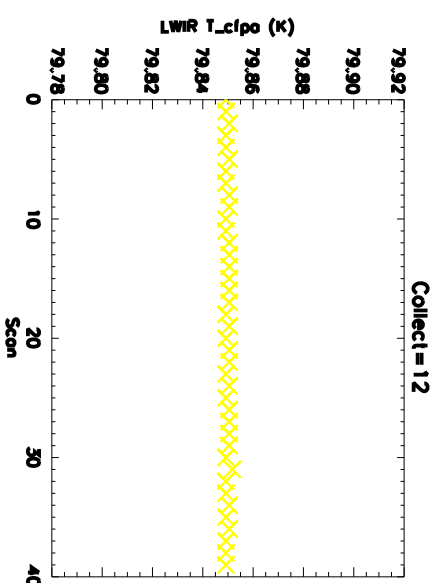
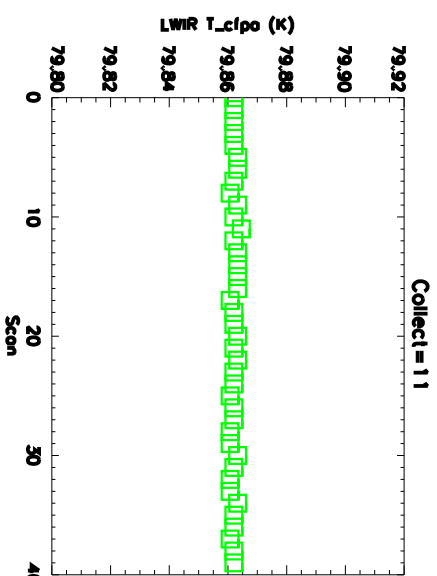
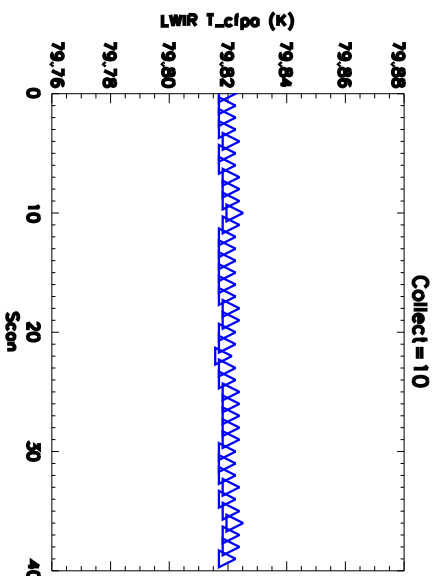
SMWIR T_cfpo vs Scan



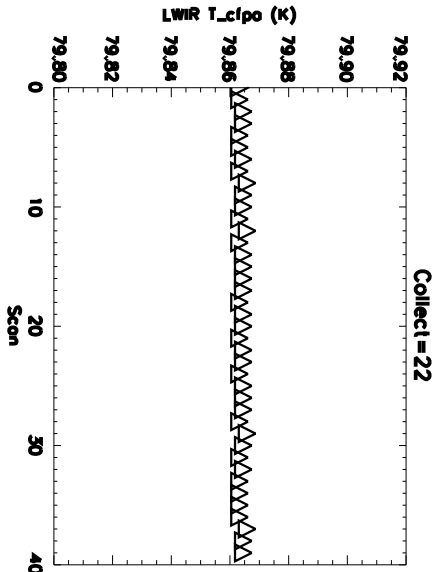
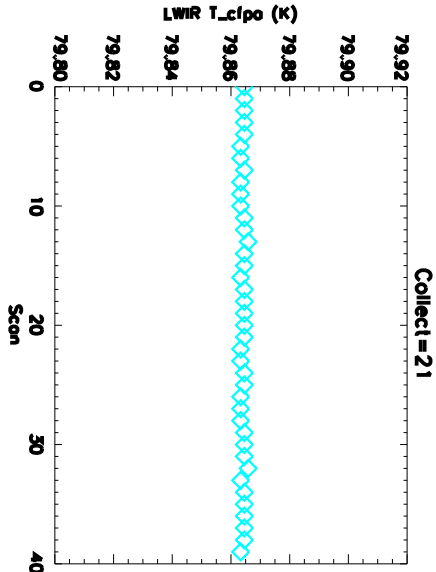
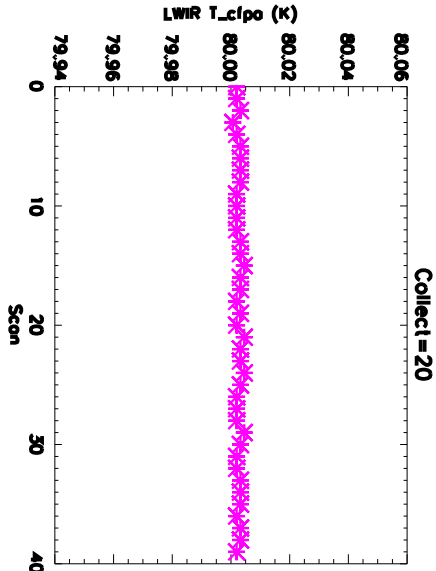
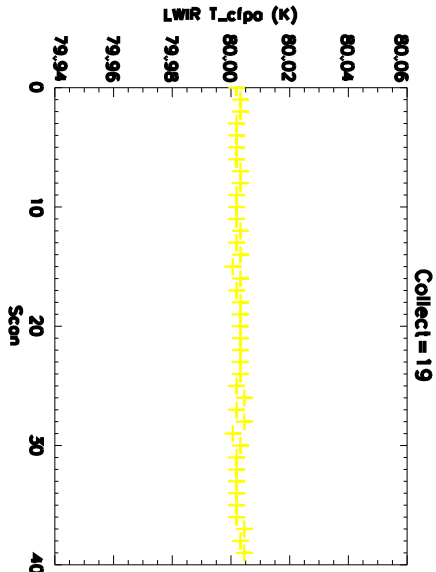
LWIR T_{cfpa} vs Time





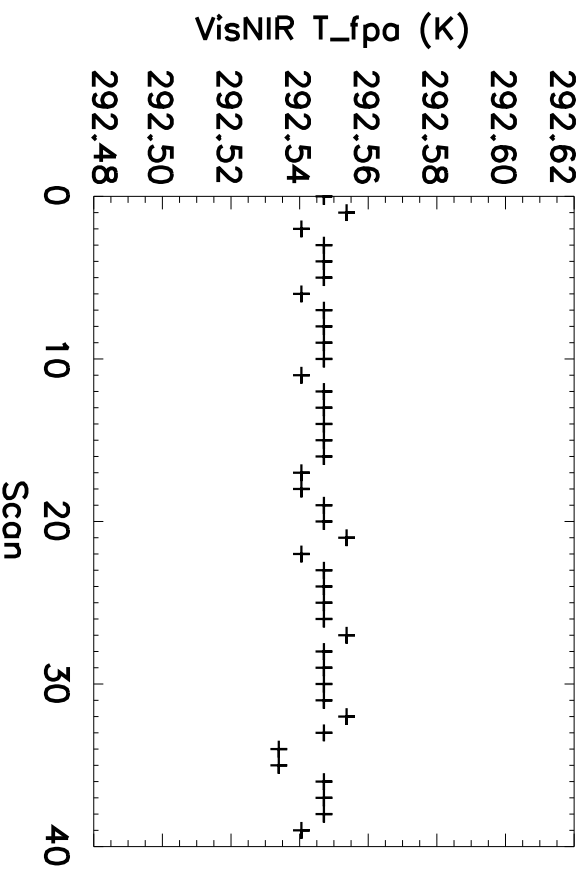


LWIR T_cfpd vs Scan

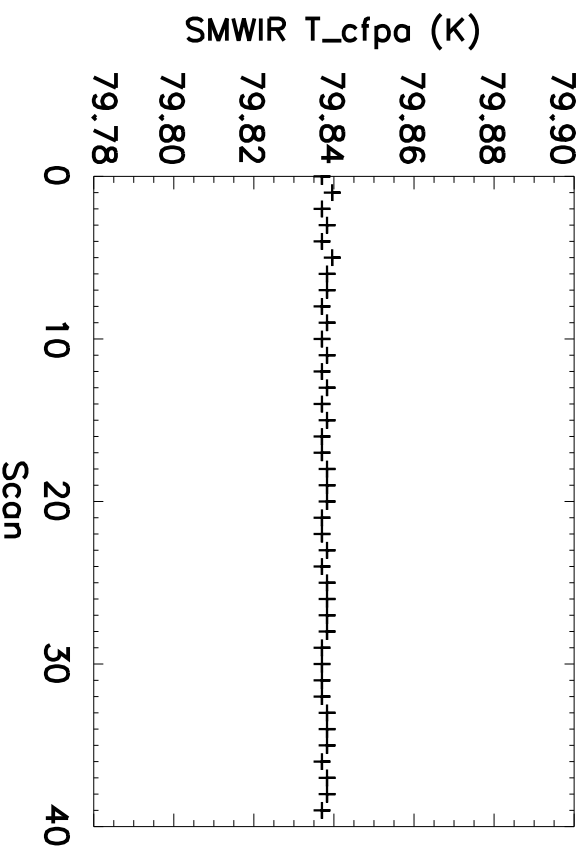


T_cfpd vs Scan: Collect=1

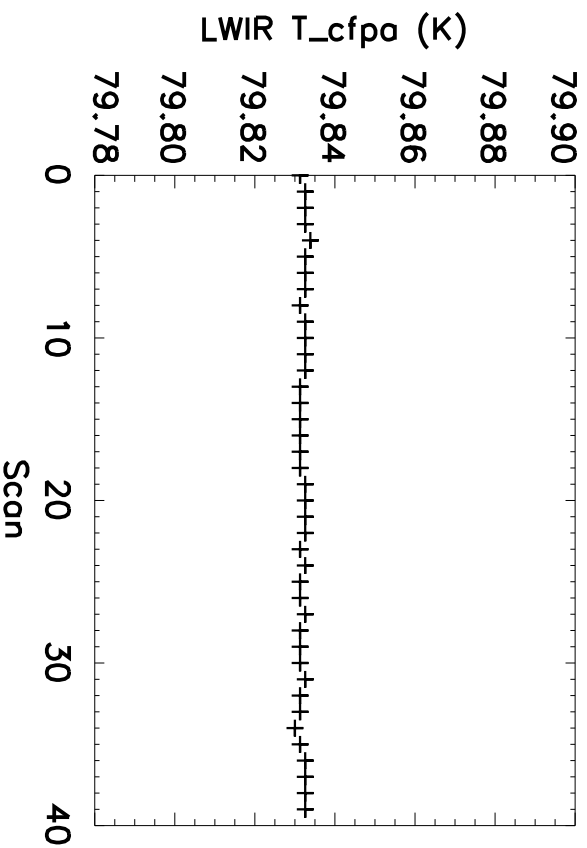
VisNIR



SMWIR

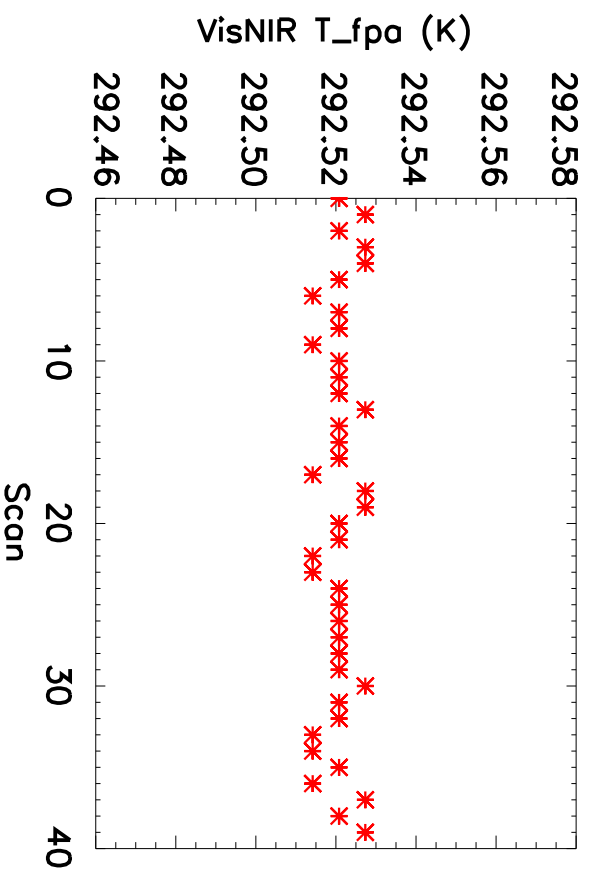


LWIR

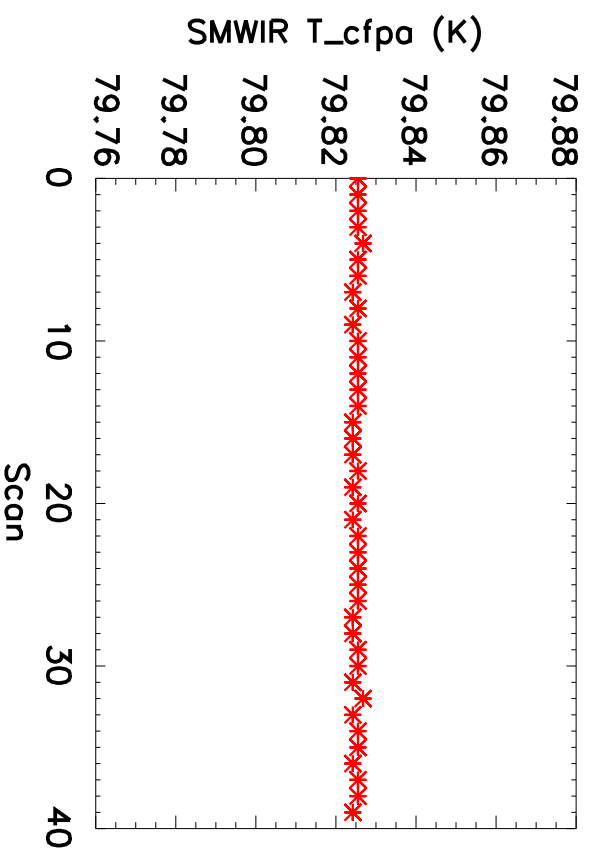


T_cfpd vs Scan: Collect=2

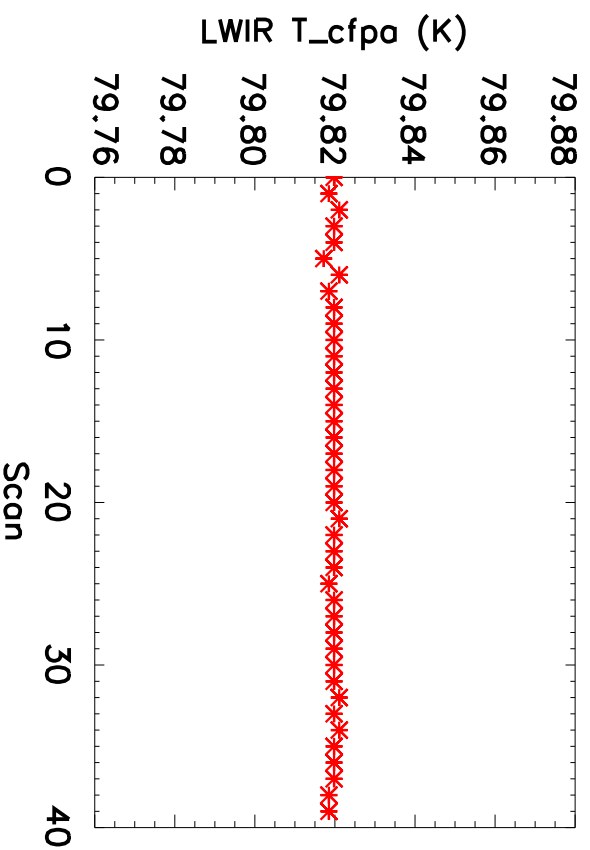
VisNIR



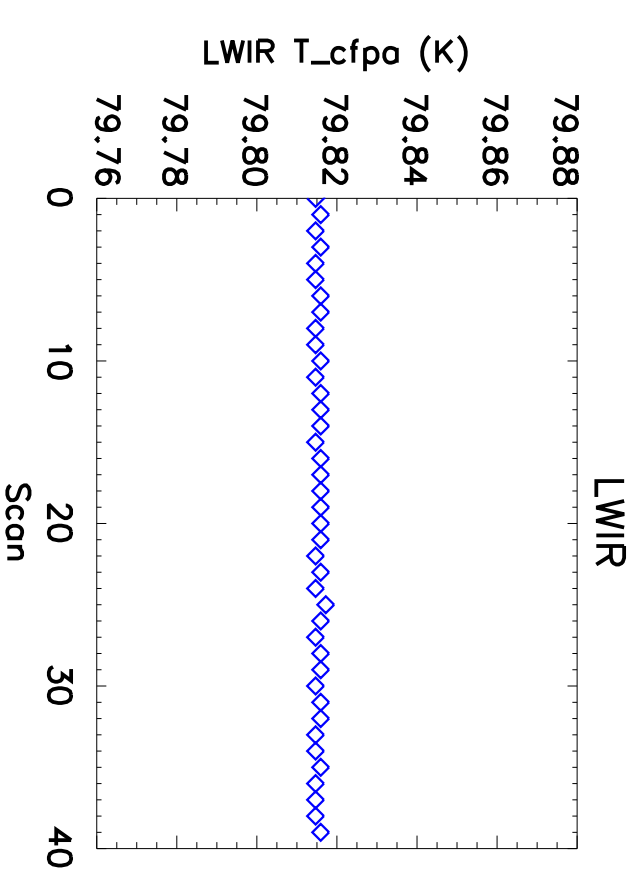
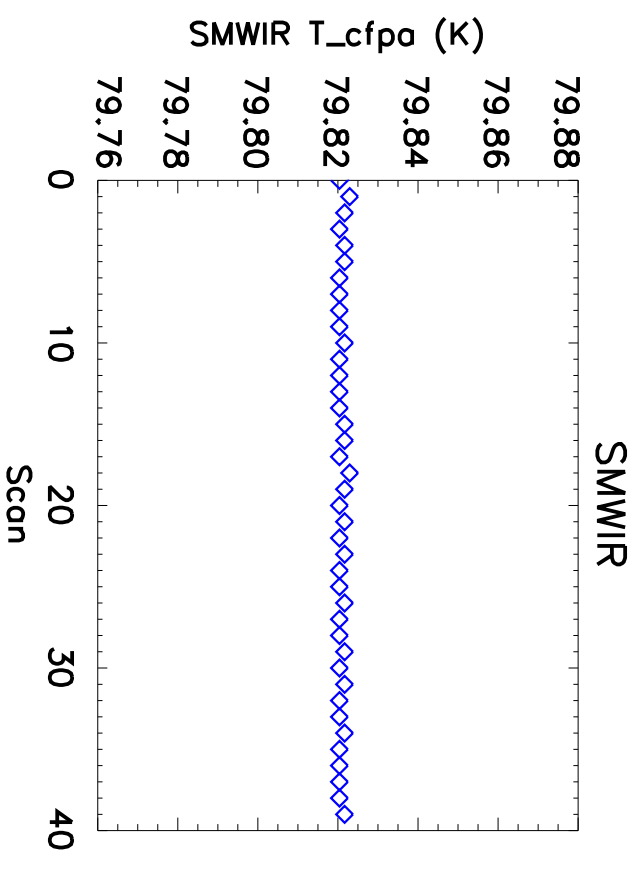
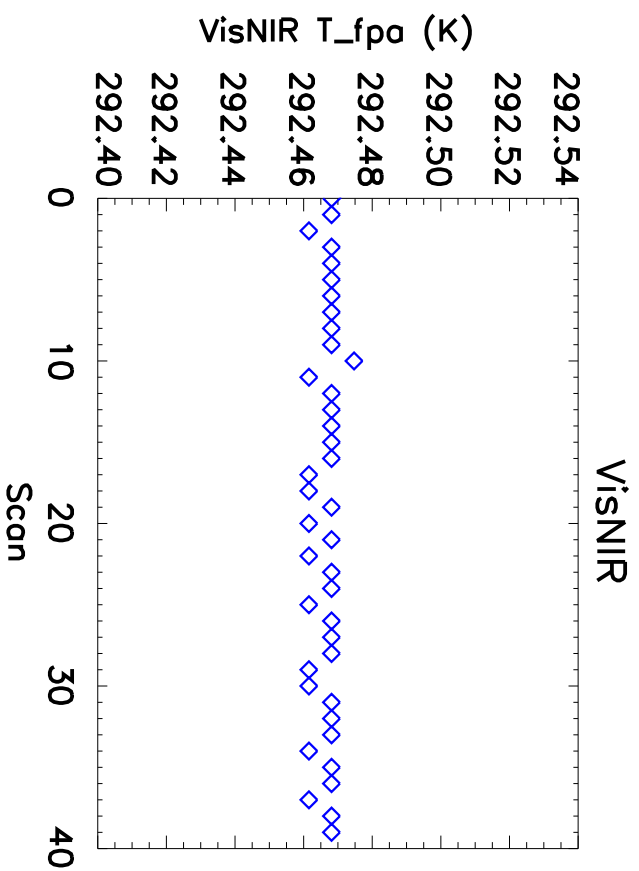
SMWIR



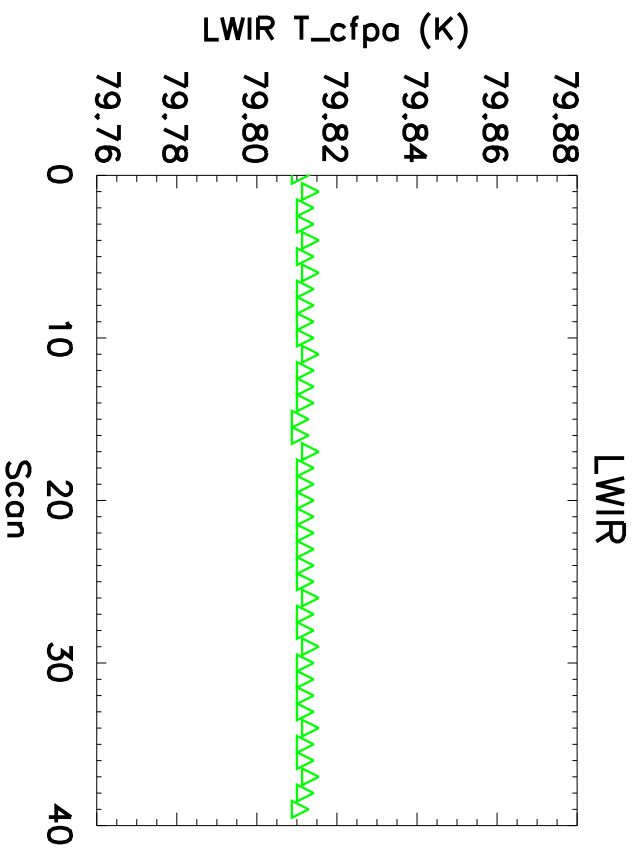
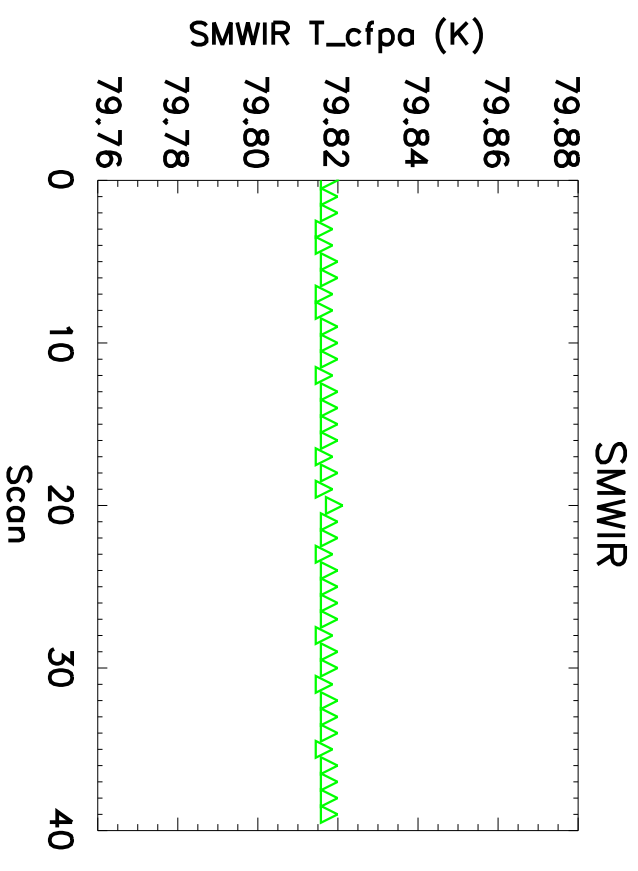
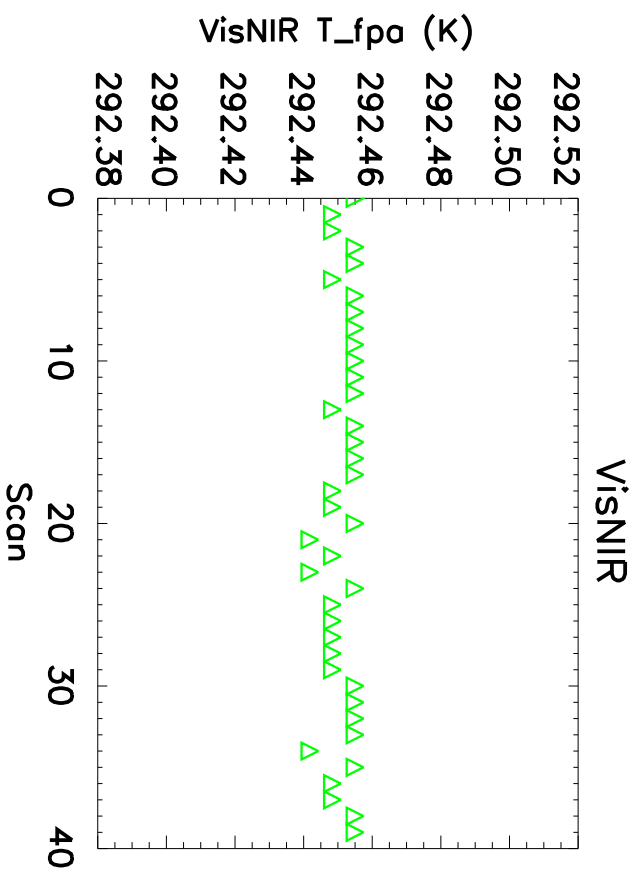
LWIR



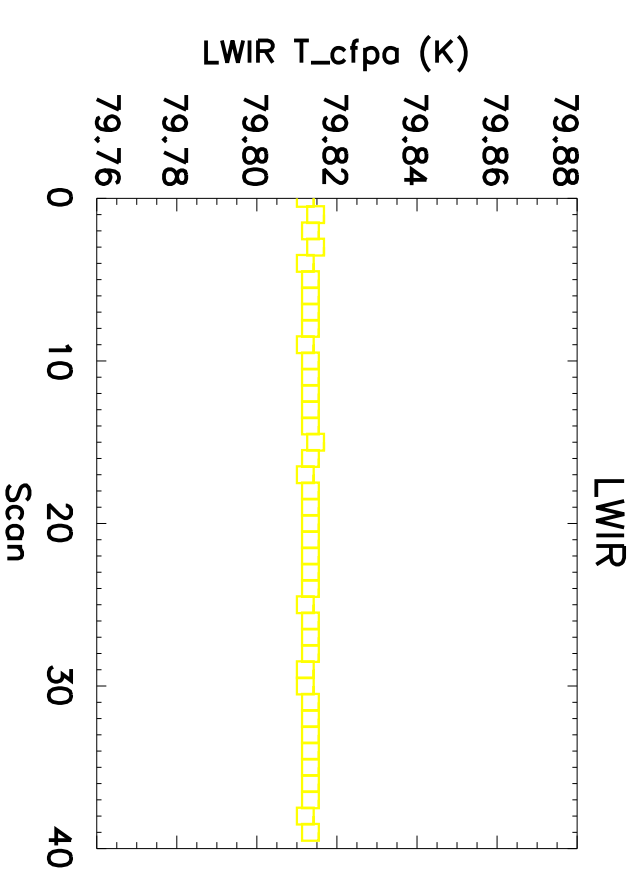
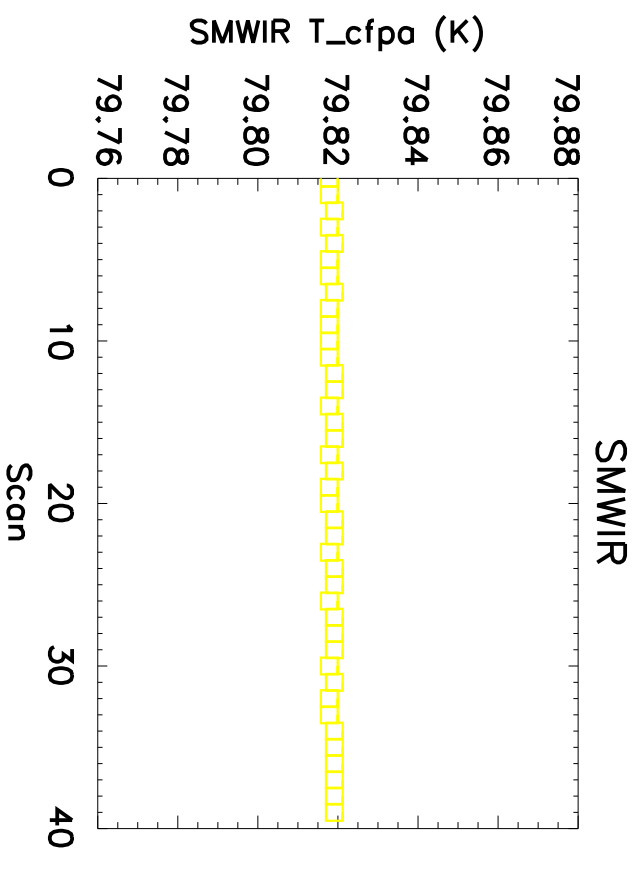
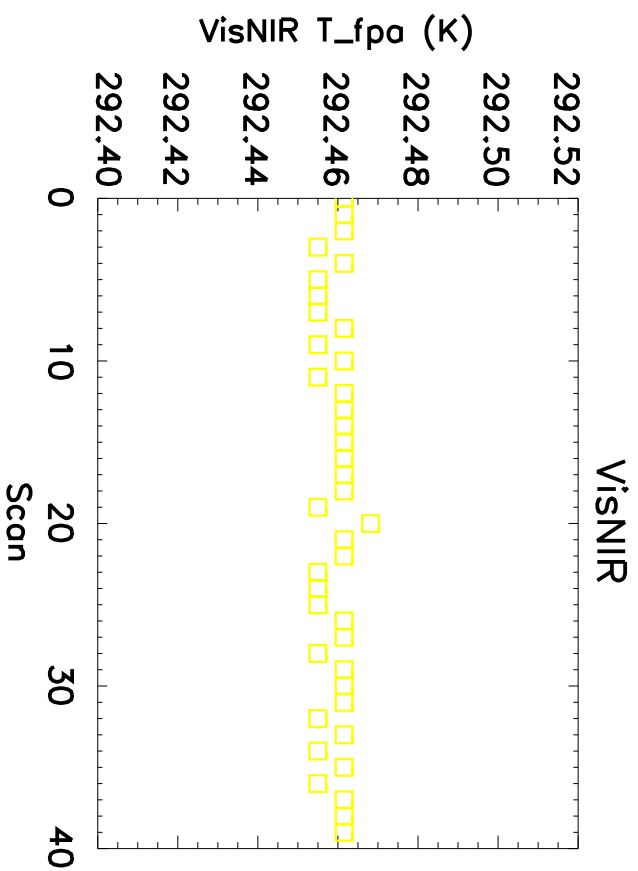
T_cfpα vs Scan: Collect=3



T_cfpα vs Scan: Collect=4

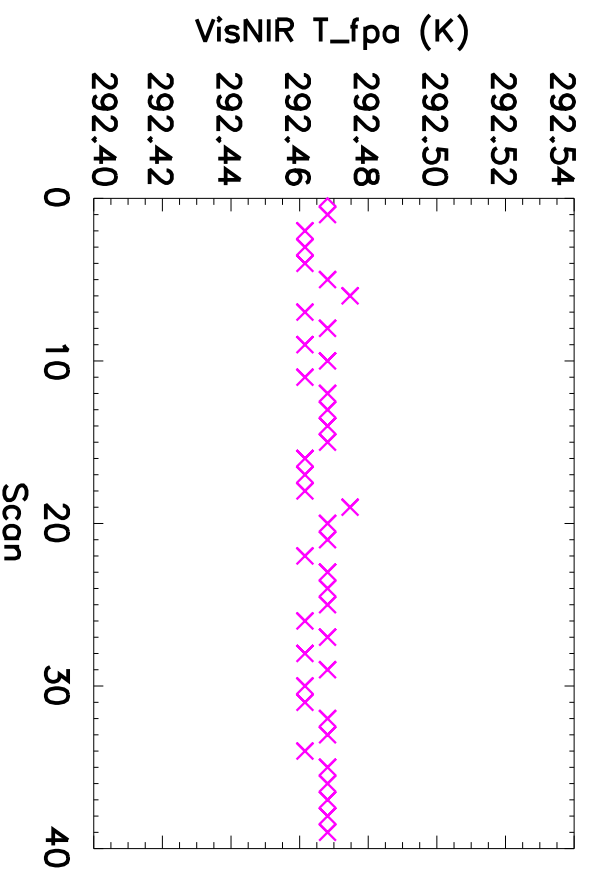


T_cfpα vs Scan: Collect=5

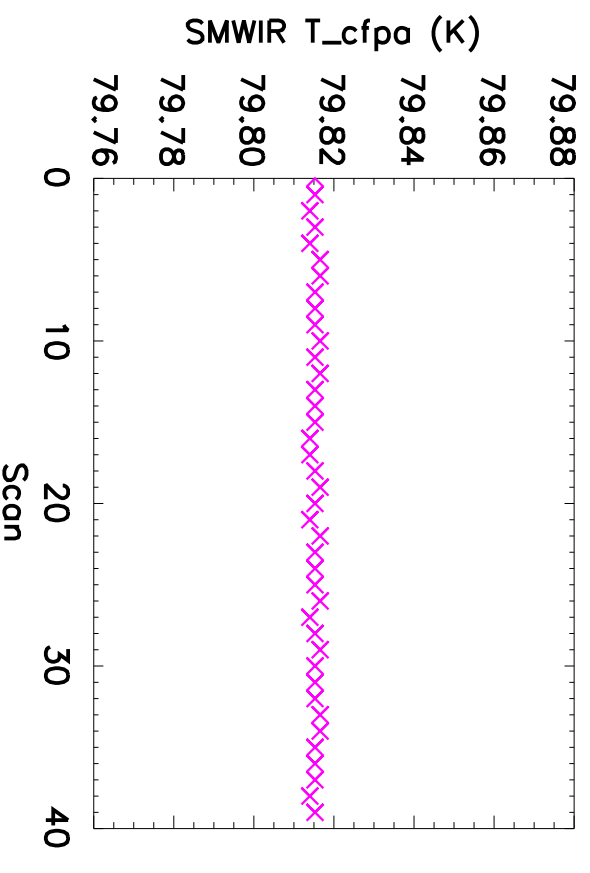


T_cfpd vs Scan: Collect=6

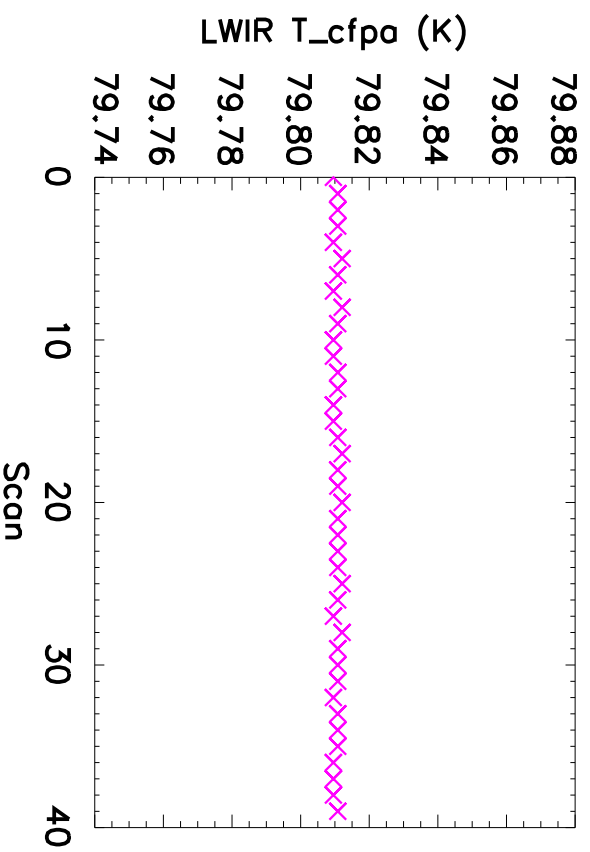
VisNIR



SMWIR

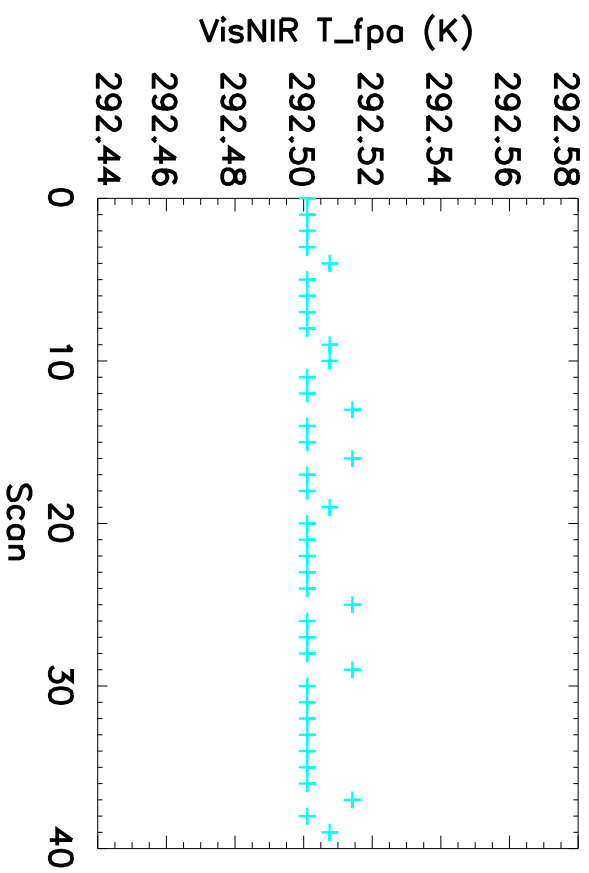


LWIR

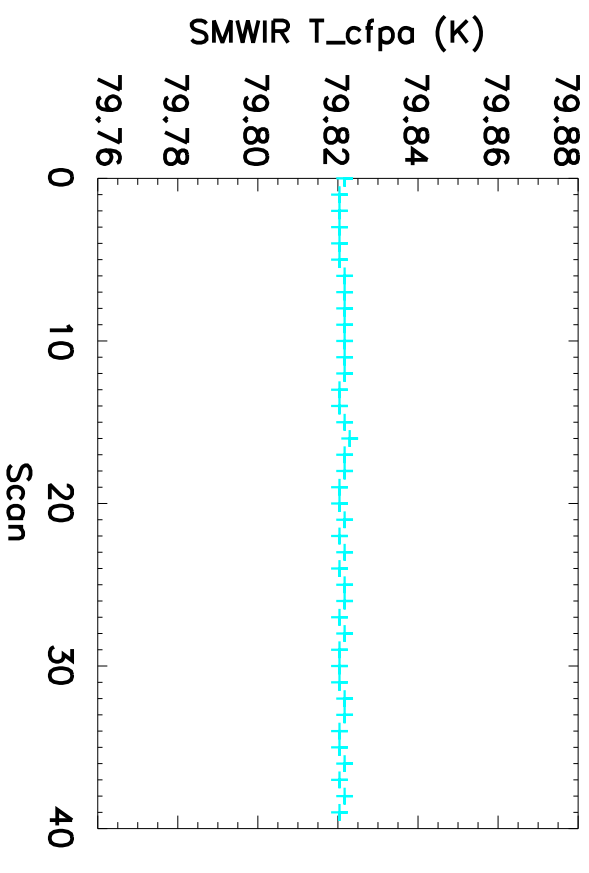


T_cfpα vs Scan: Collect=7

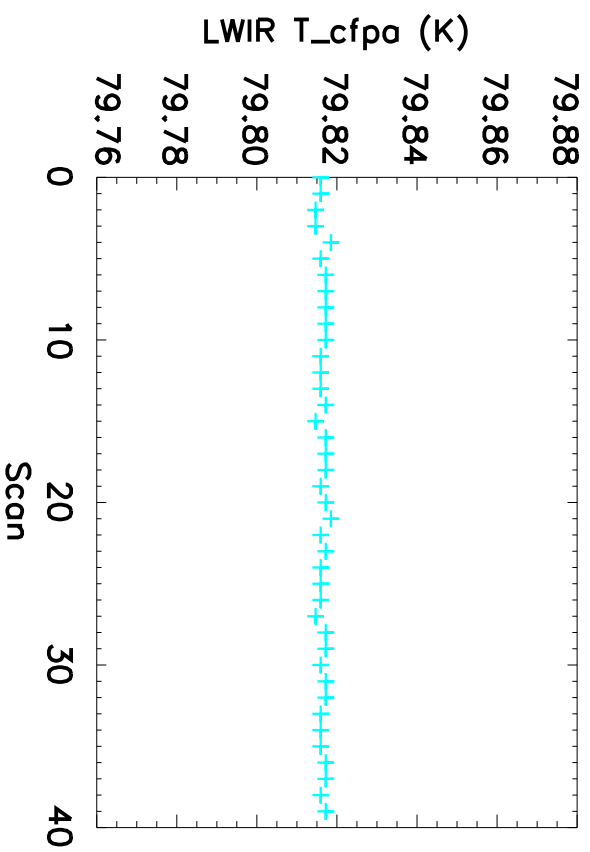
VisNIR



SMWIR

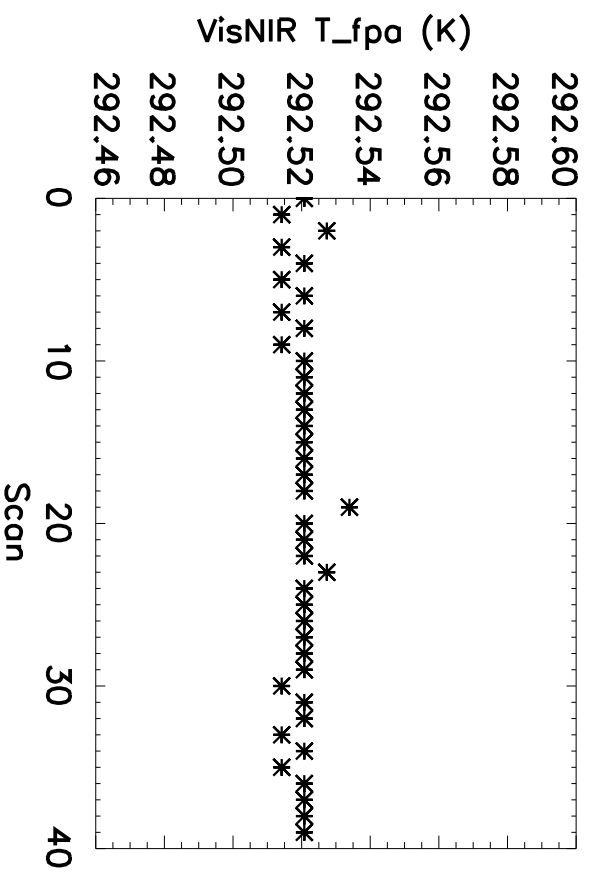


LWIR

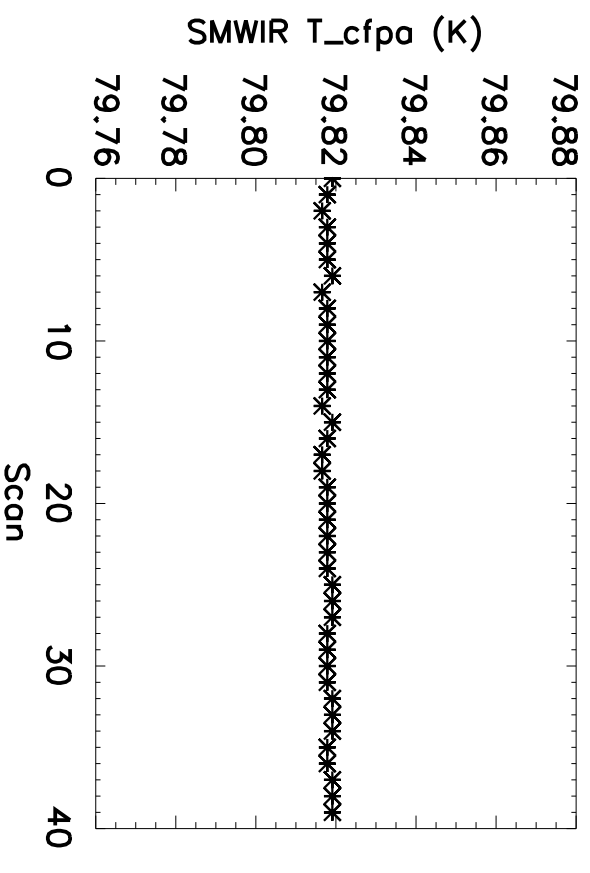


T_cfpα vs Scan: Collect=8

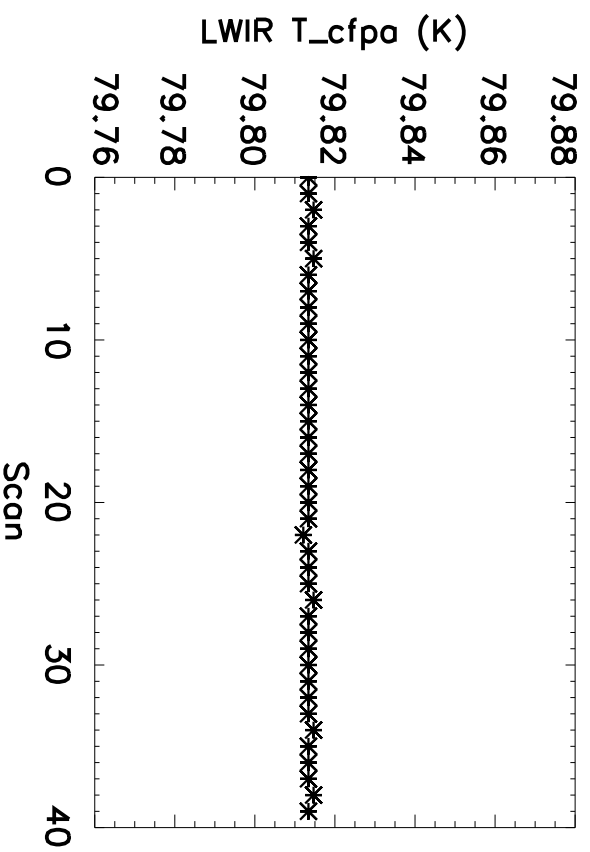
VisNIR



SMWIR

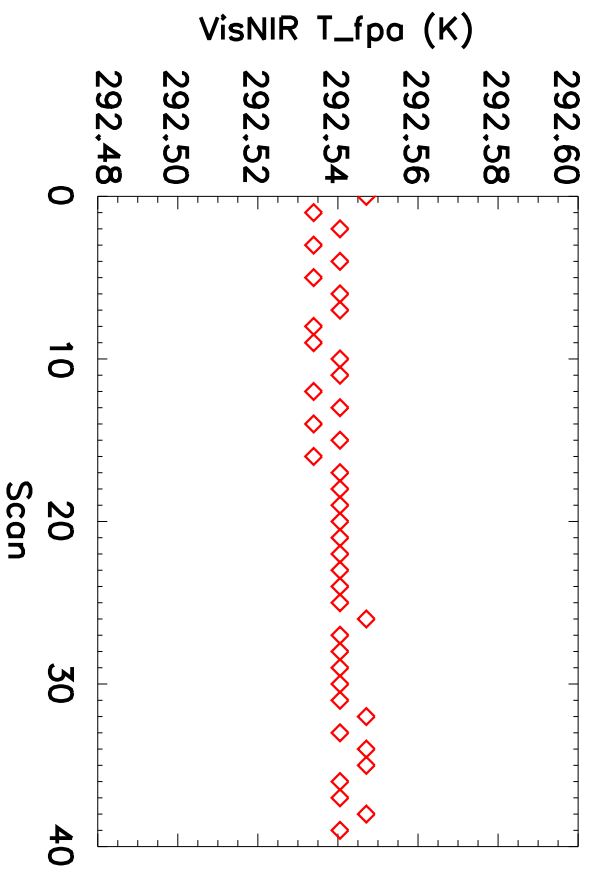


LWIR

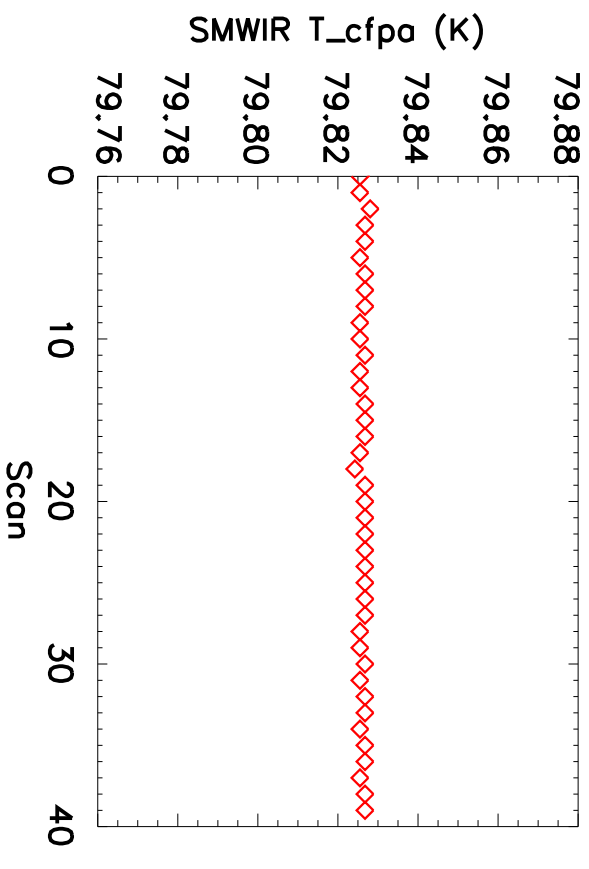


T_cfpd vs Scan: Collect=9

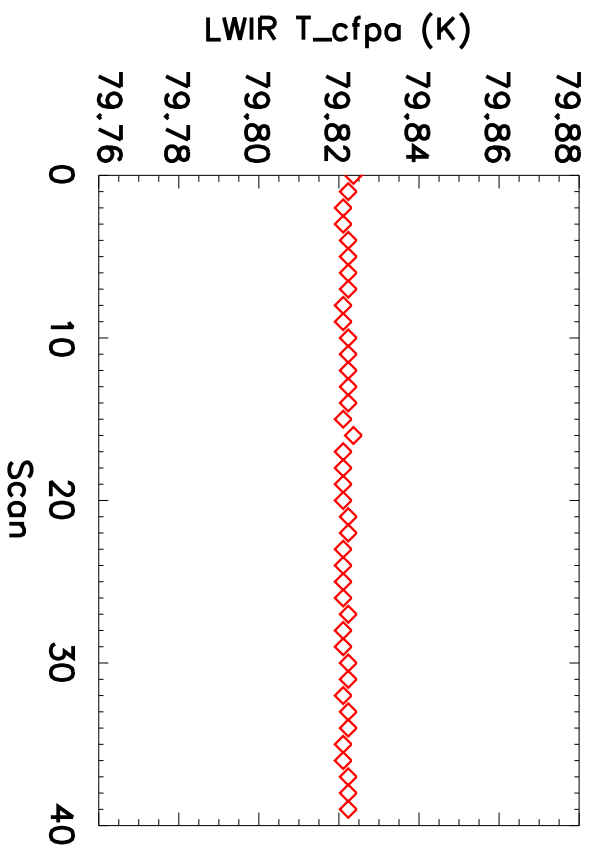
VisNIR



SMWIR

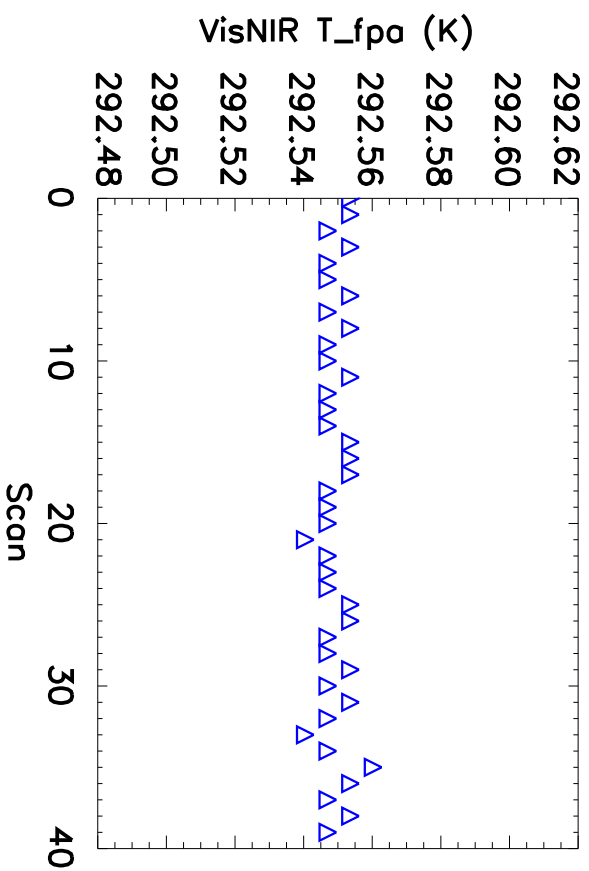


LWIR

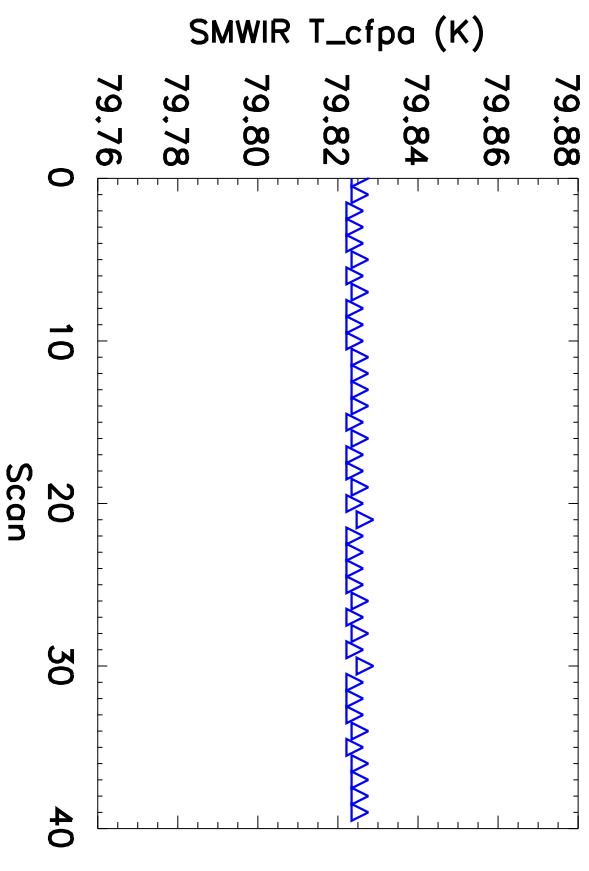


T_cfpd vs Scan: Collect=10

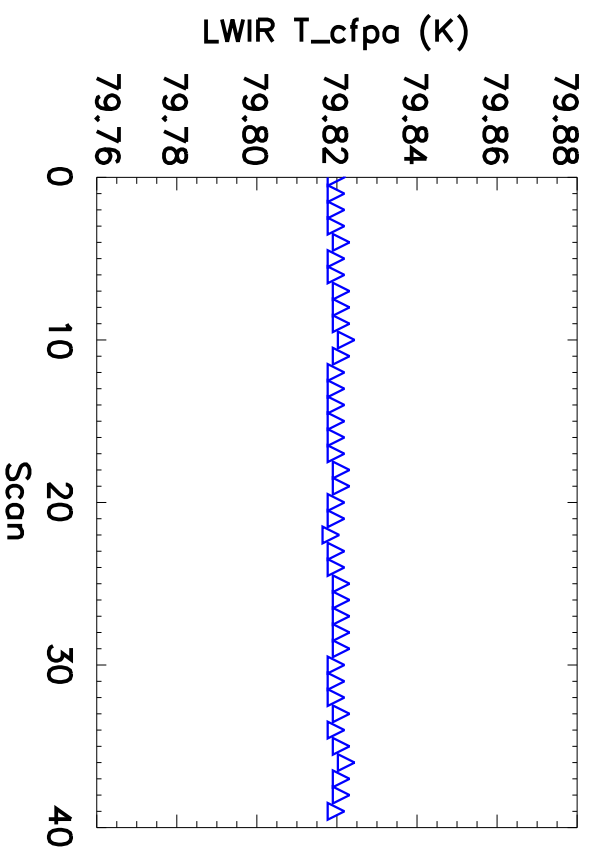
VisNIR



SMWIR

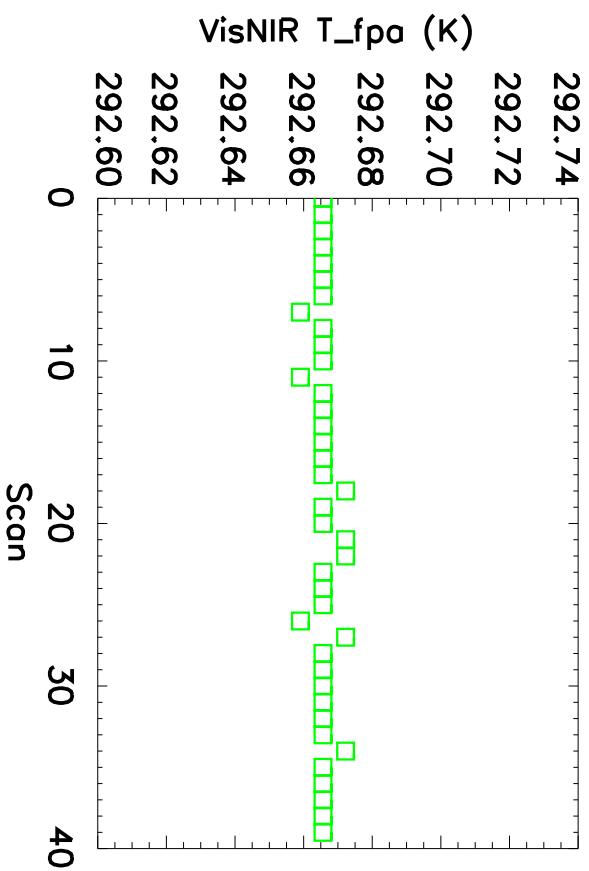


LWIR

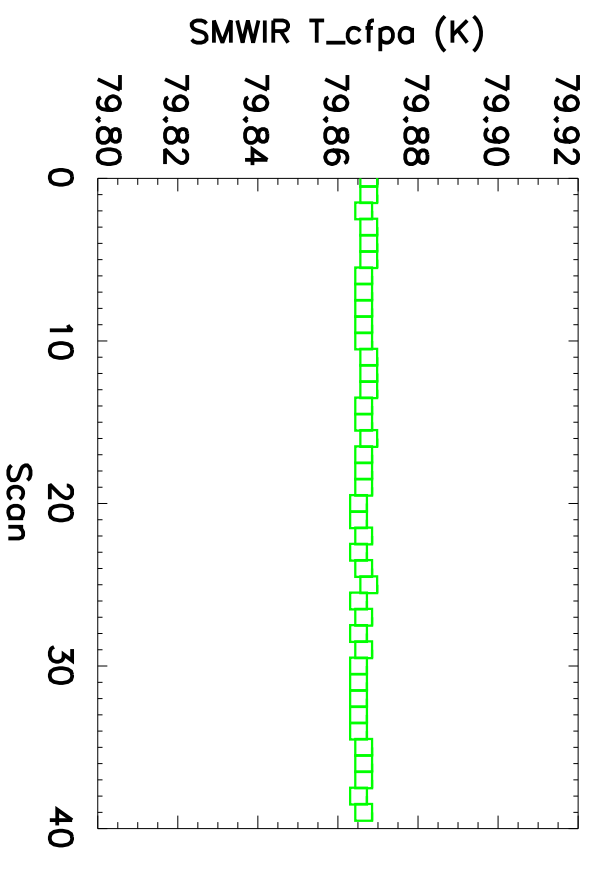


T_cfpd vs Scan: Collect=11

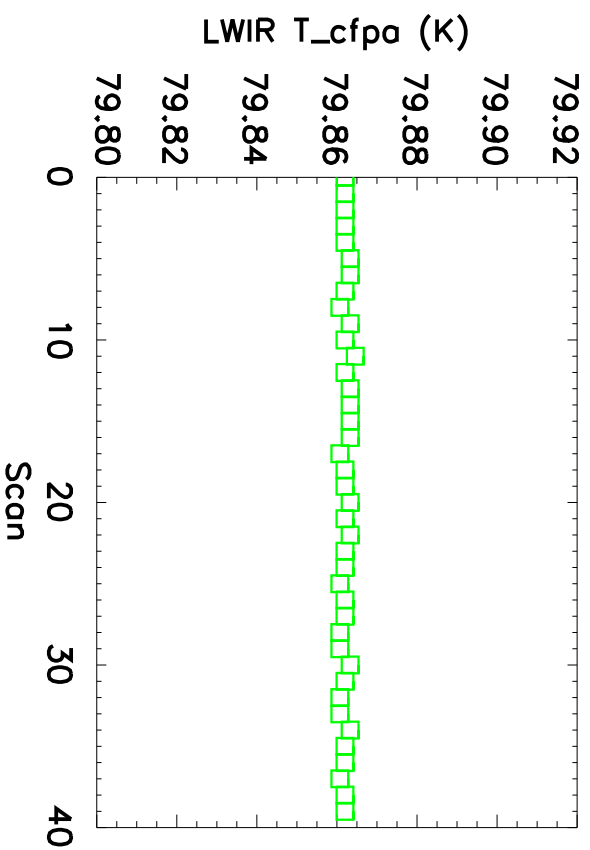
VisNIR



SMWIR

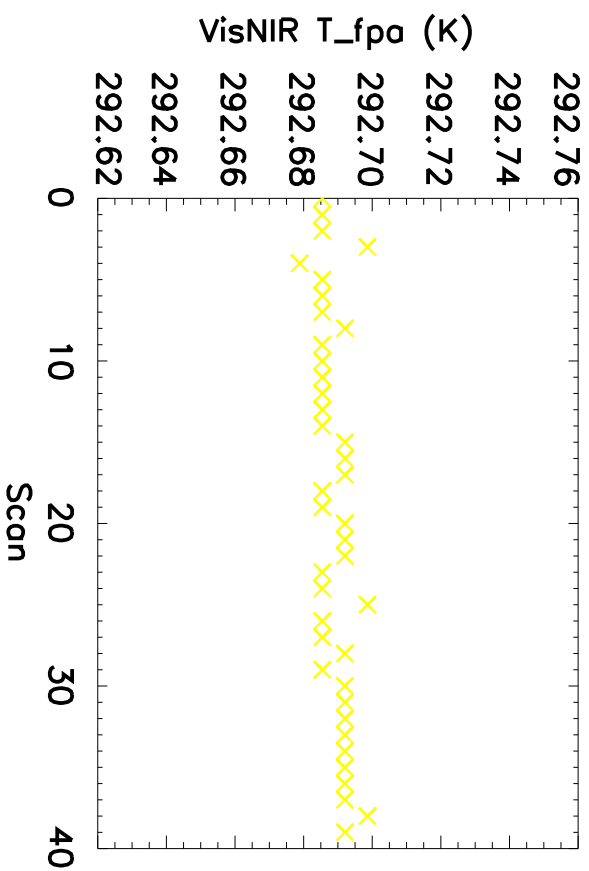


LWIR

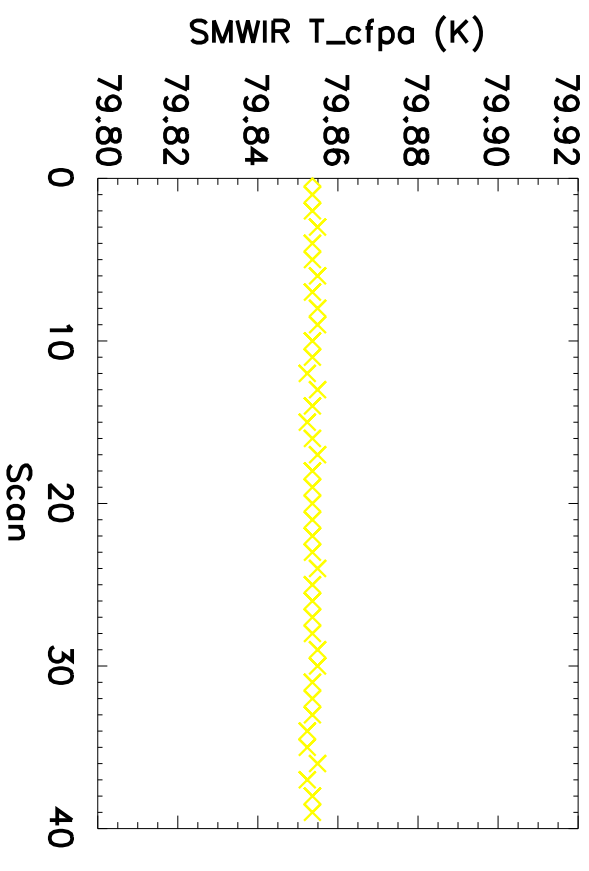


T_cfpd vs Scan: Collect=12

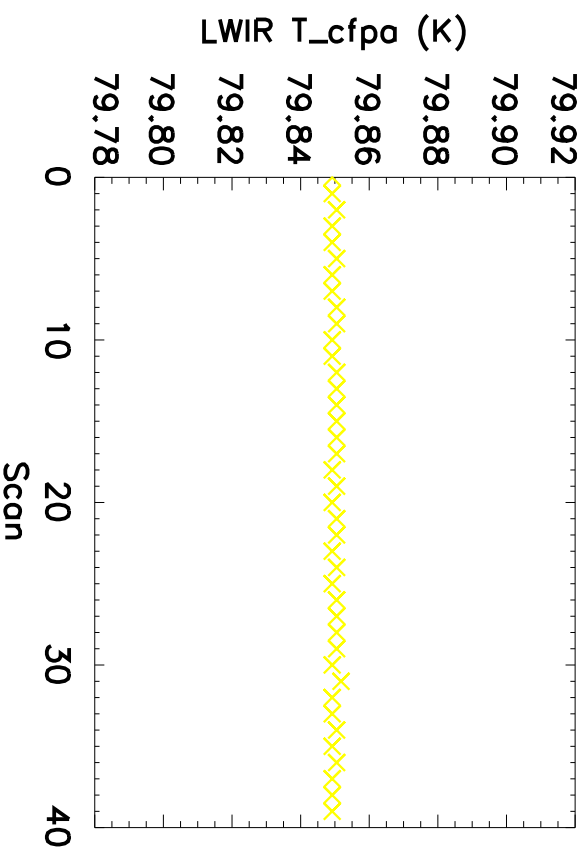
VisNIR



SMWIR

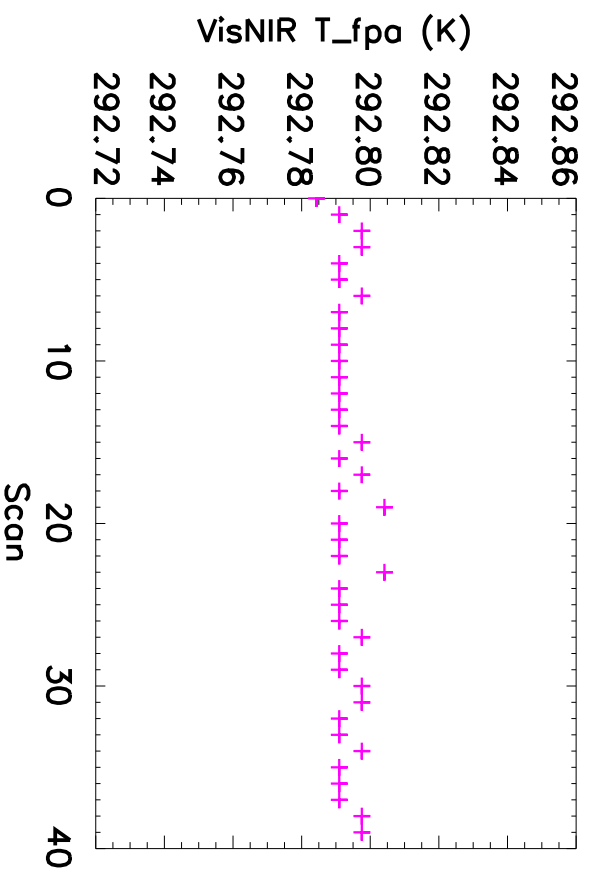


LWIR

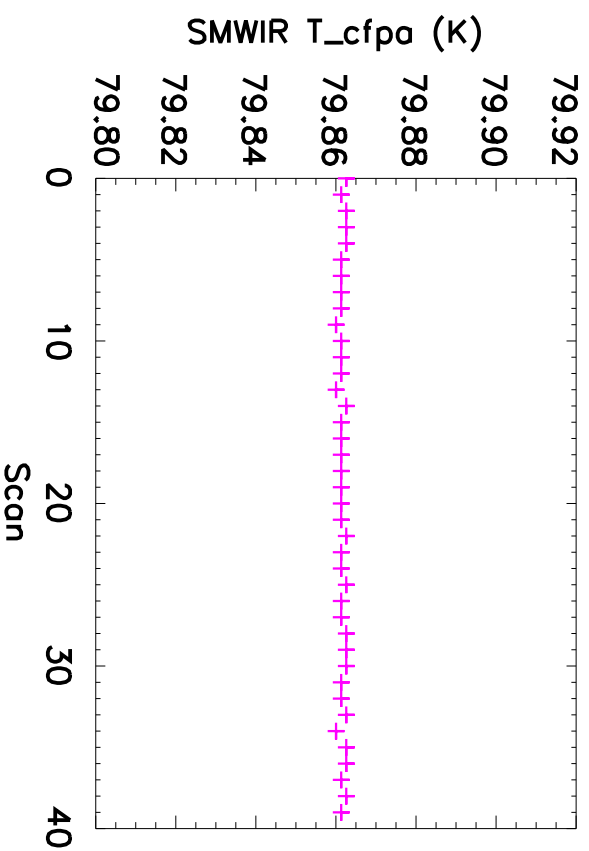


T_cfpd vs Scan: Collect=13

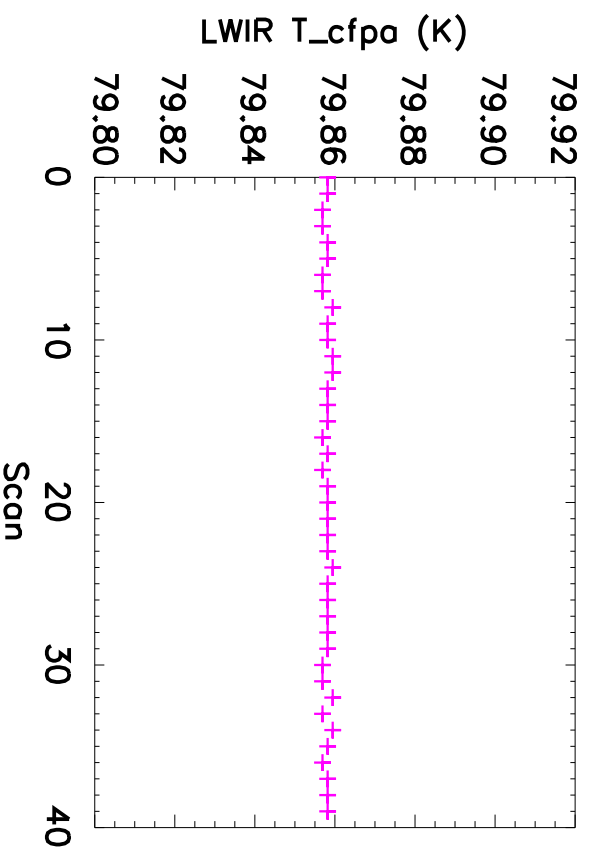
VisNIR



SMWIR

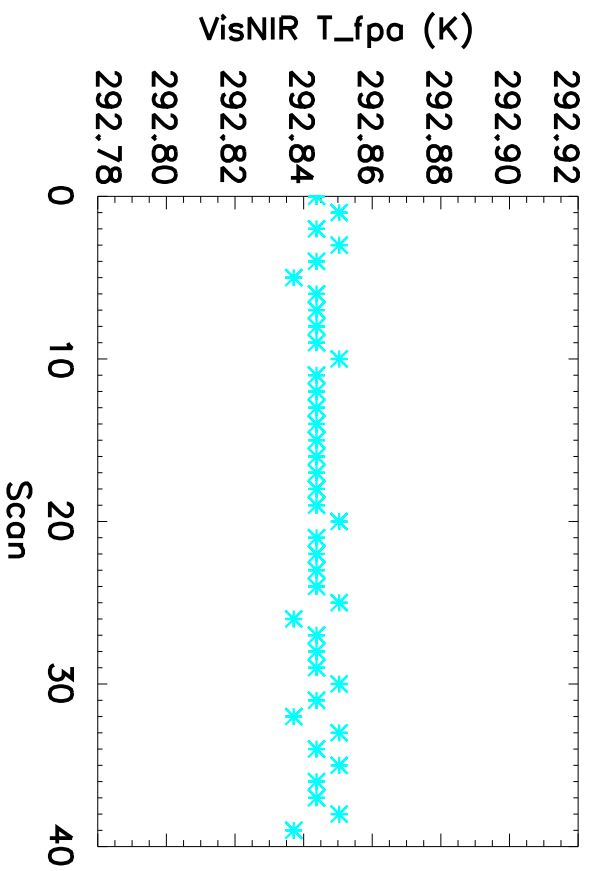


LWIR

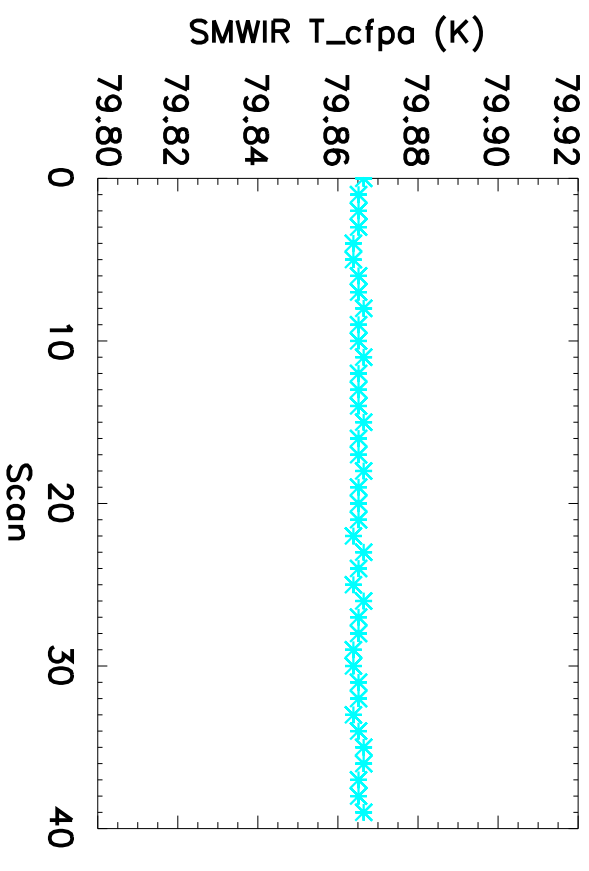


T_cfpd vs Scan: Collect=14

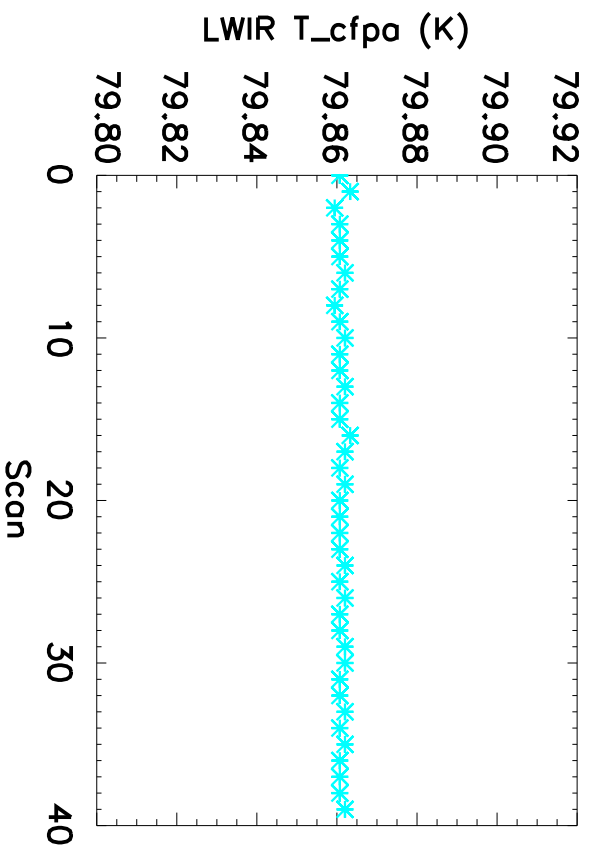
VisNIR



SMWIR

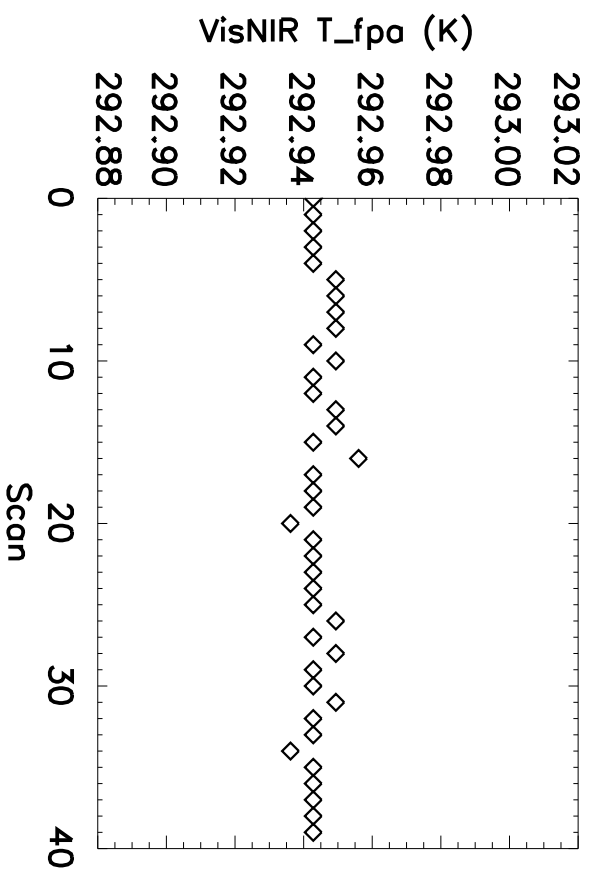


LWIR

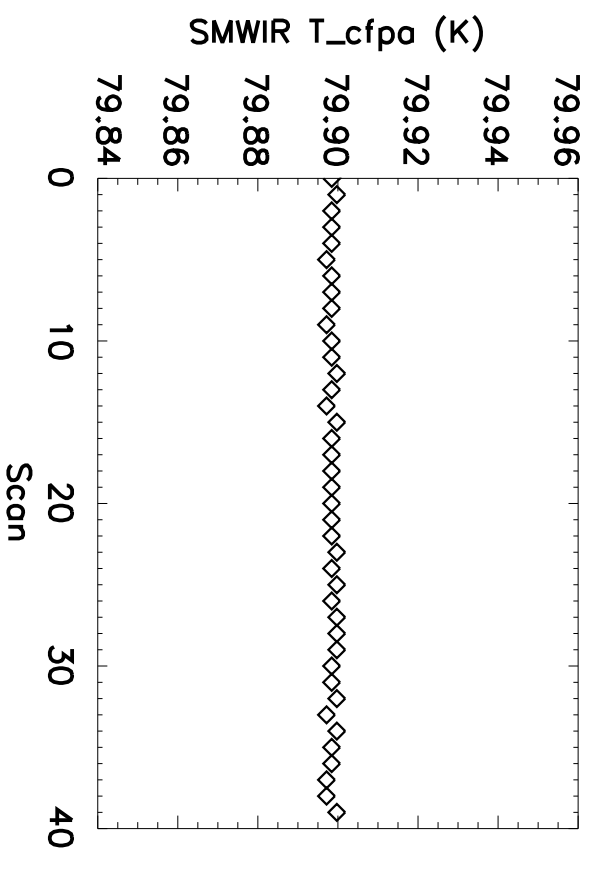


T_cfpd vs Scan: Collect=15

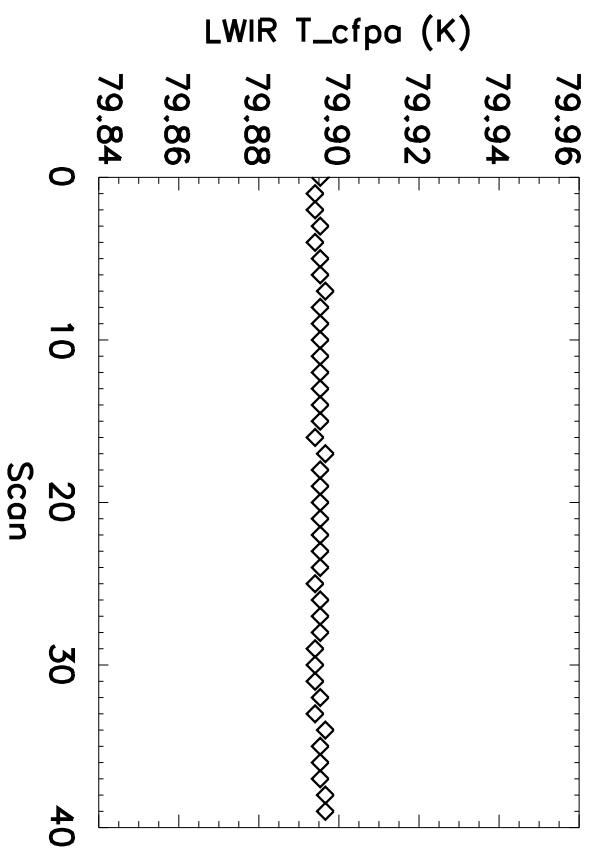
VisNIR



SMWIR

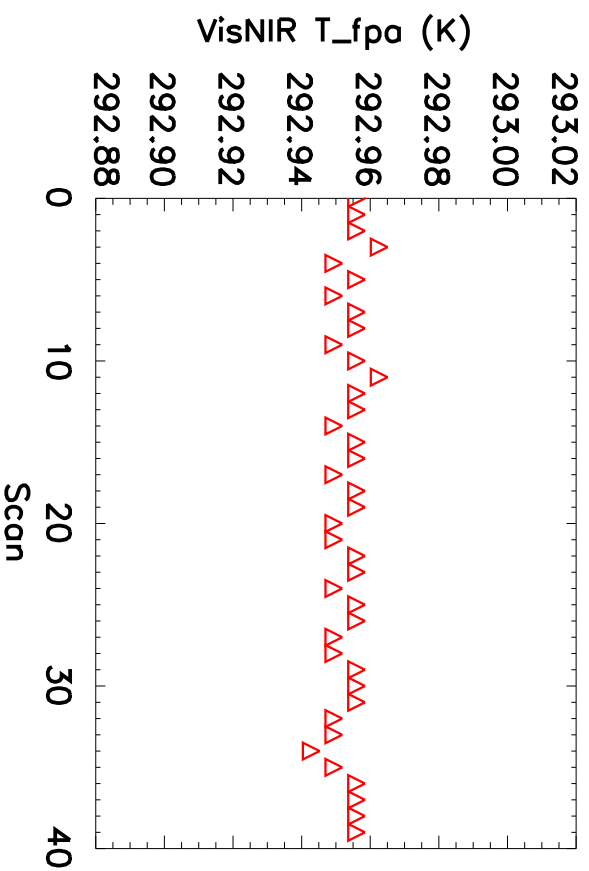


LWIR

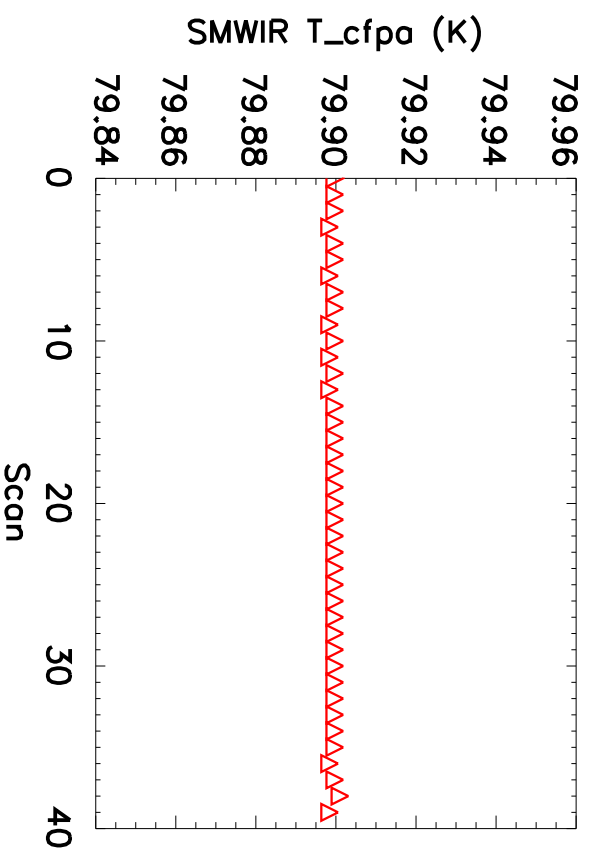


T_cfpd vs Scan: Collect=16

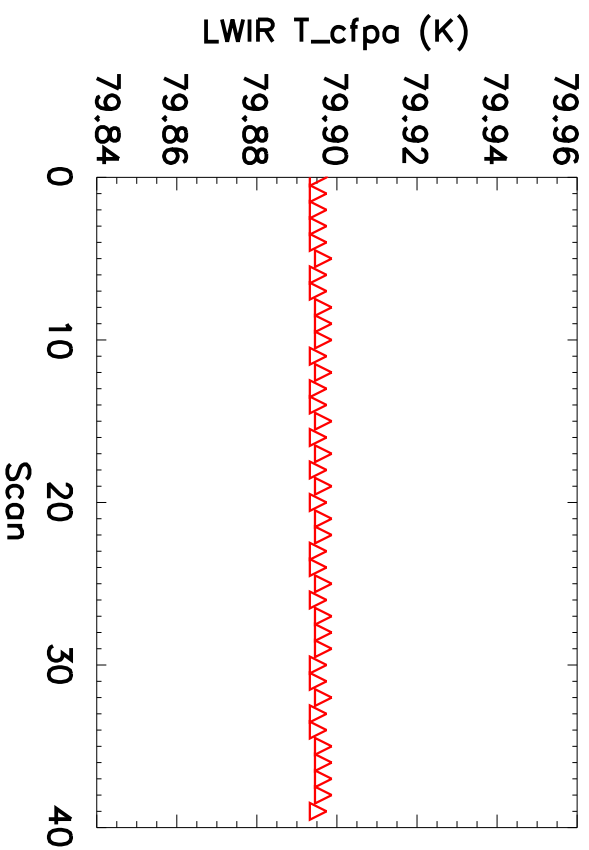
VisNIR



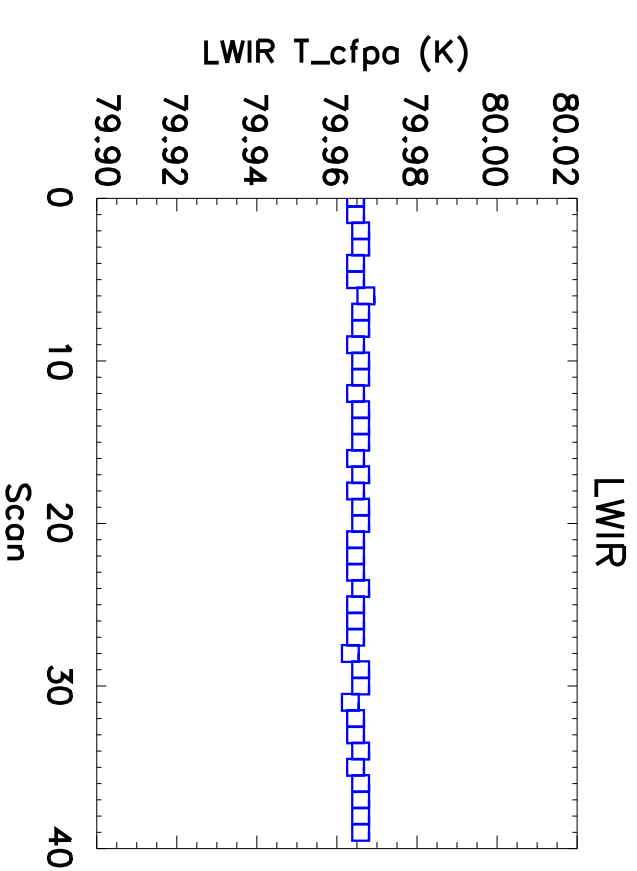
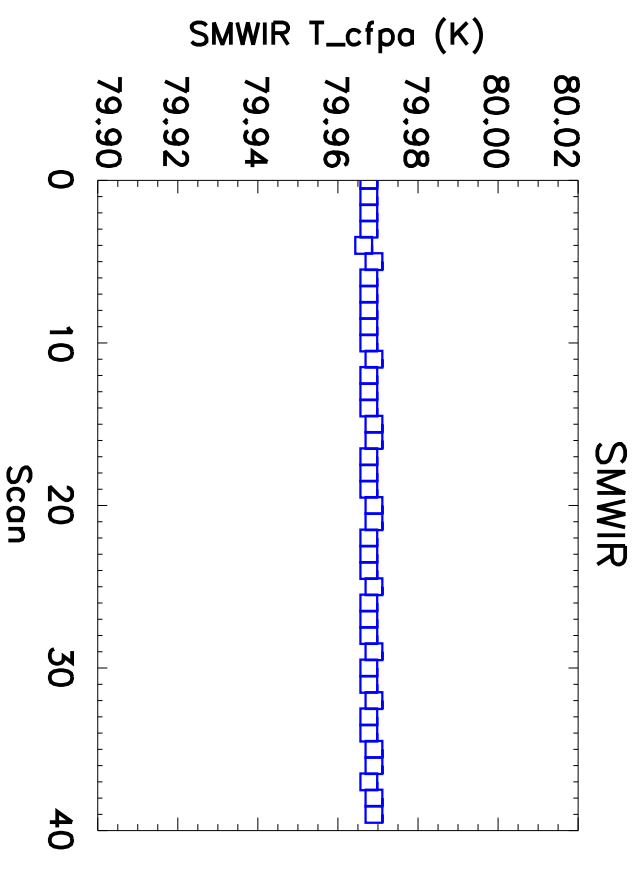
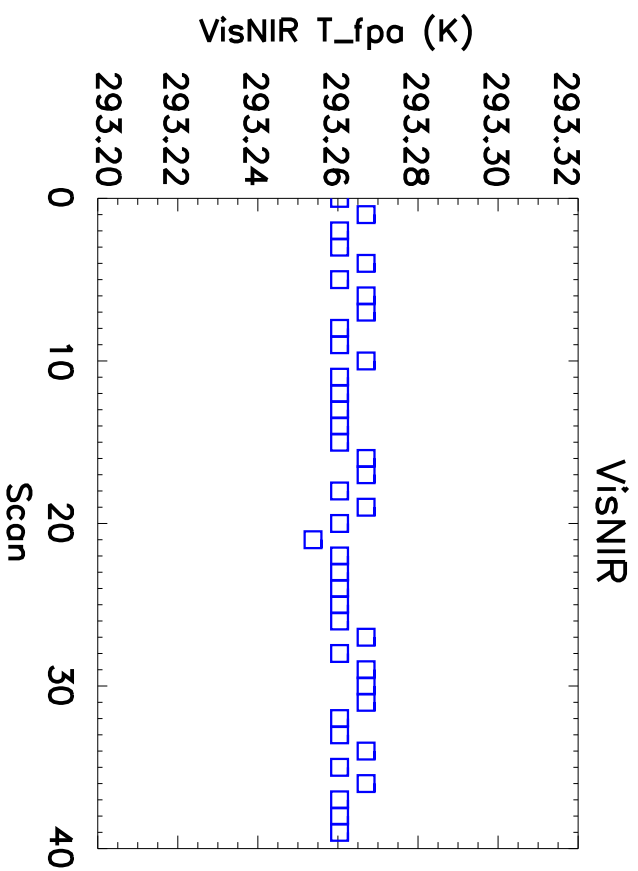
SMWIR



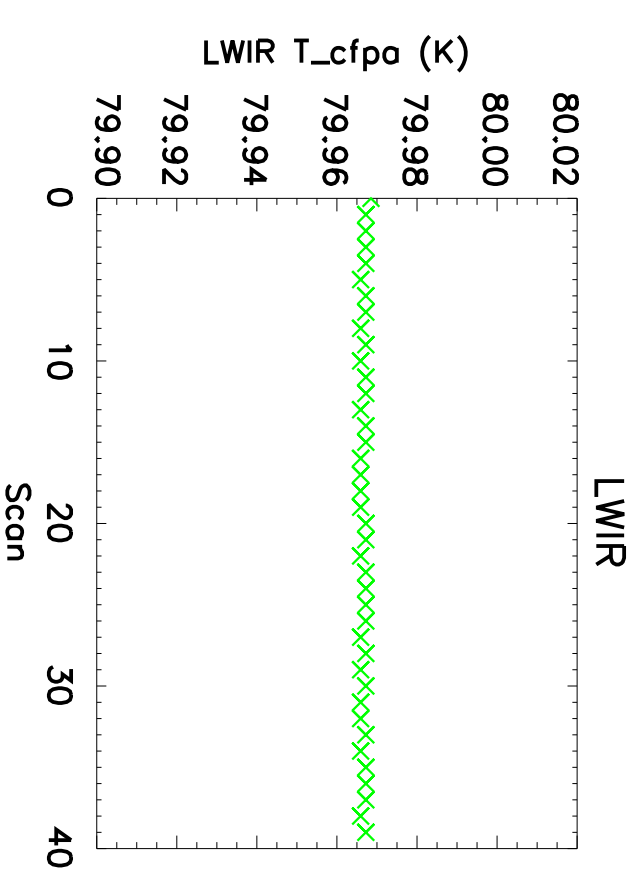
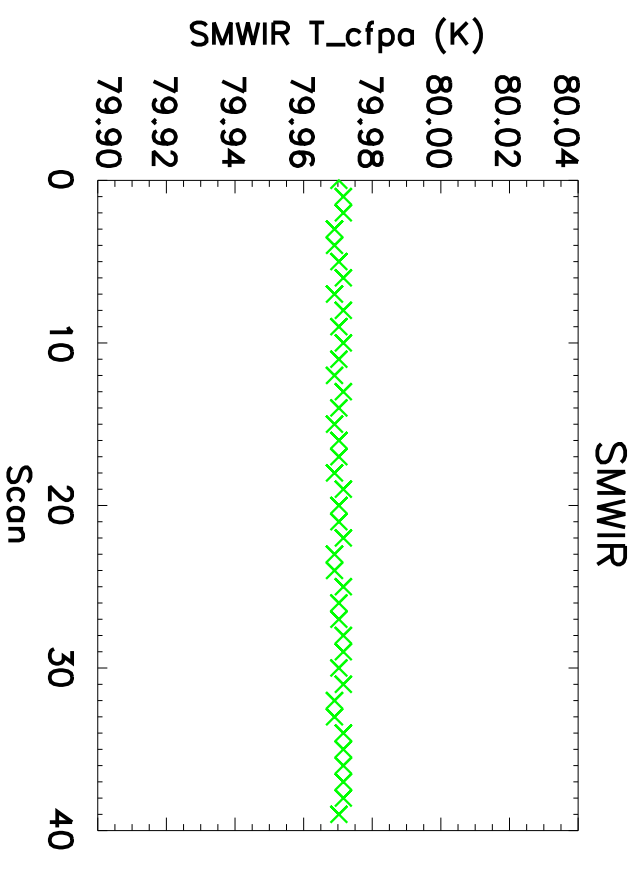
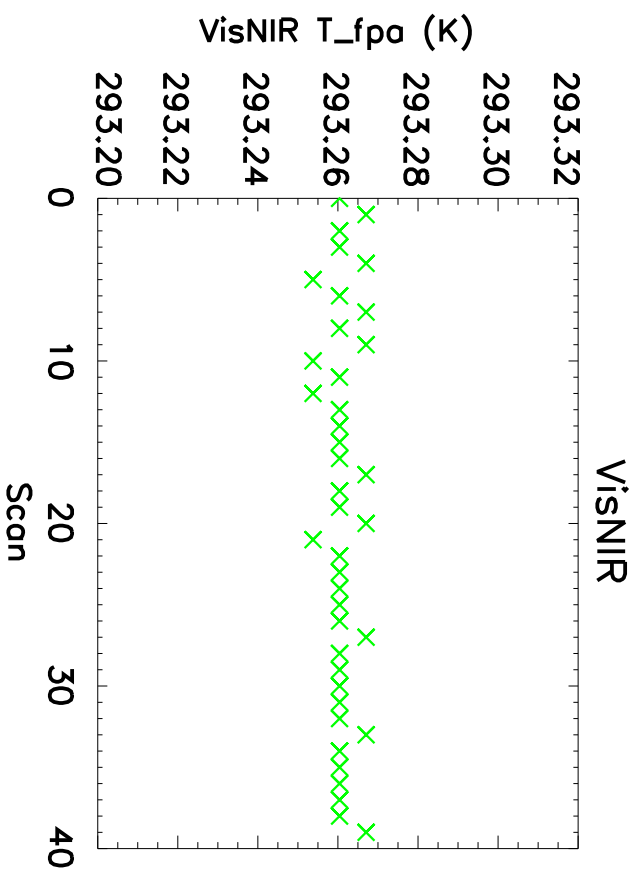
LWIR



T_cfpd vs Scan: Collect=17

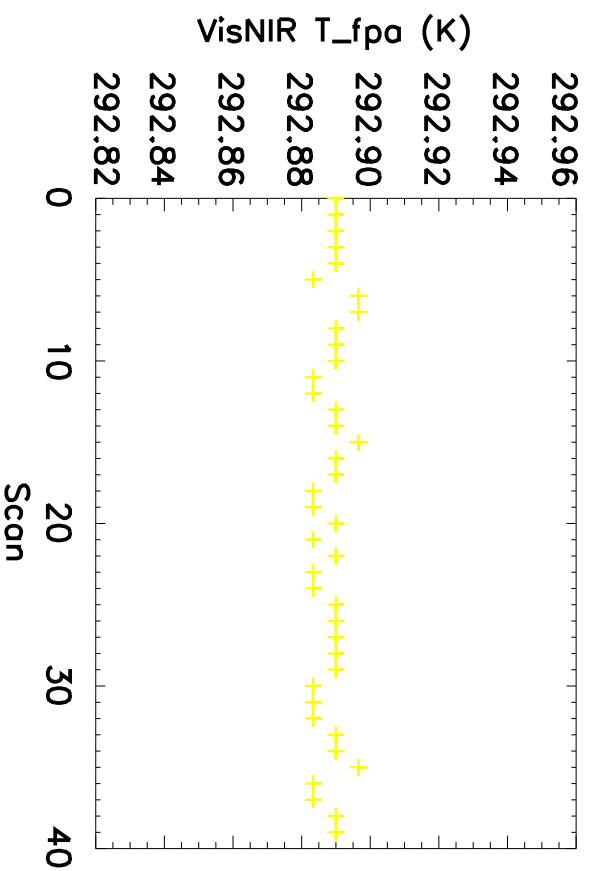


T_cfpd vs Scan: Collect=18

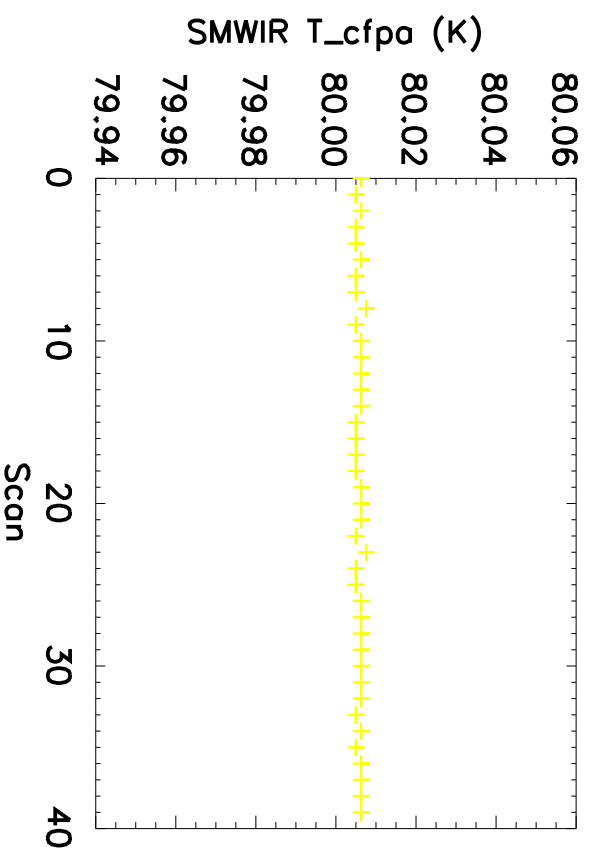


T_cfpd vs Scan: Collect=19

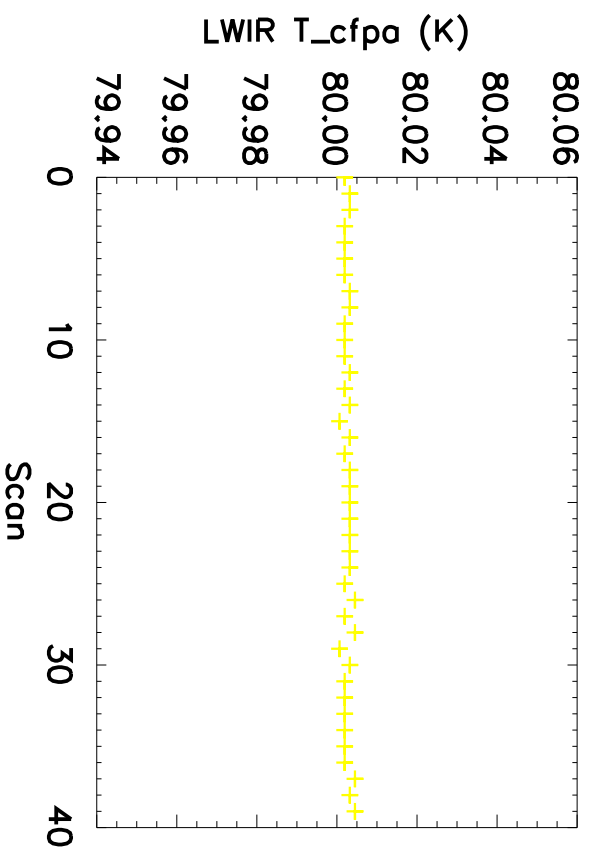
VisNIR



SMWIR

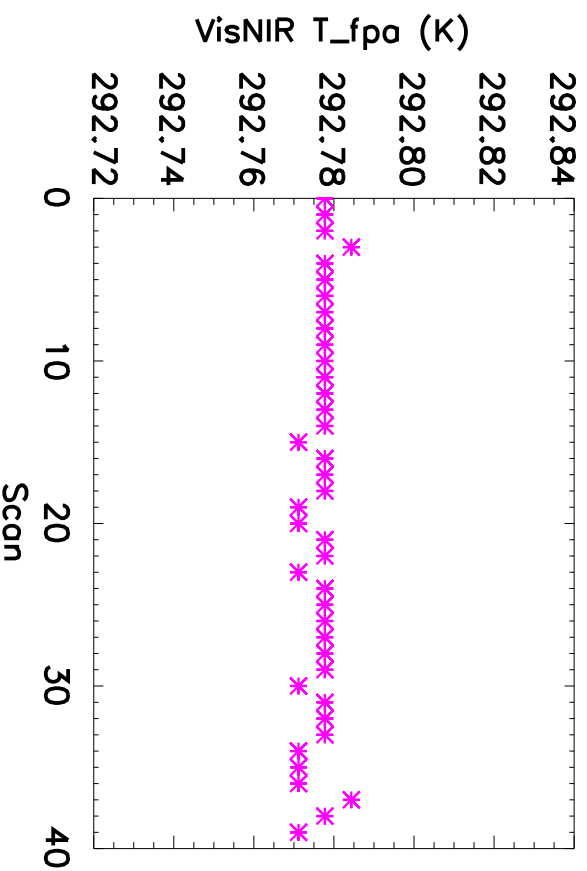


LWIR

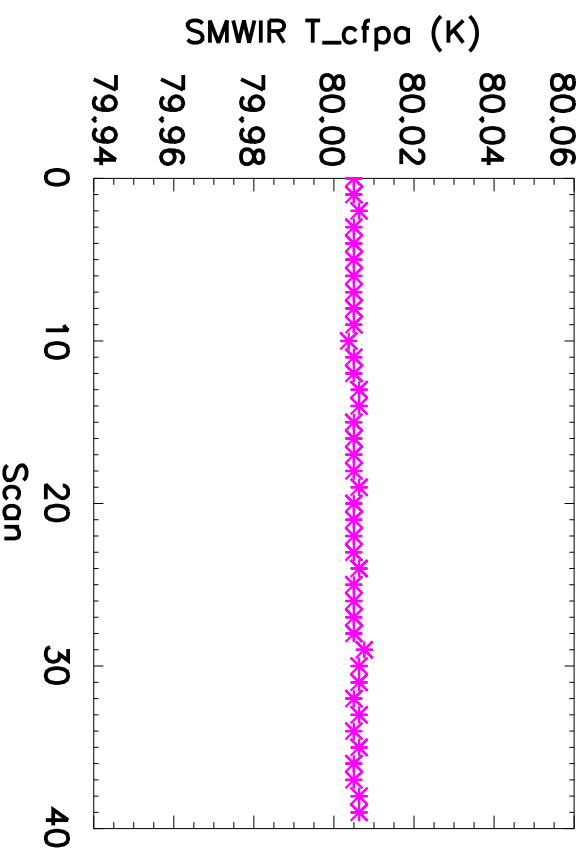


T_cfpd vs Scan: Collect=20

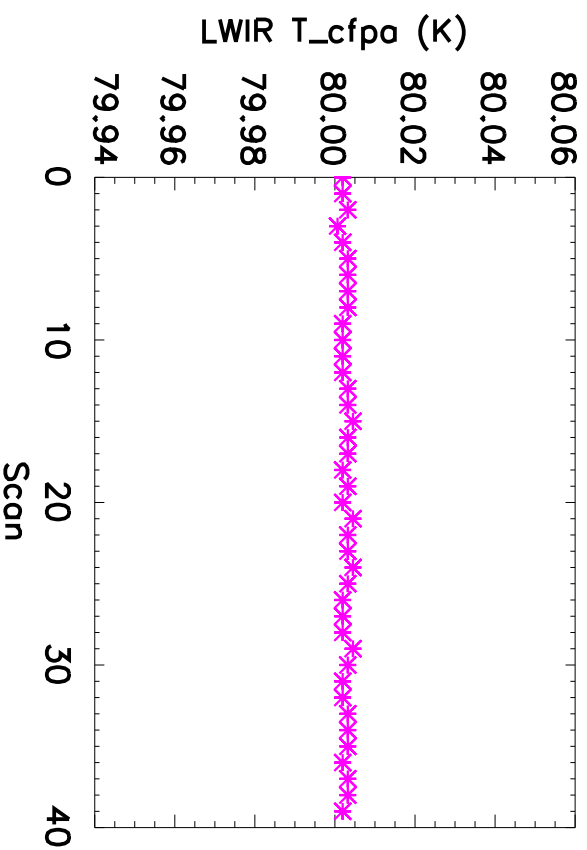
VisNIR



SMWIR

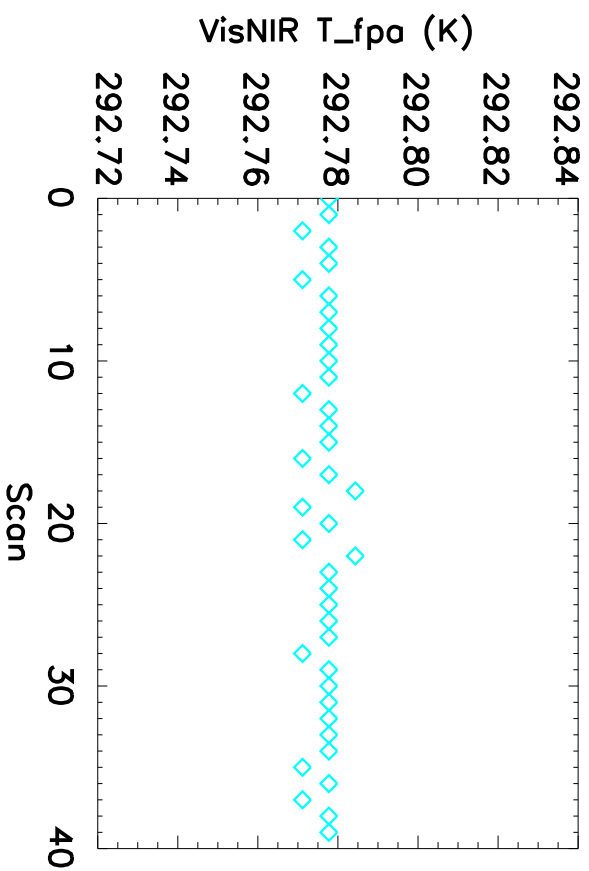


LWIR

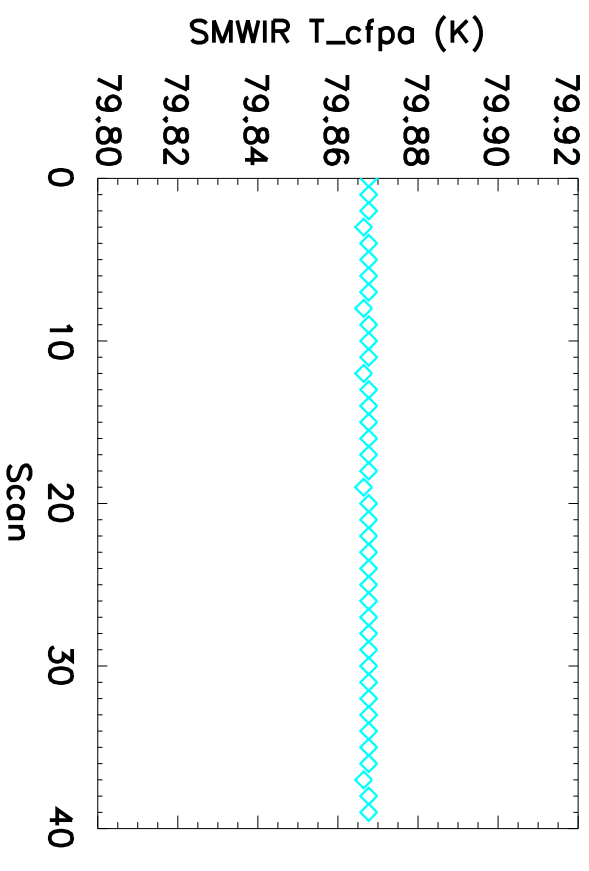


T_cfpd vs Scan: Collect=21

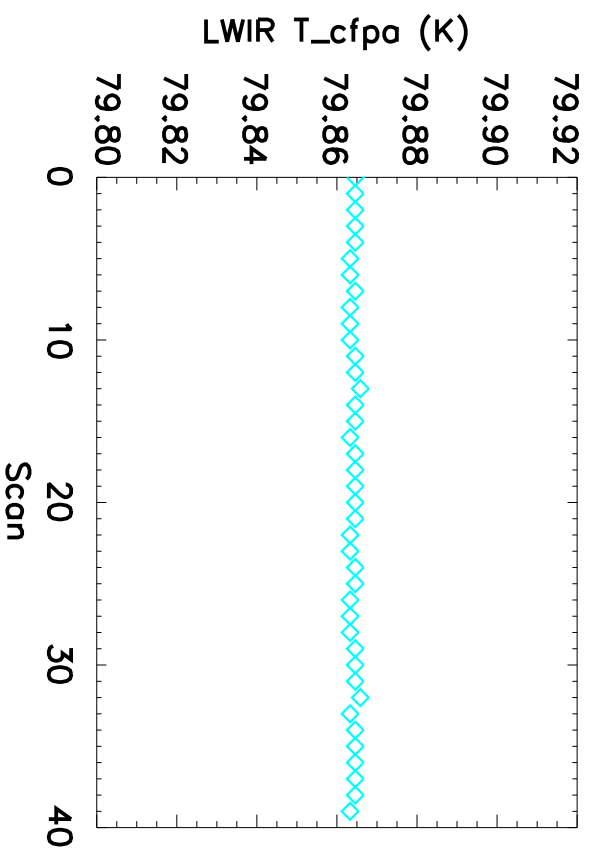
VisNIR



SMWIR

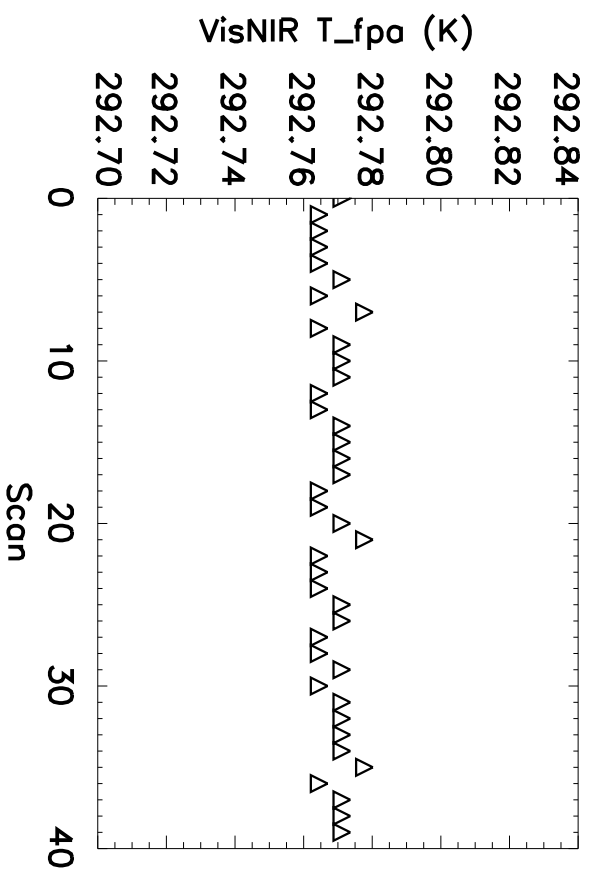


LWIR

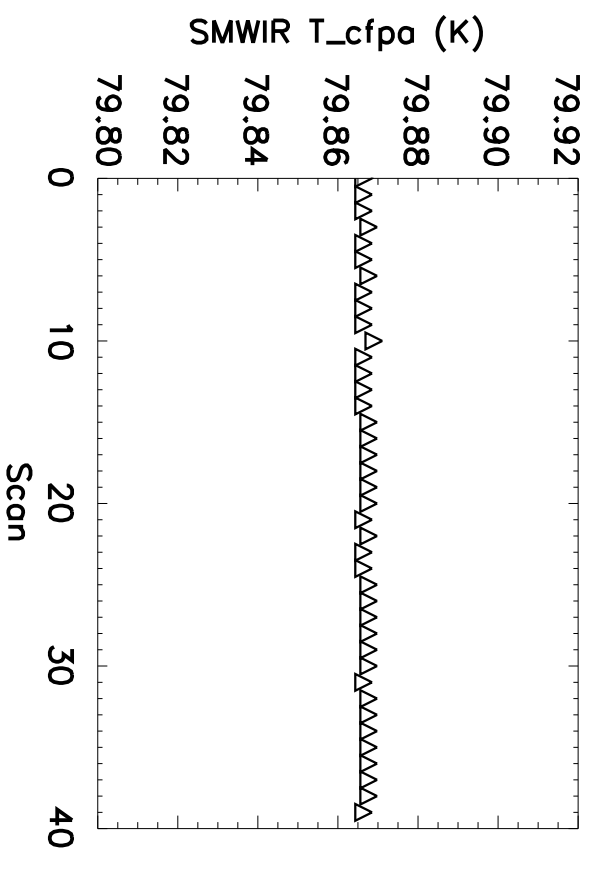


T_cfpd vs Scan: Collect=22

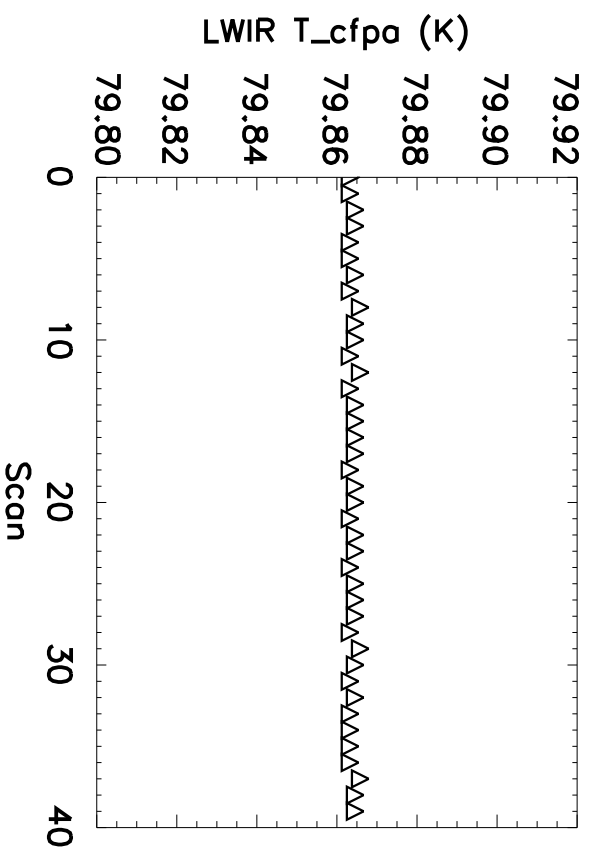
VisNIR



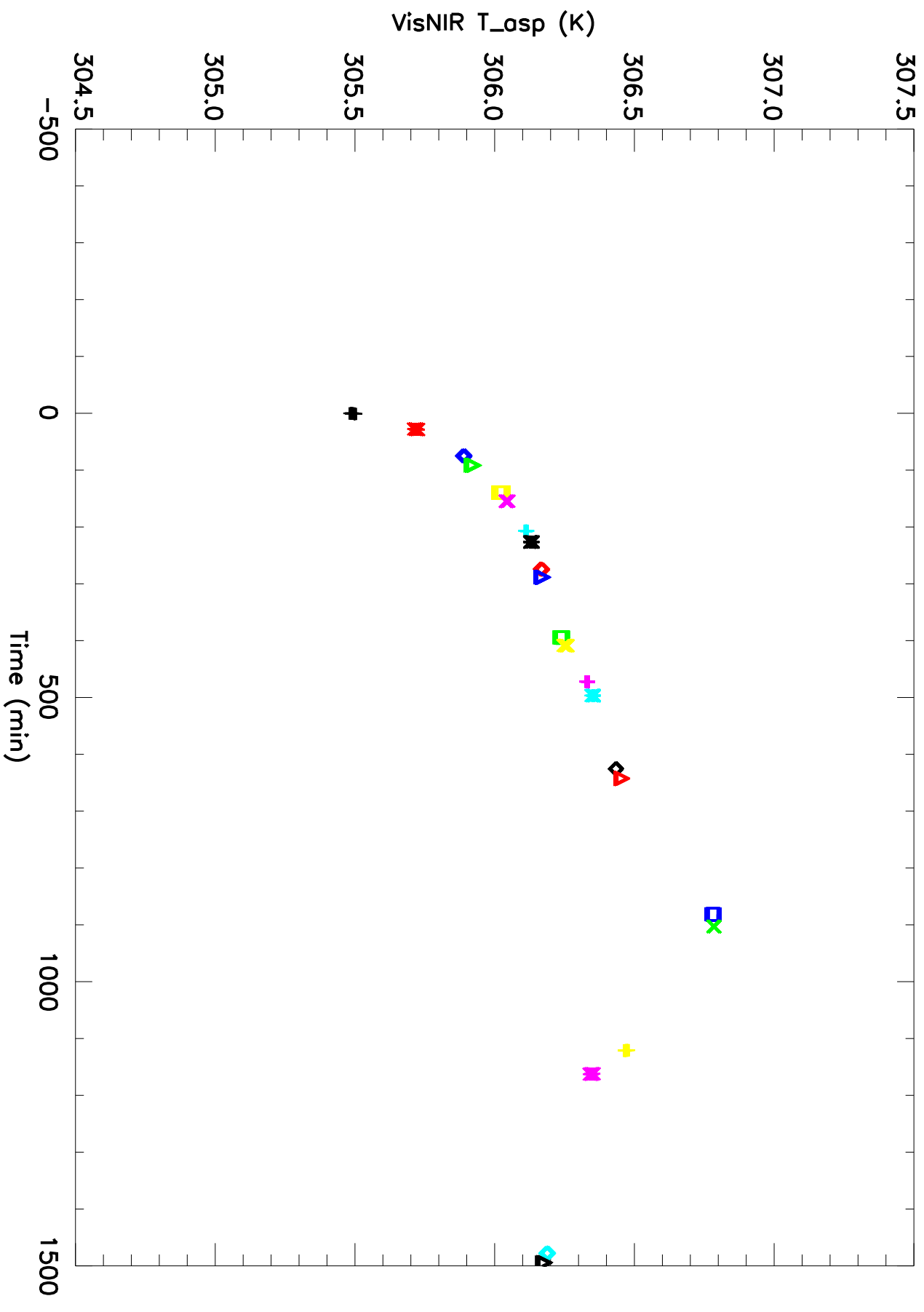
SMWIR

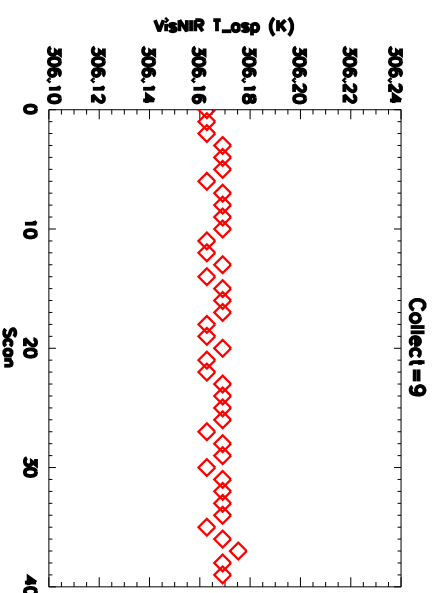
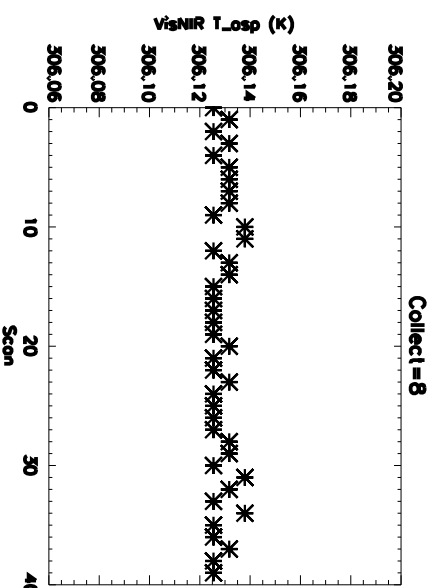
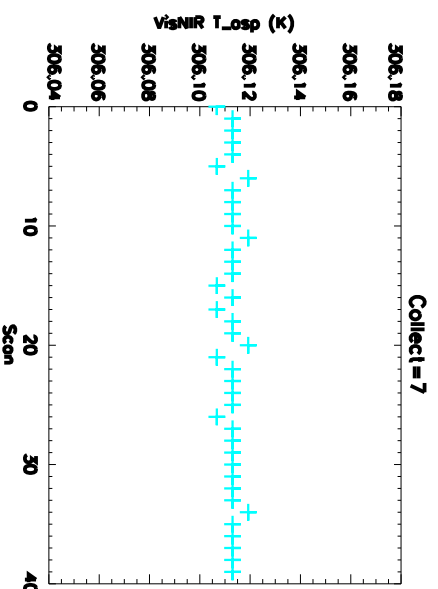
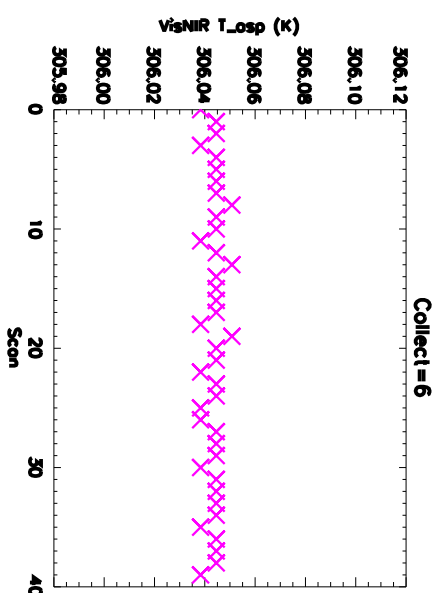
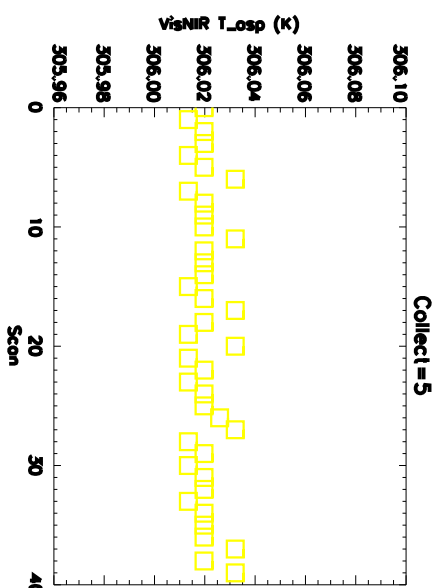
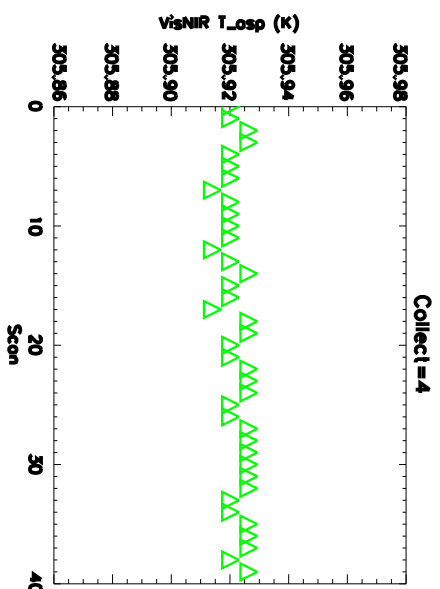
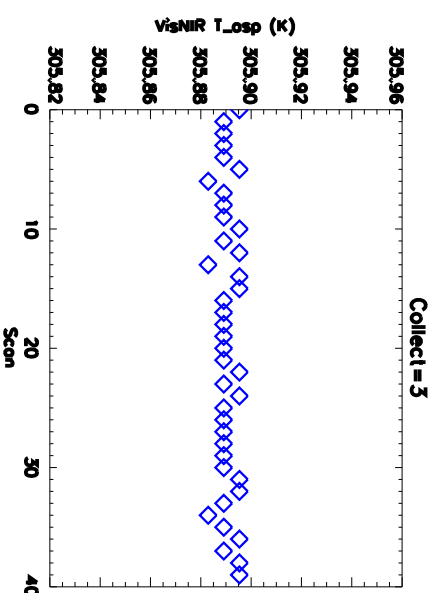
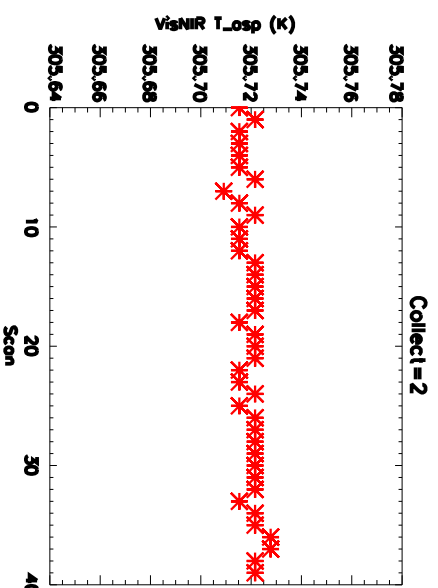
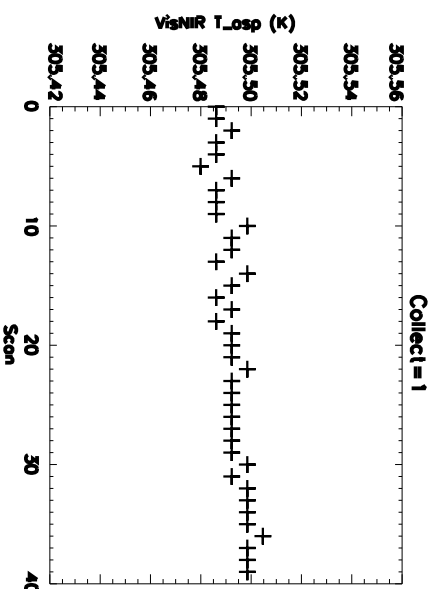


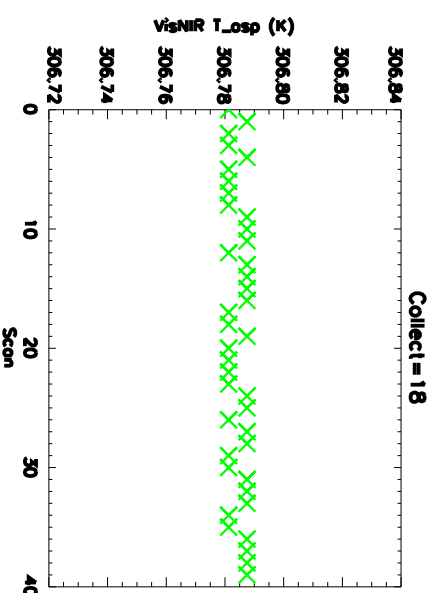
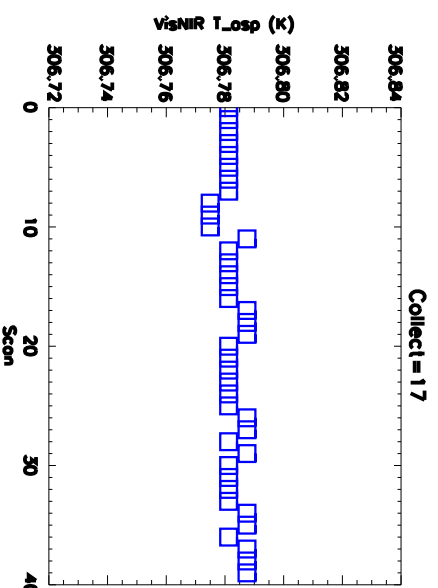
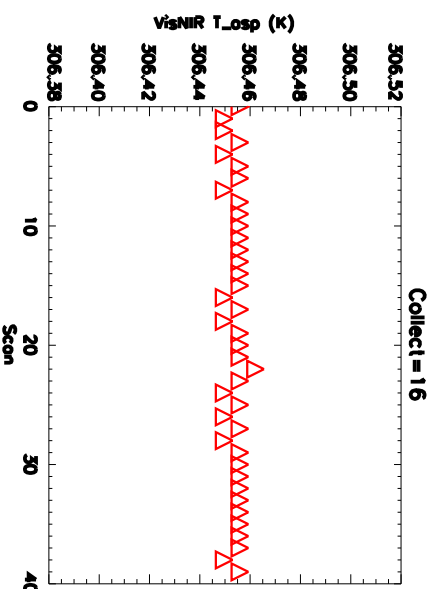
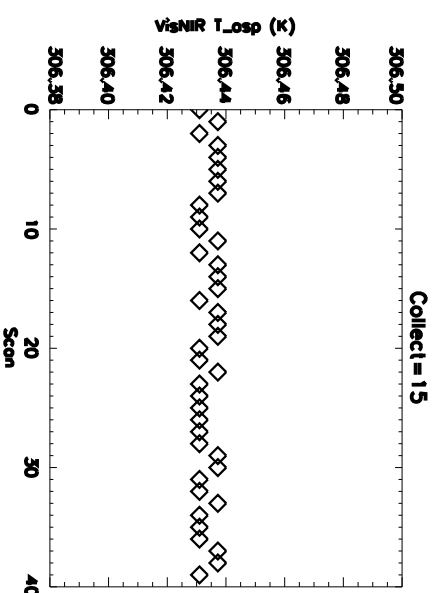
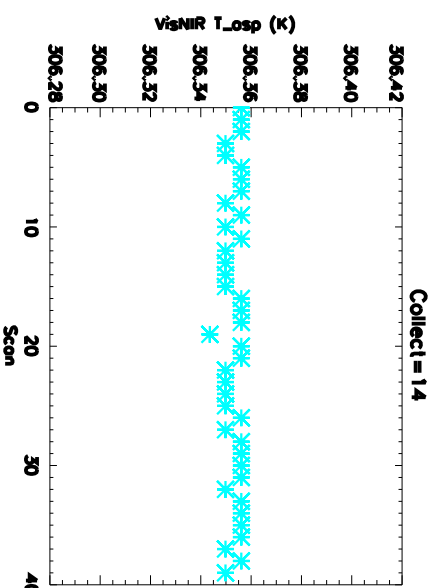
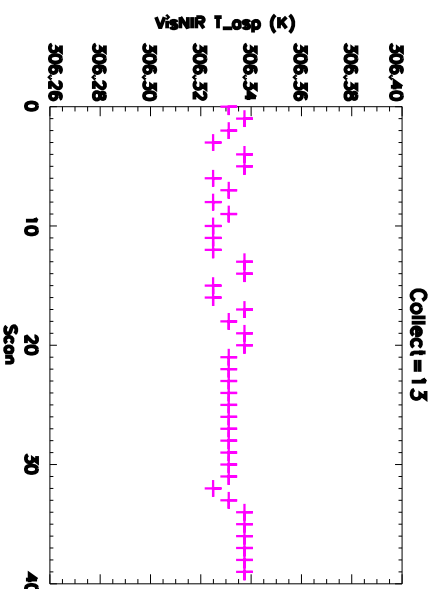
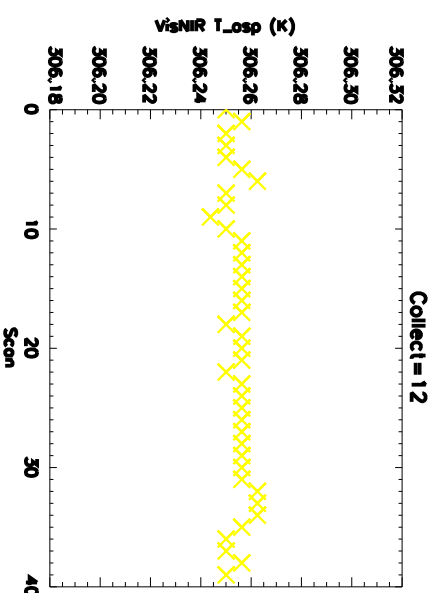
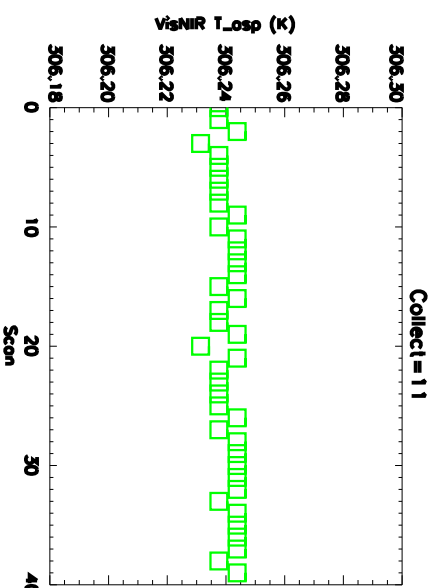
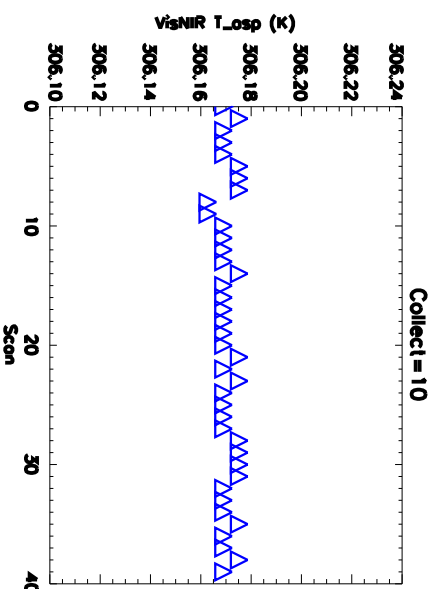
LWIR



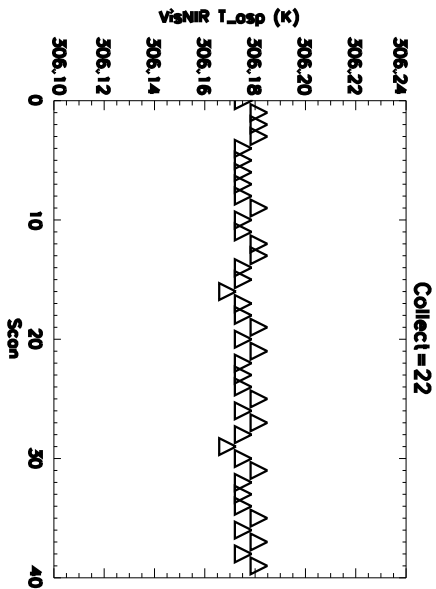
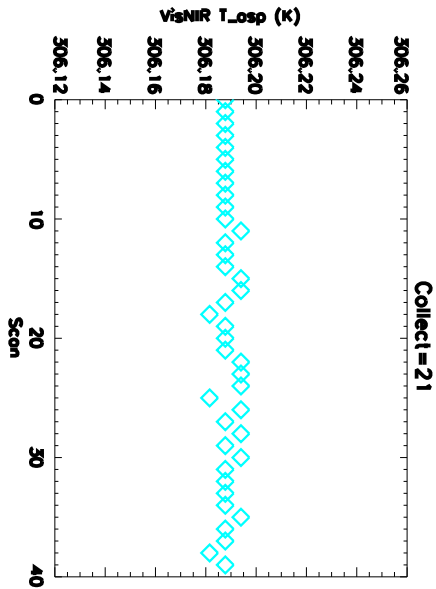
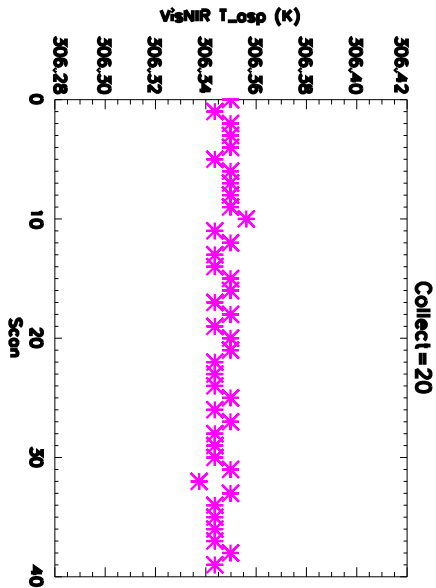
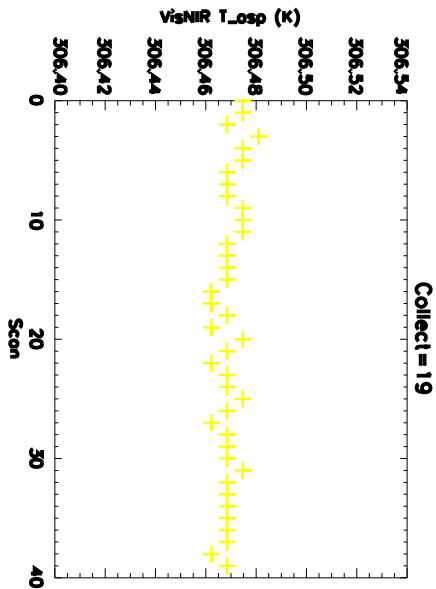
VisNIR T_{asp} vs Time



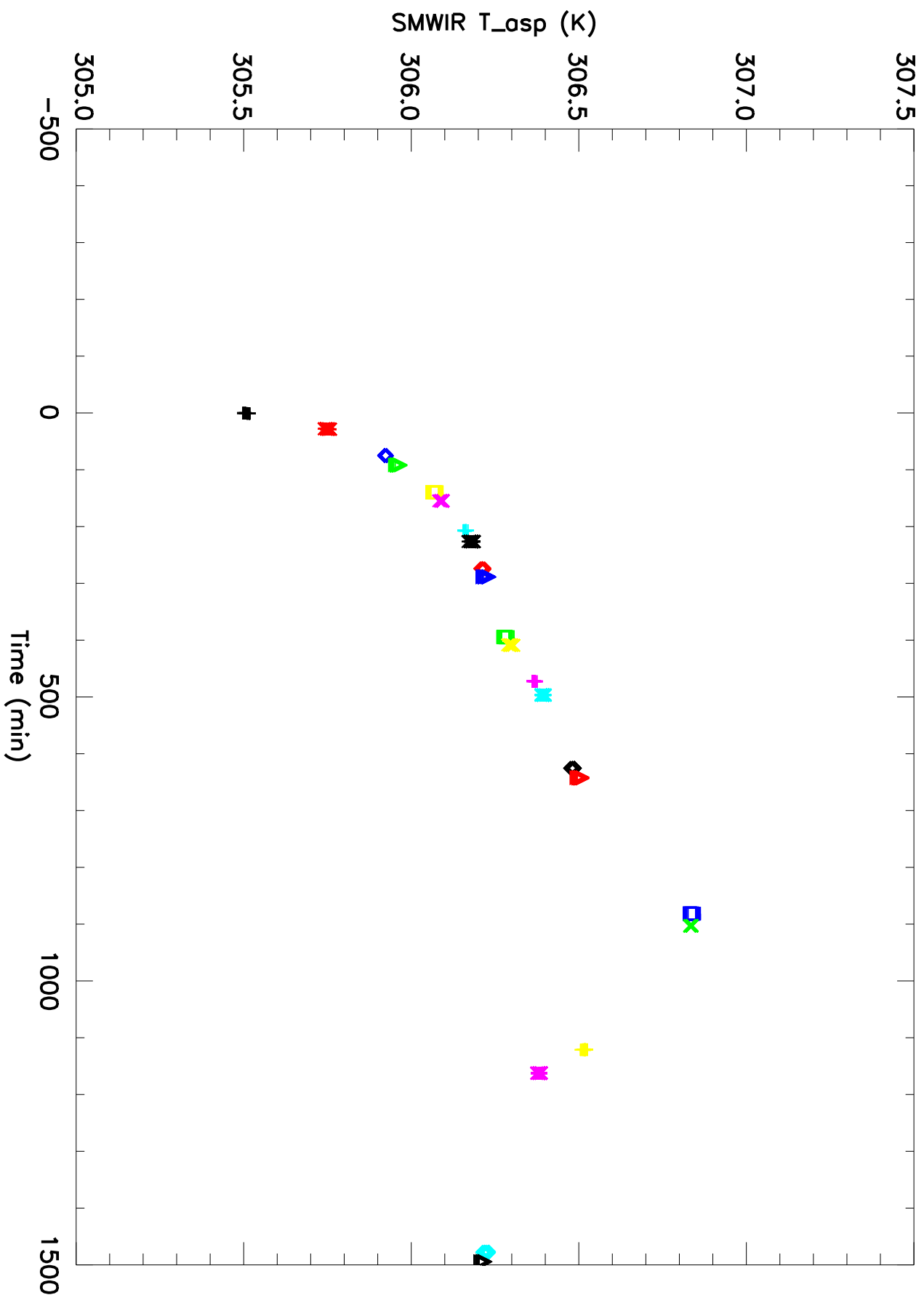


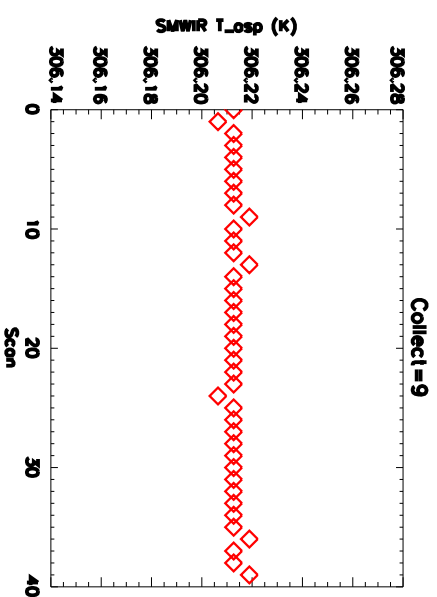
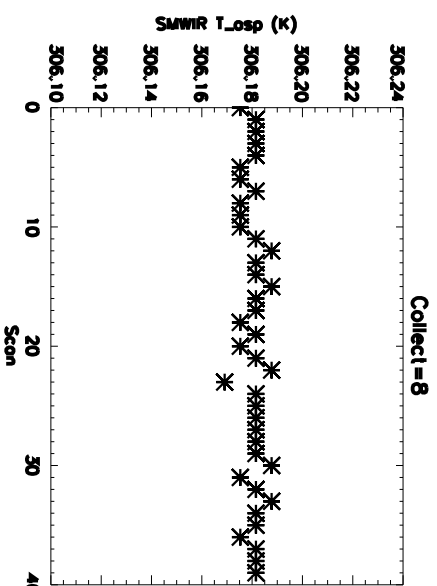
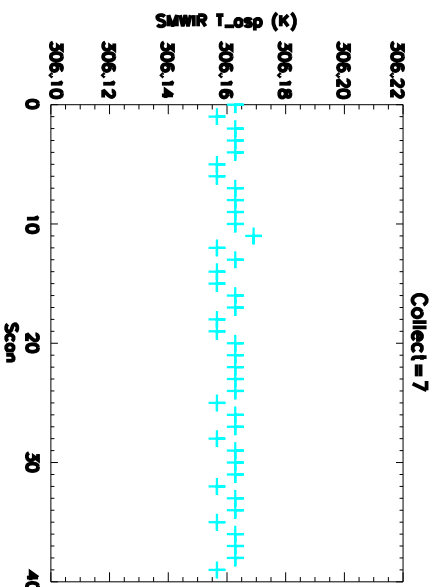
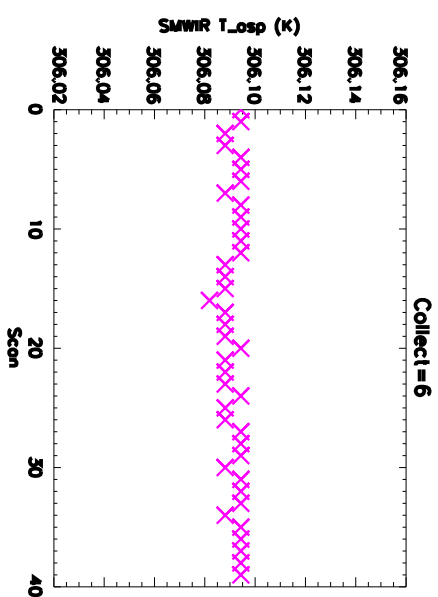
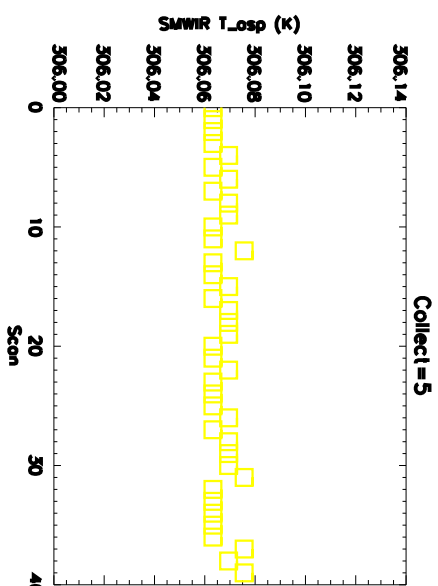
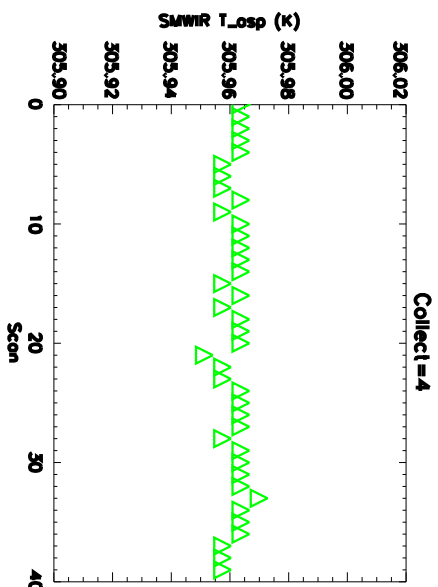
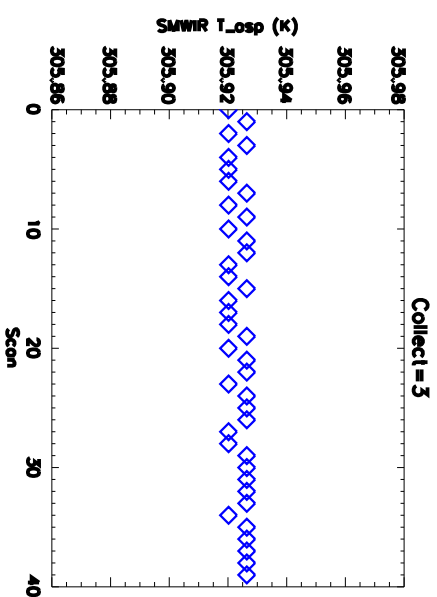
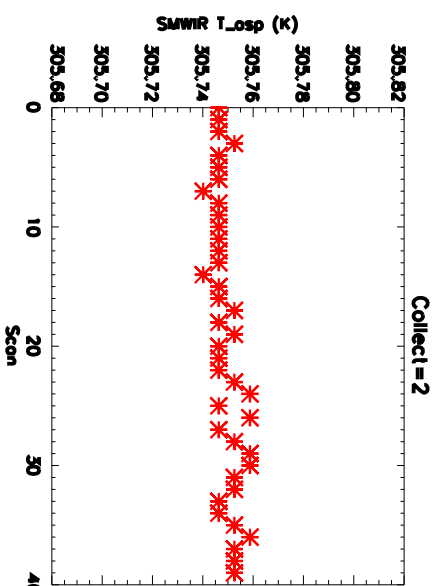
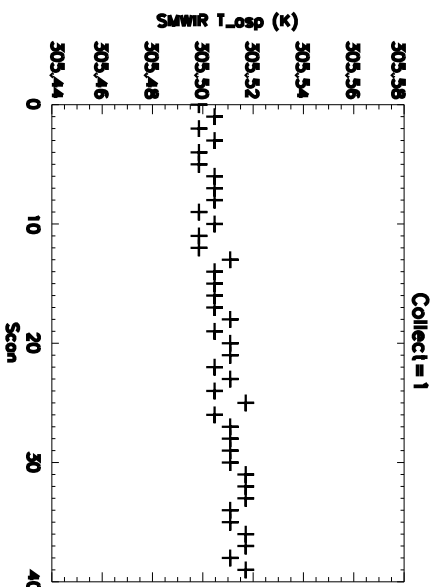


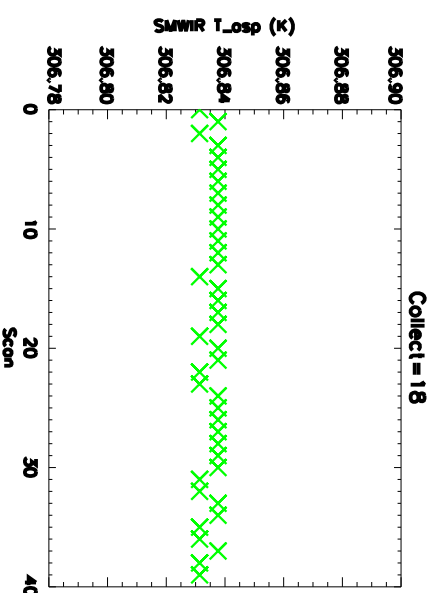
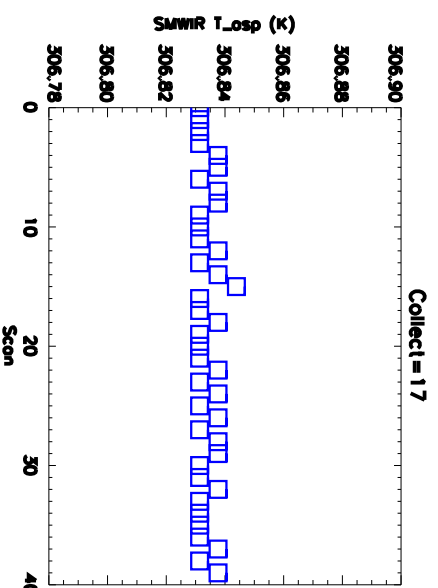
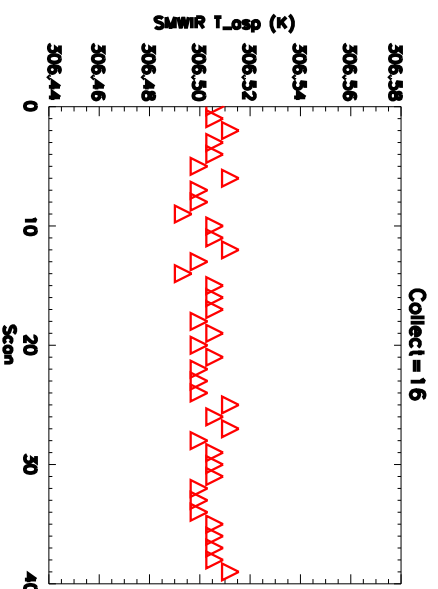
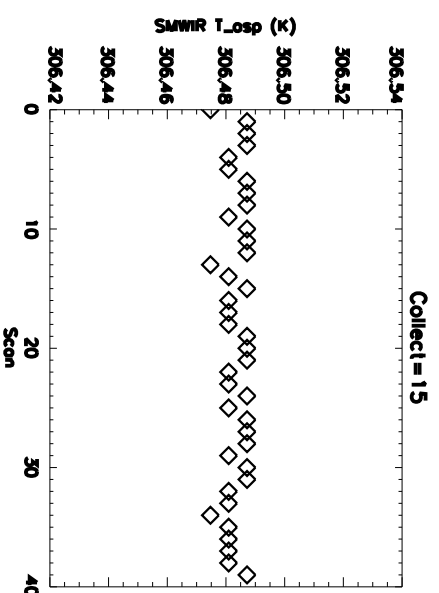
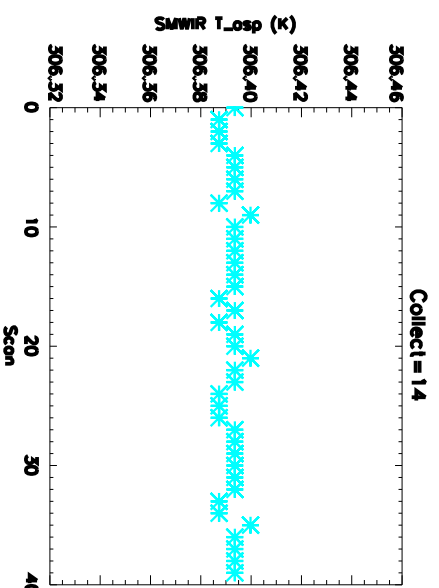
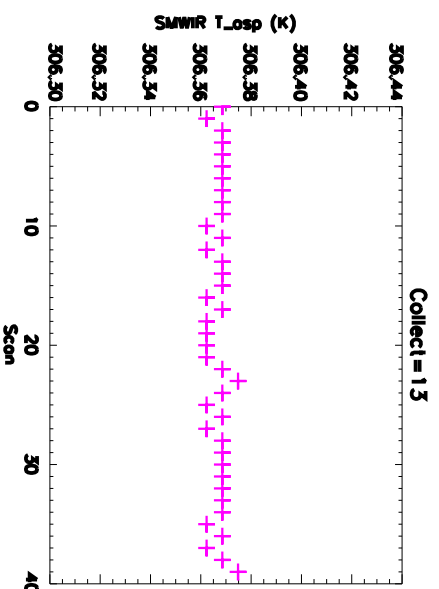
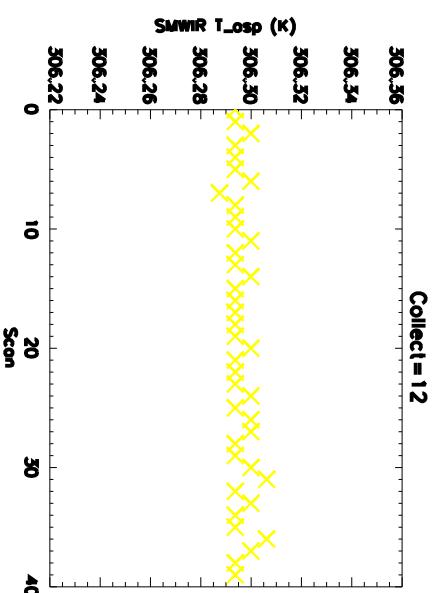
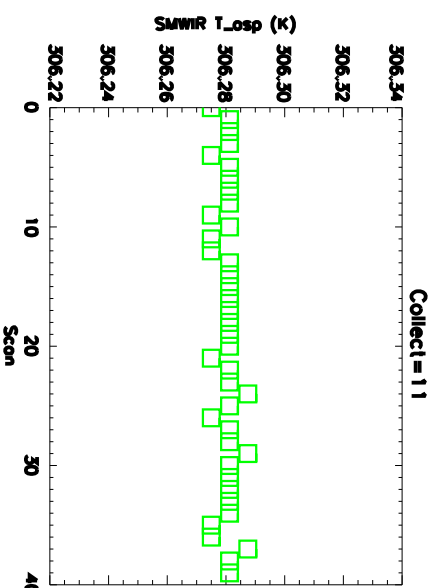
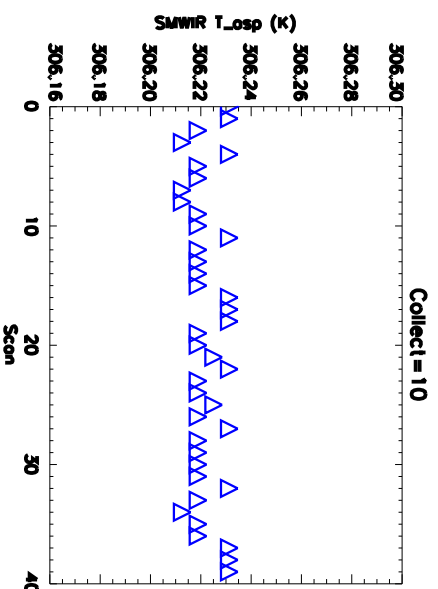
VisNIR T_osp vs Scan



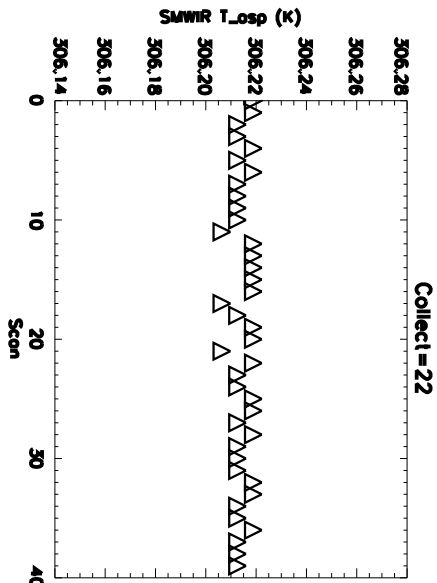
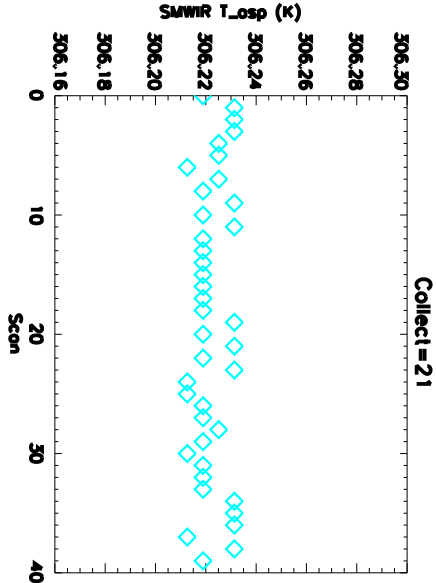
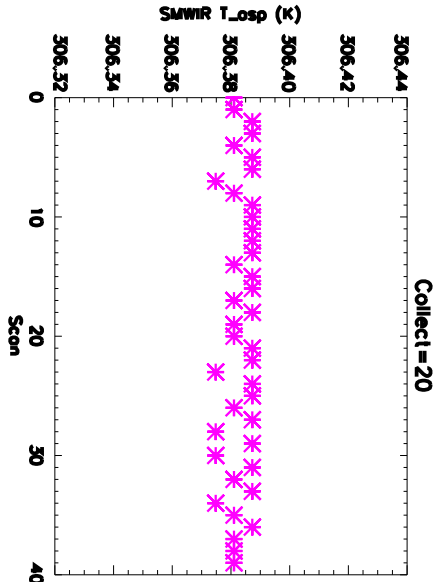
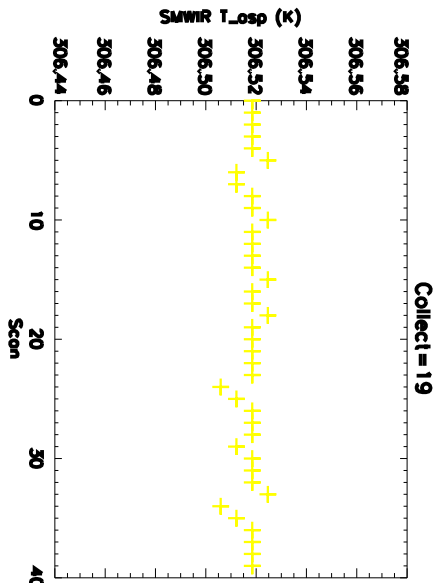
SMWIR T_asp vs Time



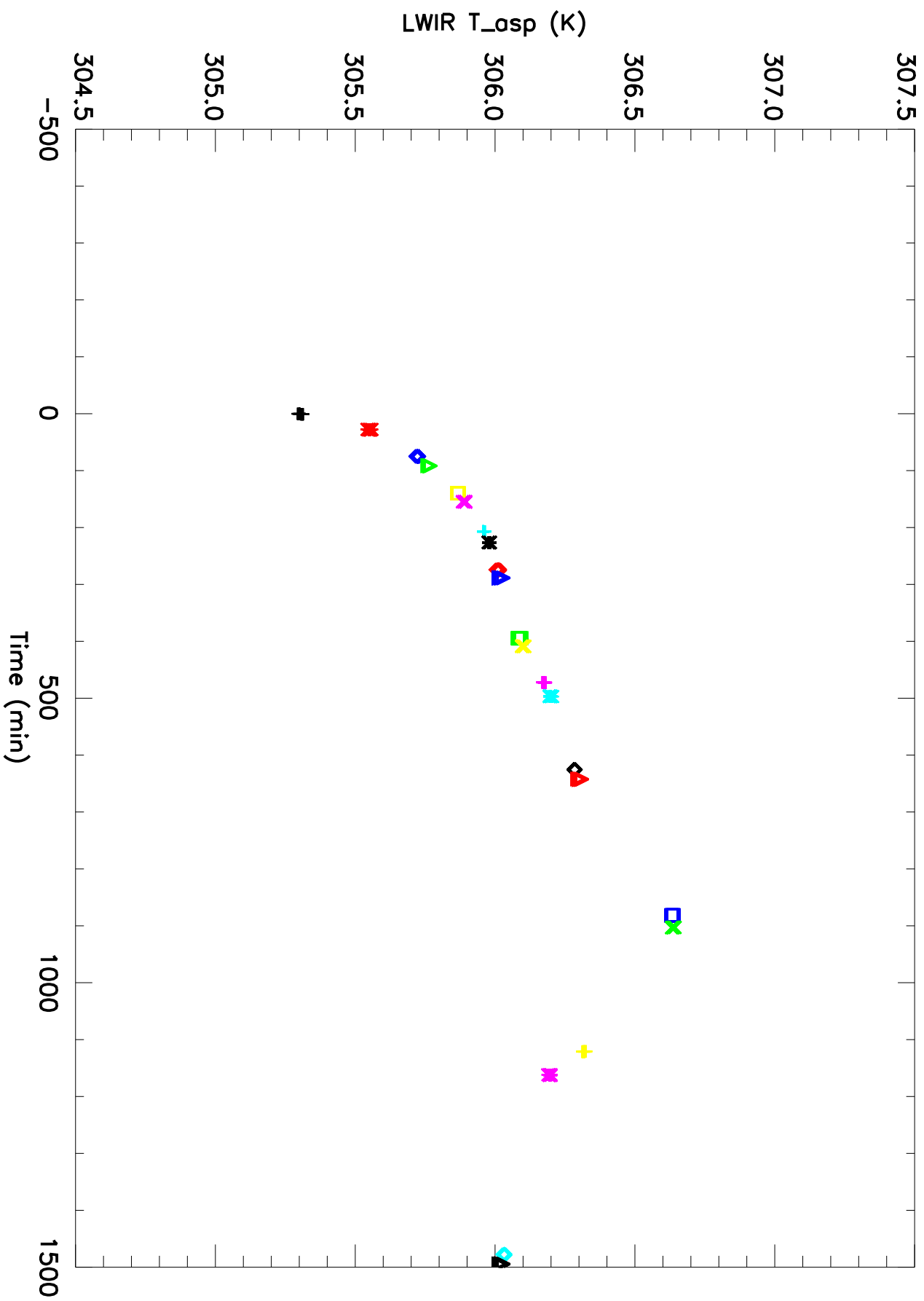


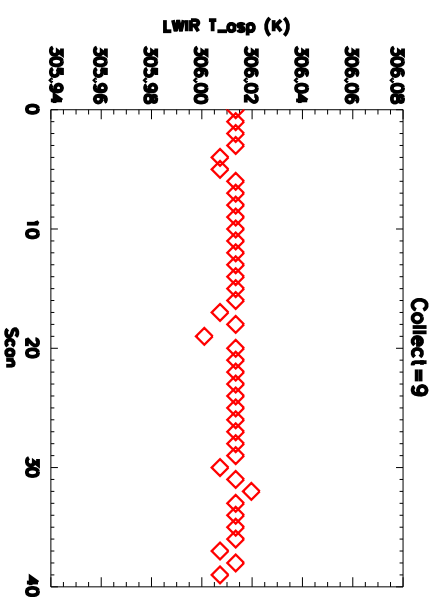
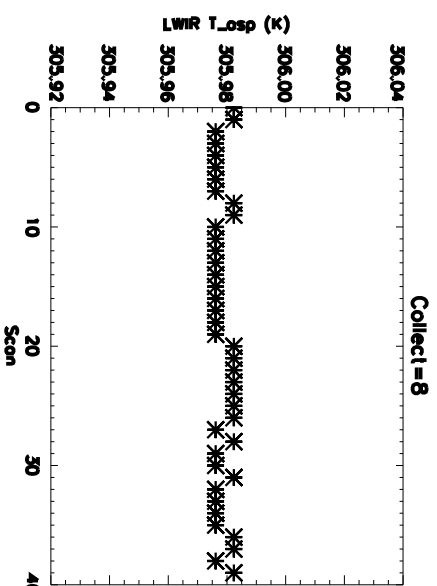
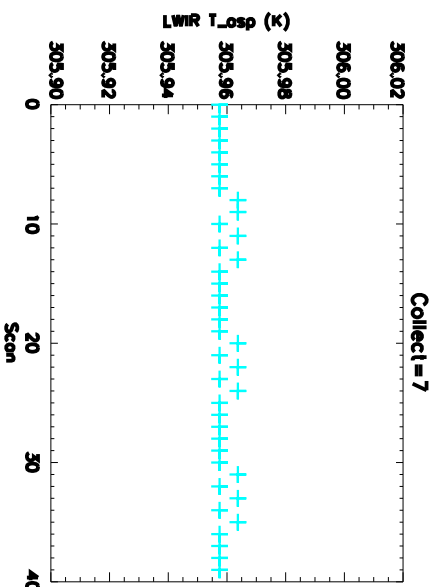
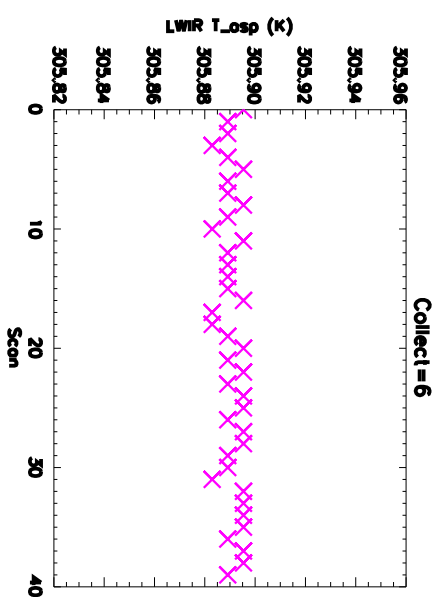
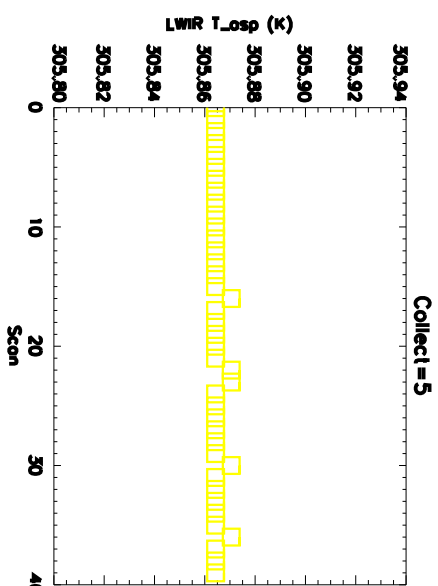
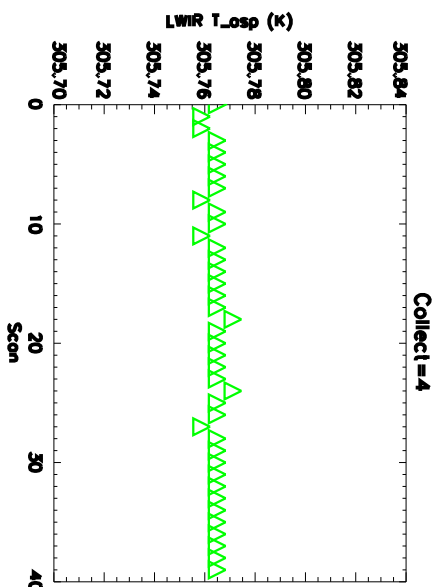
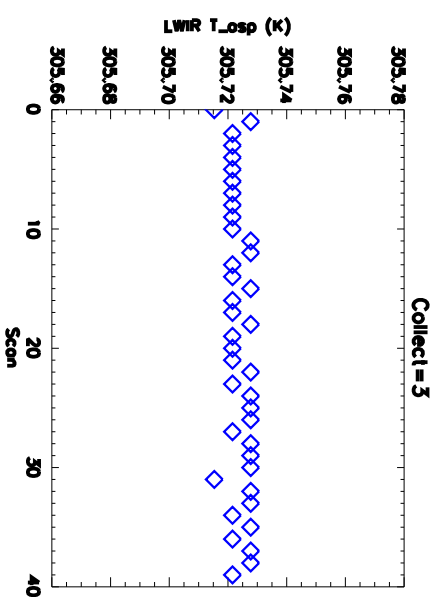
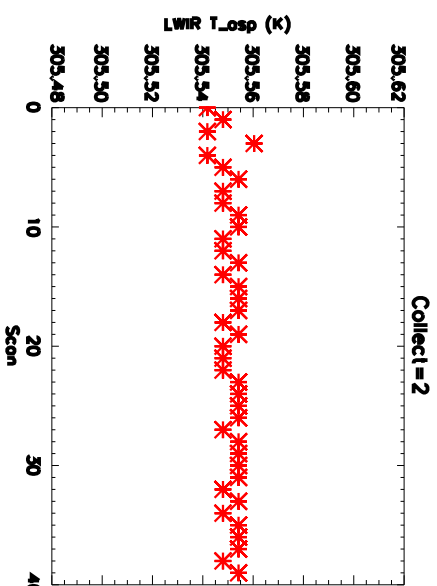
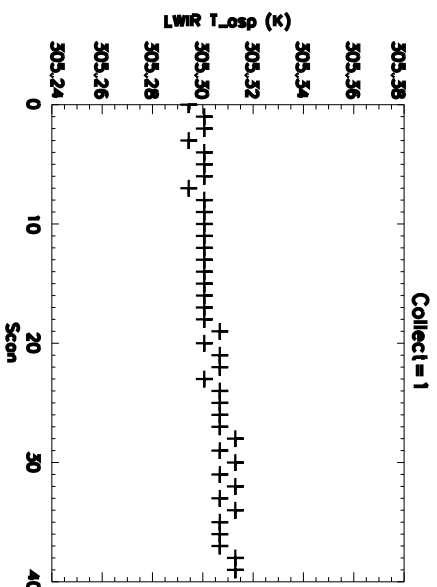


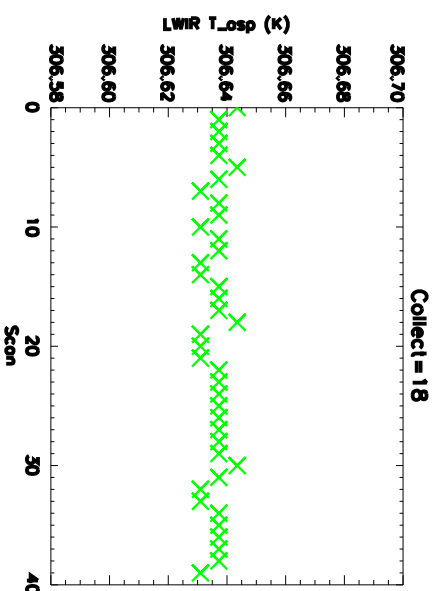
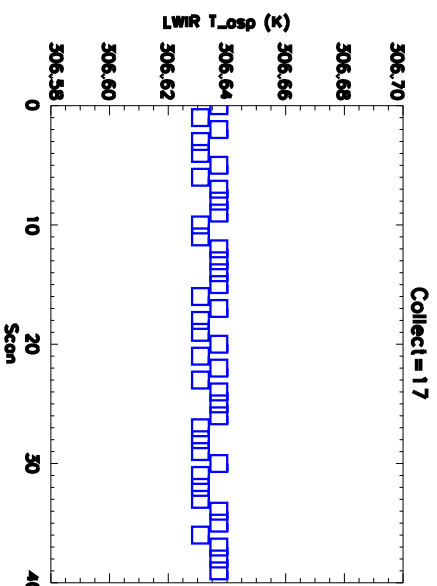
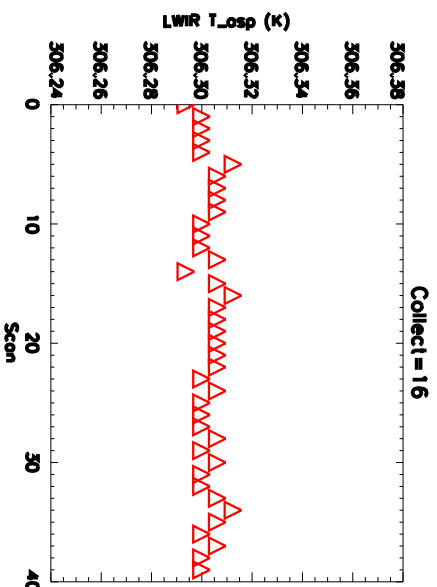
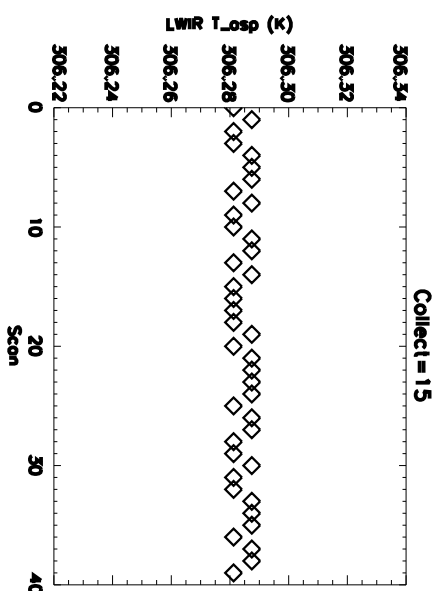
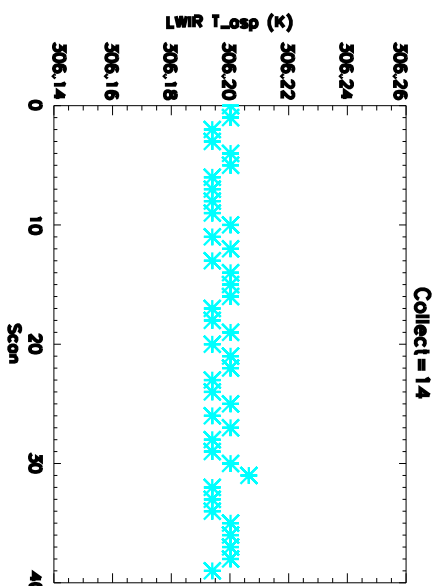
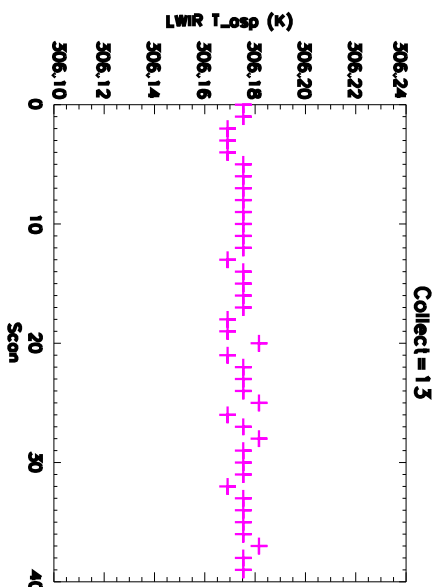
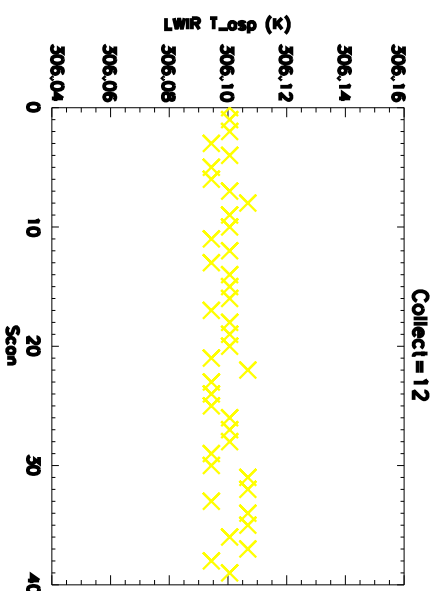
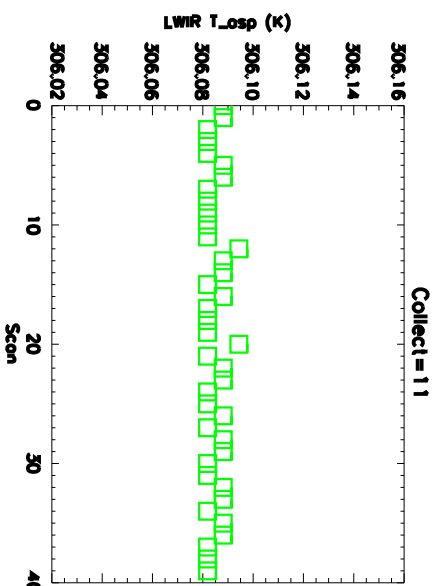
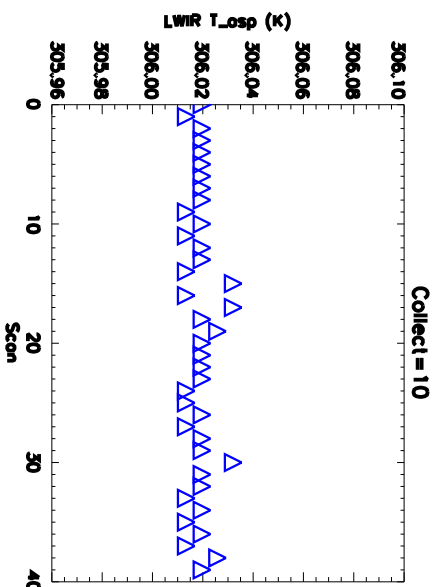
SMWIR T_osp vs Scan



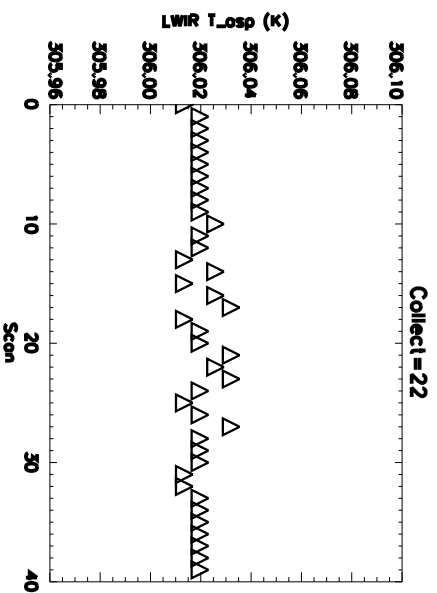
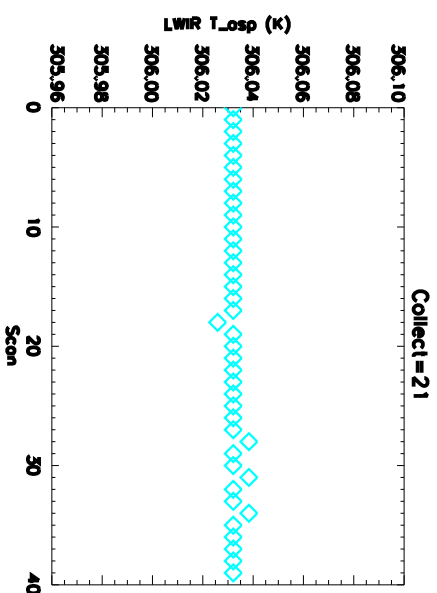
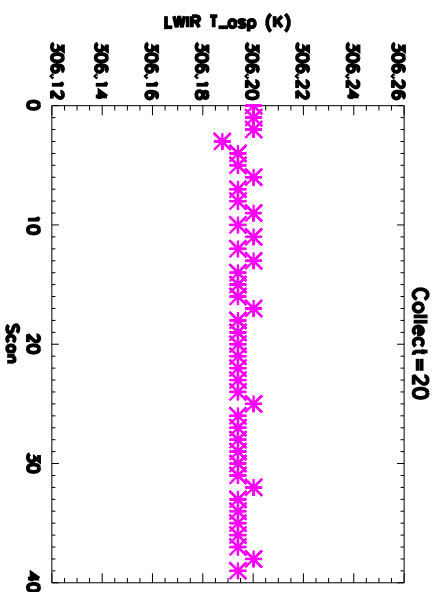
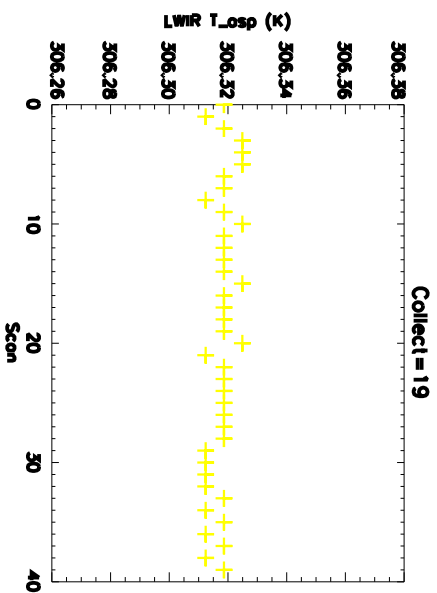
LWIR T_{osp} vs Time





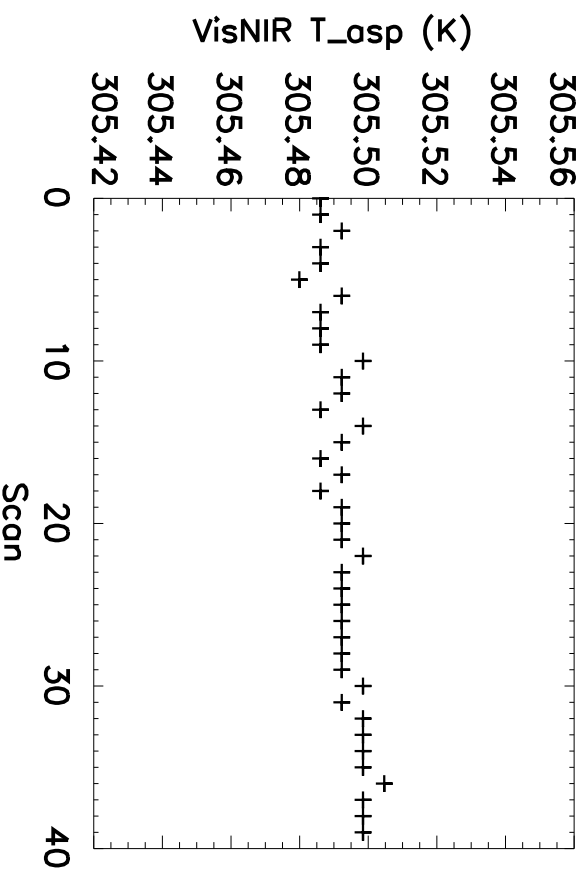


LWIR T_osp vs Scan

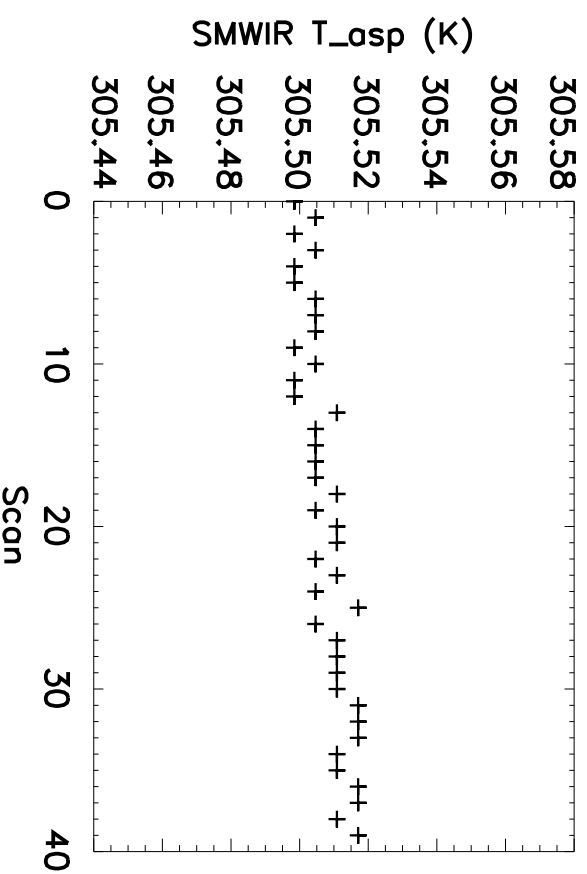


T_osp vs Scan: Collect=1

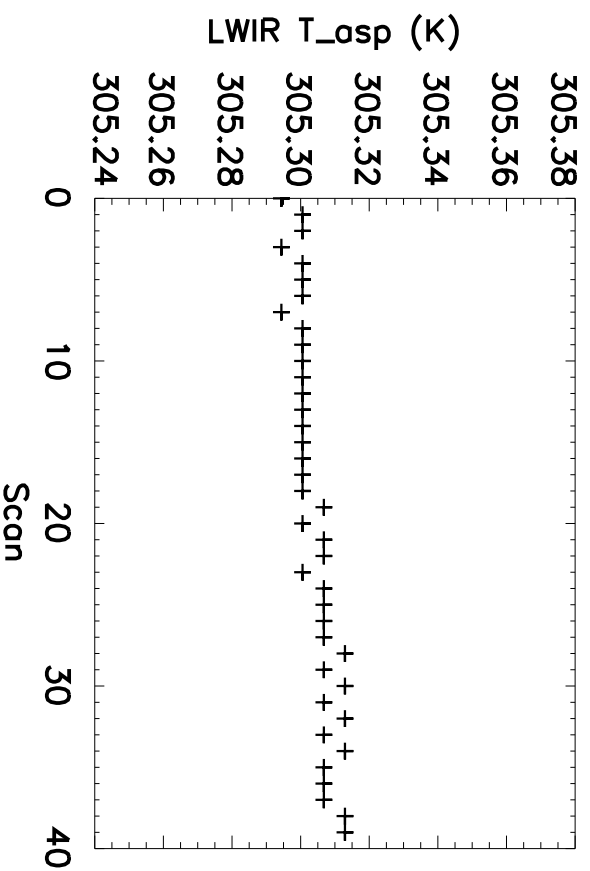
VisNIR



SMWIR

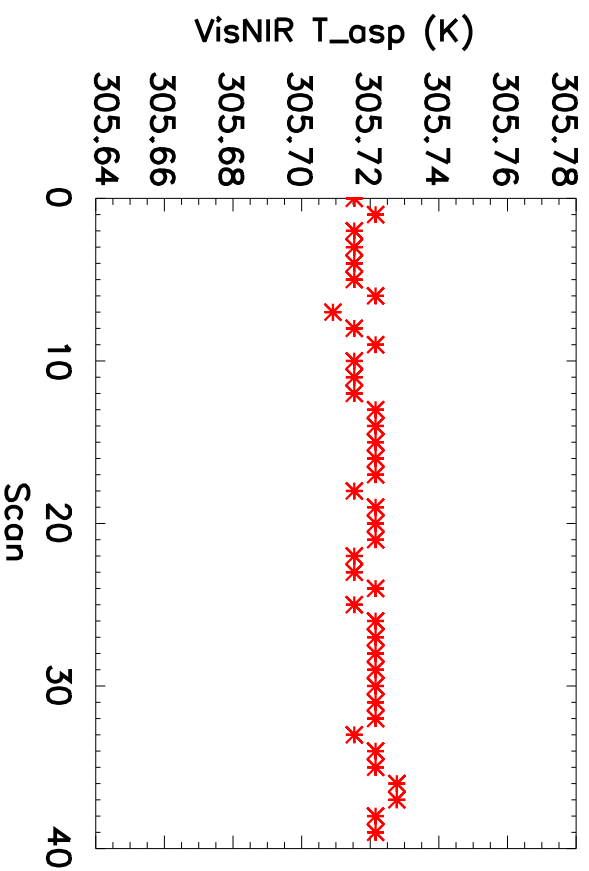


LWIR

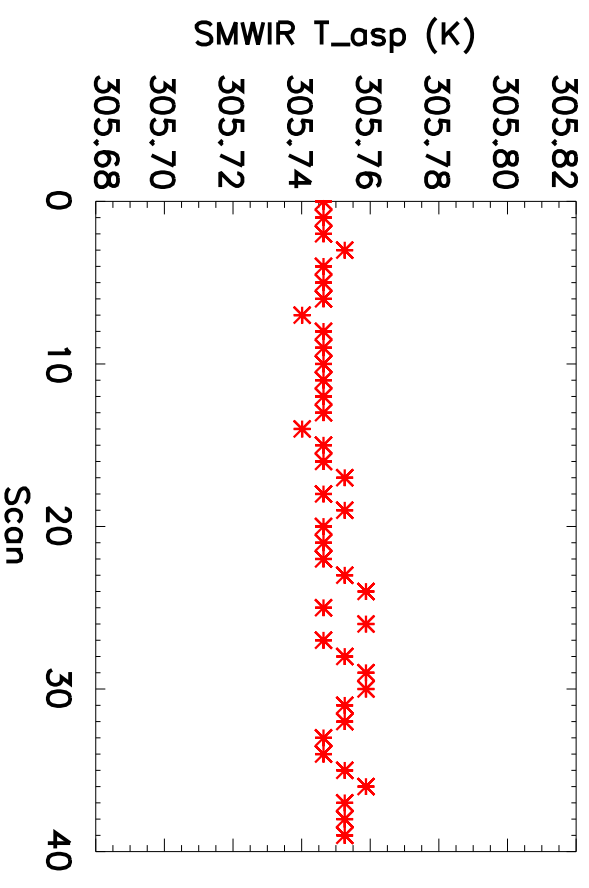


T_osp vs Scan: Collect=2

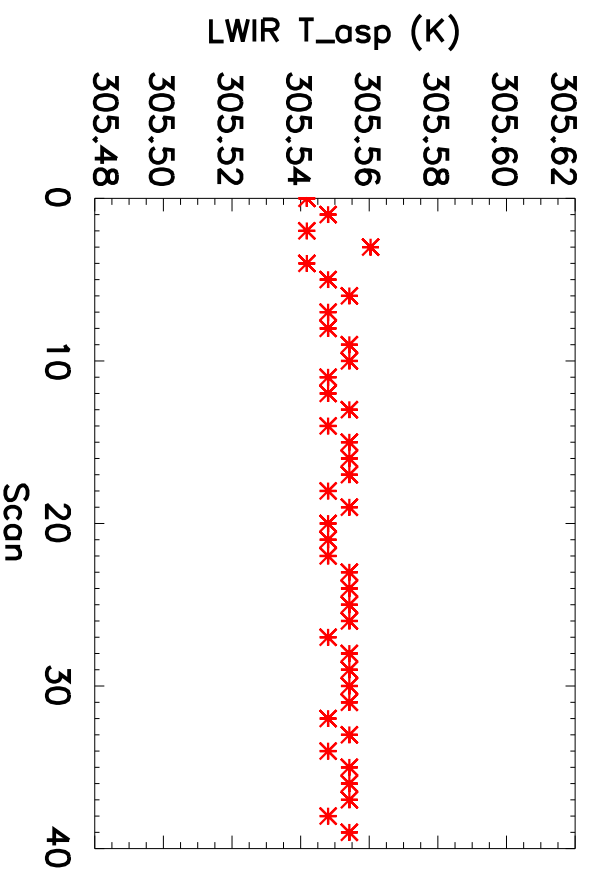
VisNIR



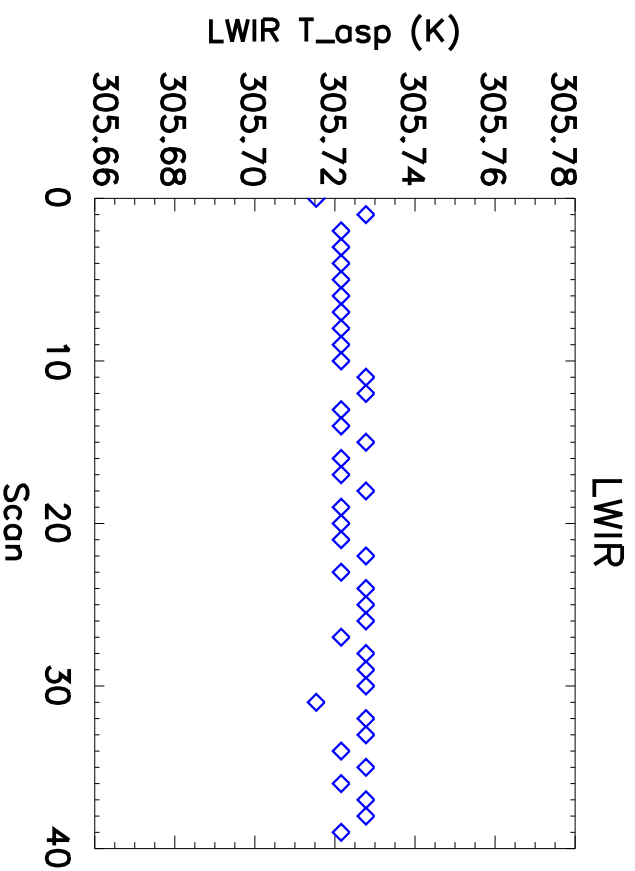
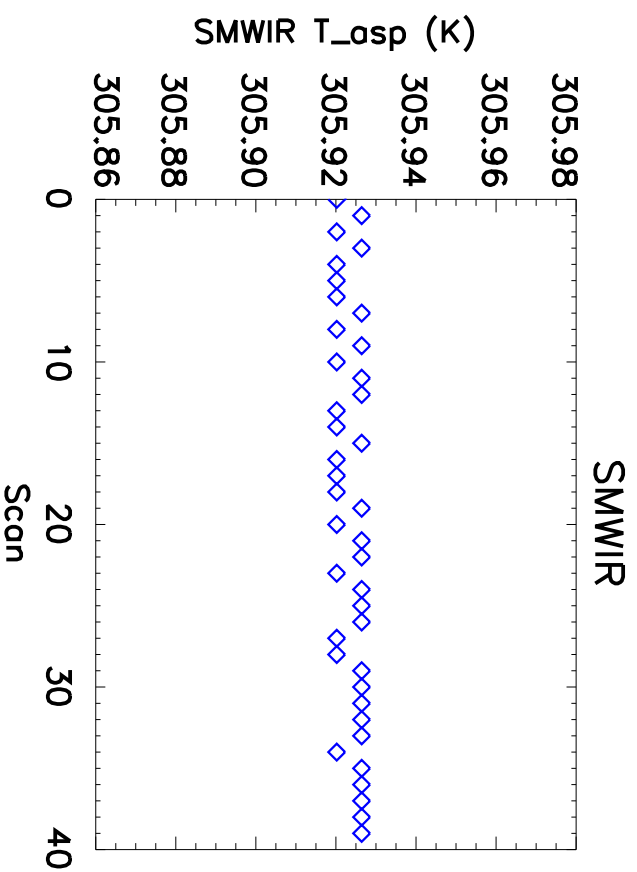
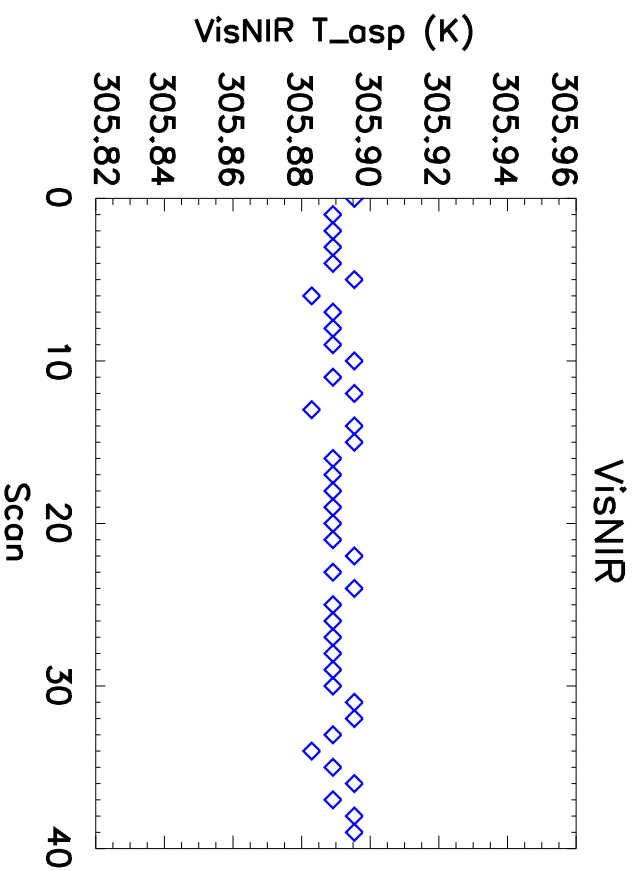
SMWIR



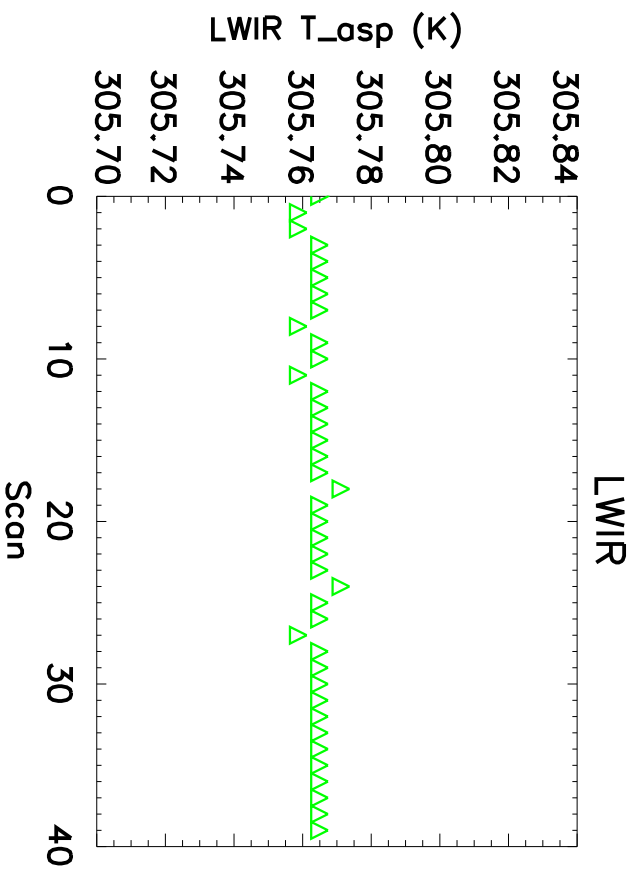
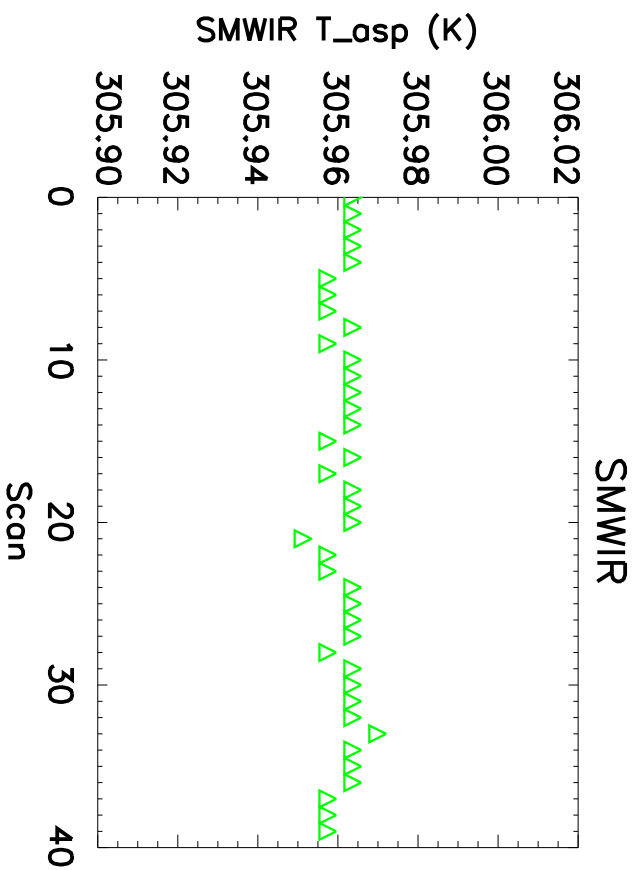
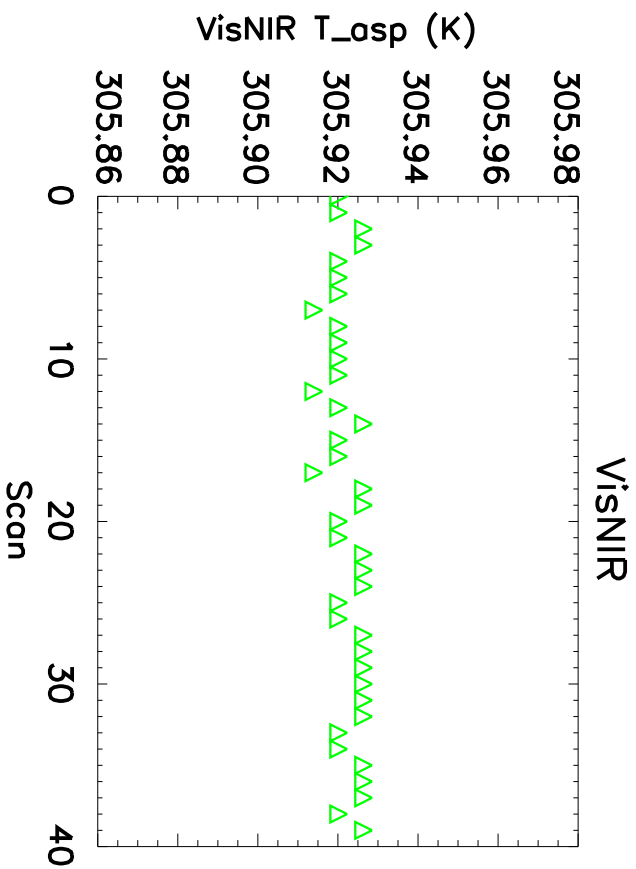
LWIR



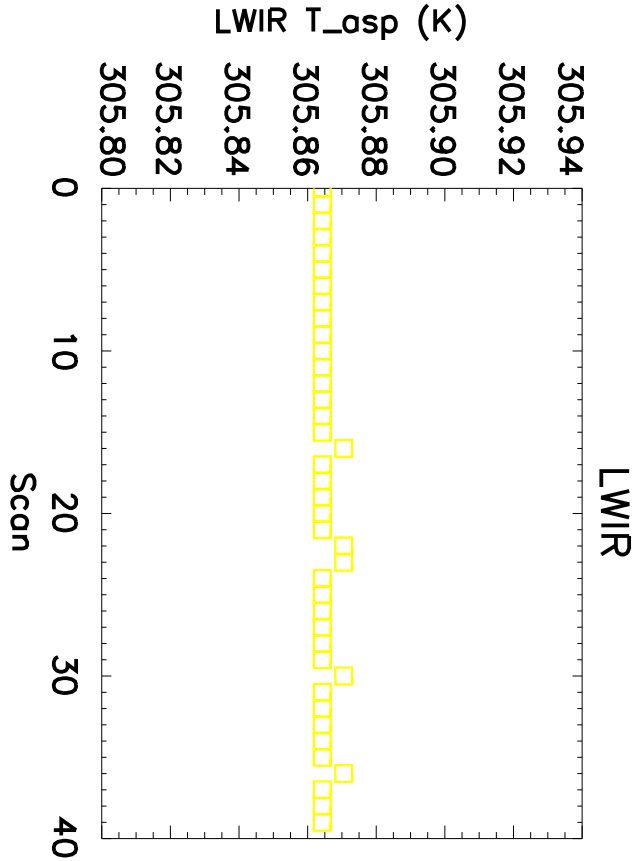
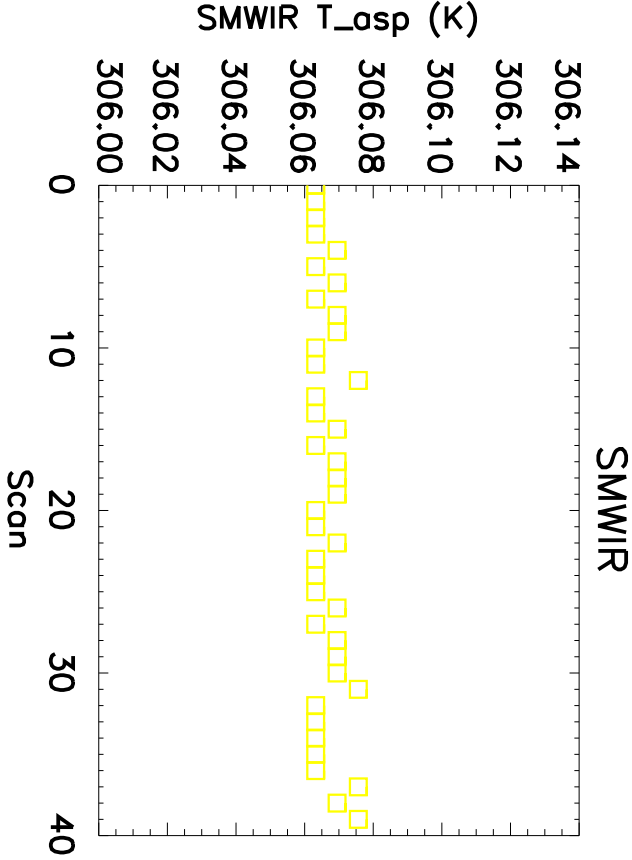
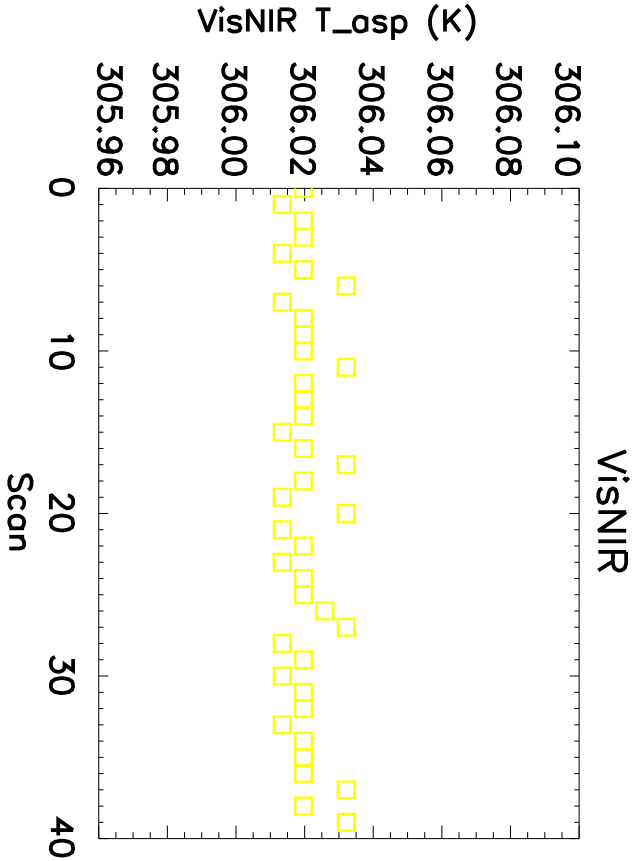
T_osp vs Scan: Collect=3



T_osp vs Scan: Collect=4

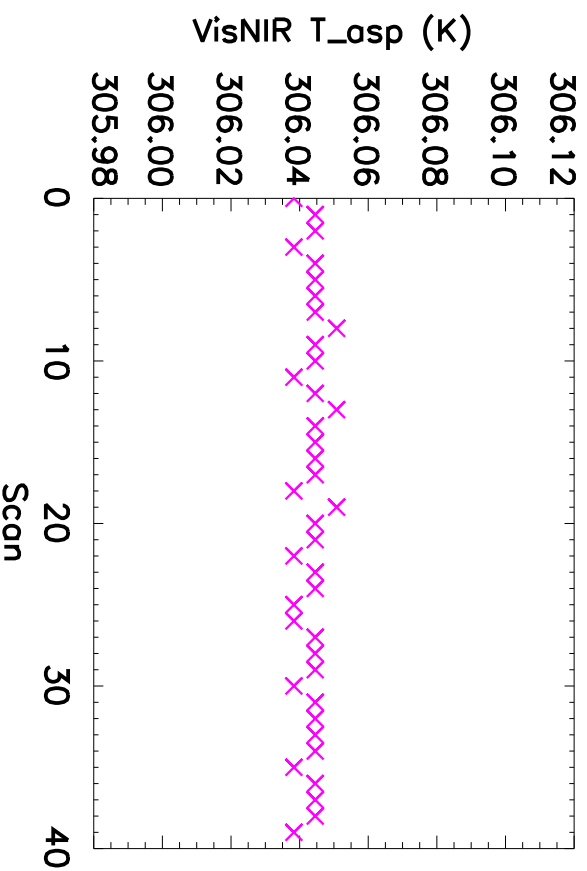


T_osp vs Scan: Collect=5

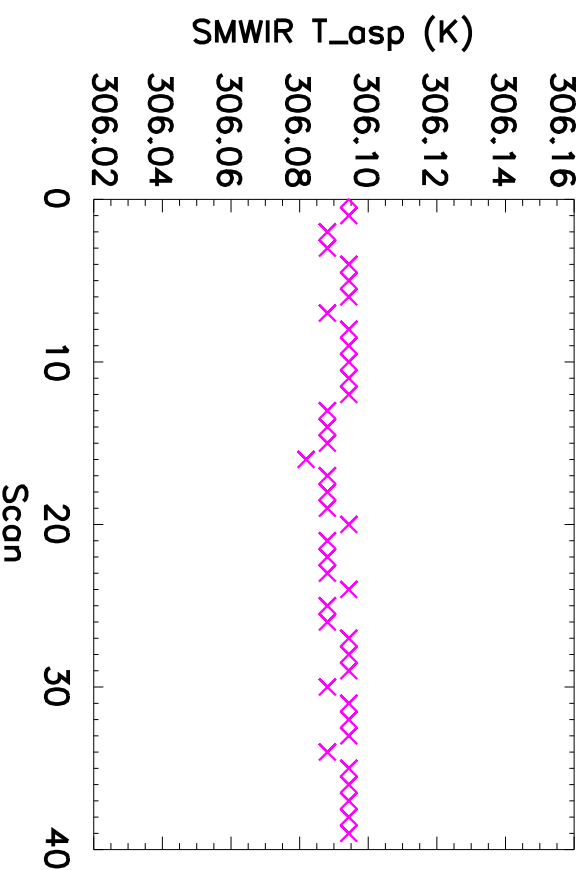


T_osp vs Scan: Collect=6

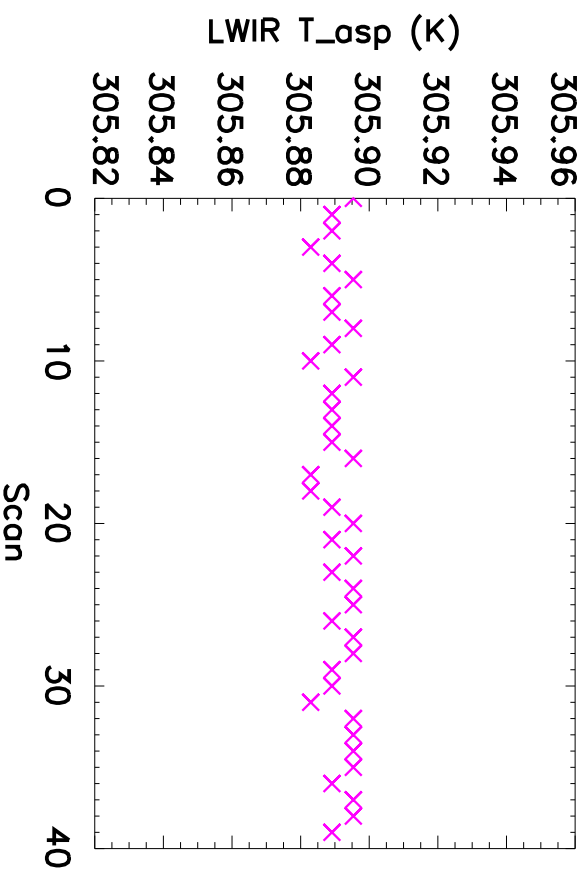
VisNIR



SMWIR

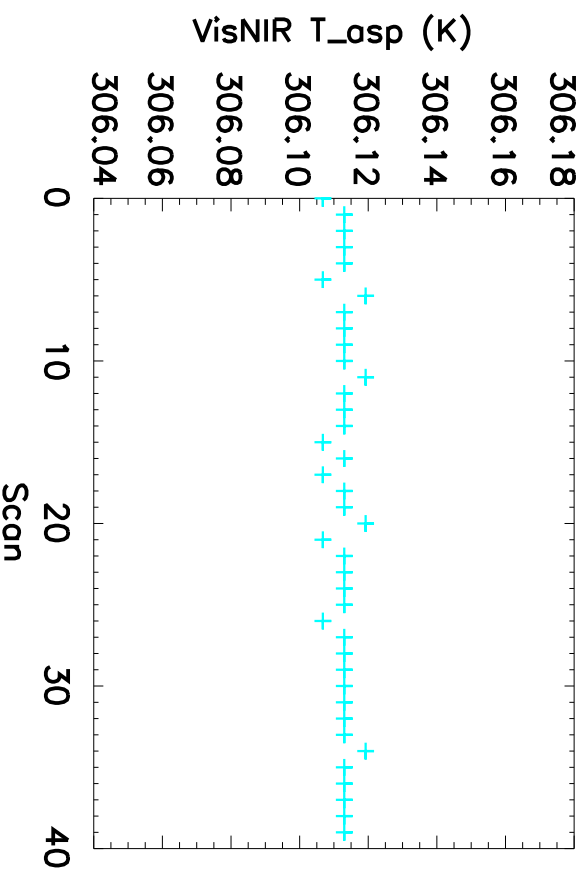


LWIR

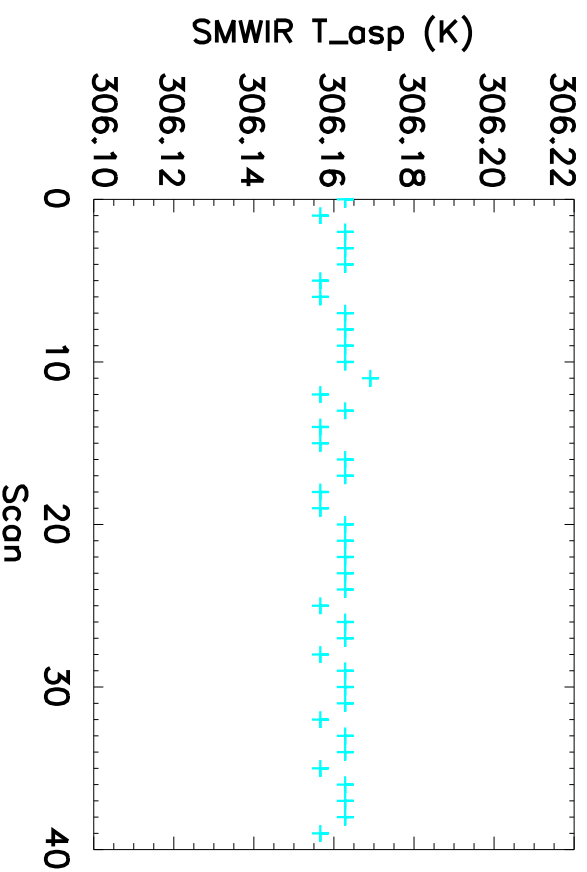


T_osp vs Scan: Collect=7

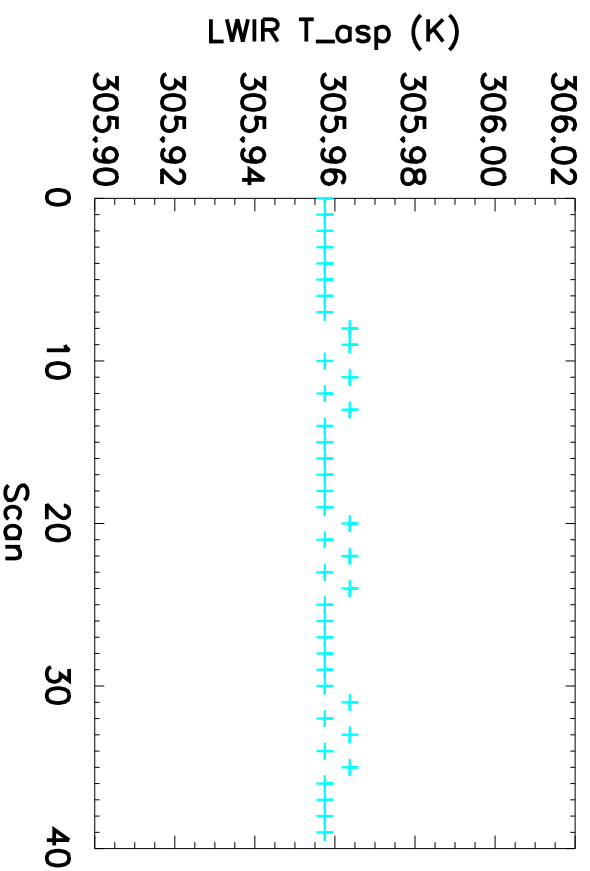
VisNIR



SMWIR

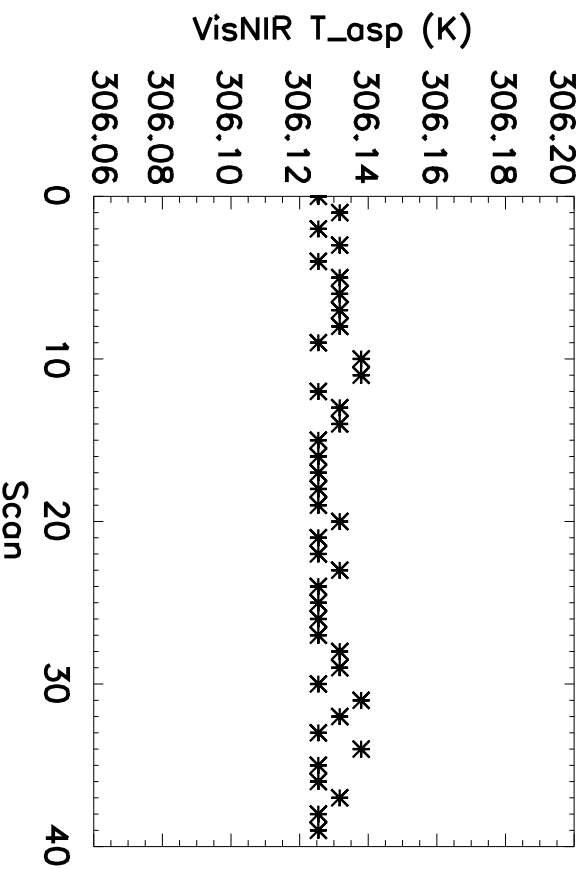


LWIR

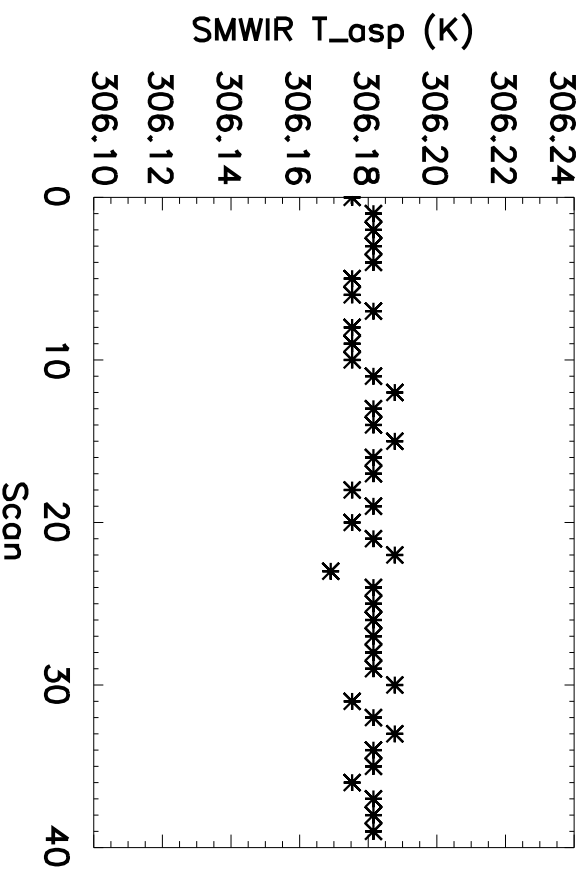


T_osp vs Scan: Collect=8

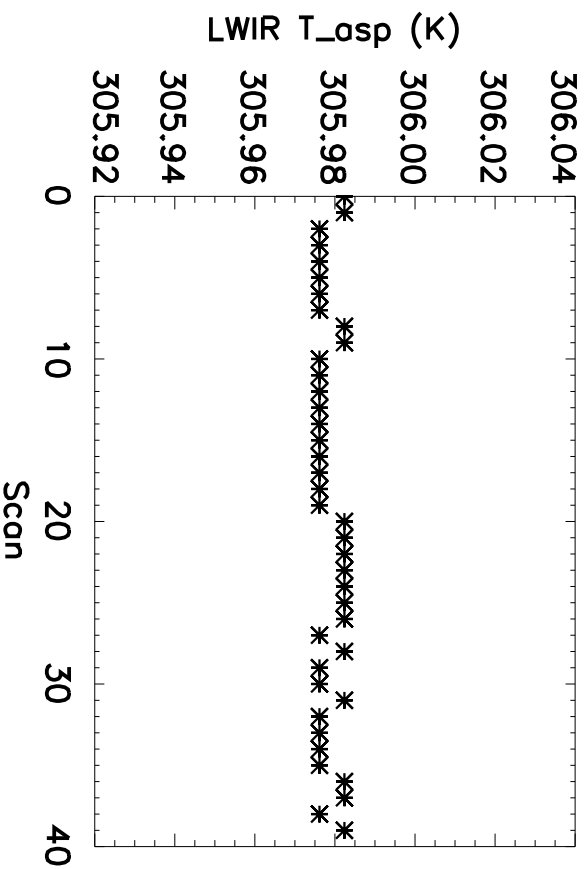
VisNIR



SMWIR

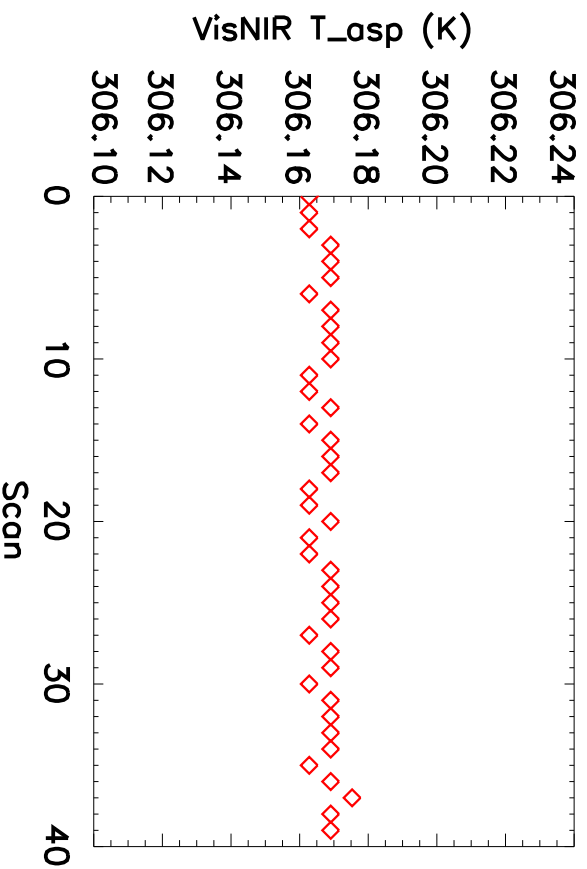


LWIR

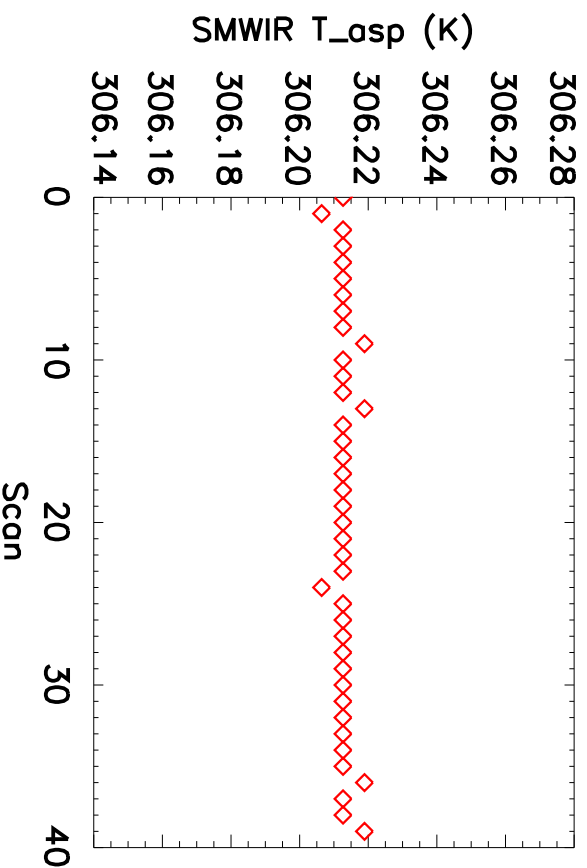


T_osp vs Scan: Collect=9

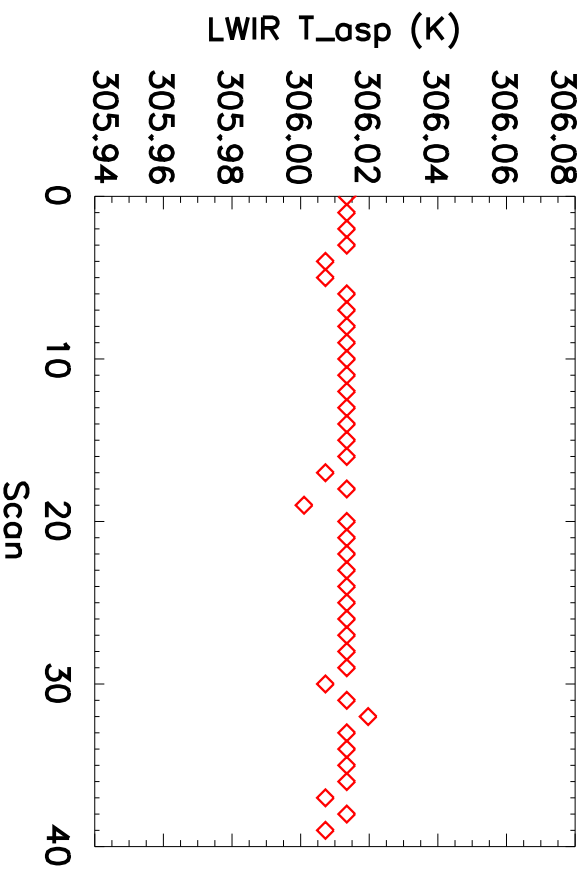
VisNIR



SMWIR

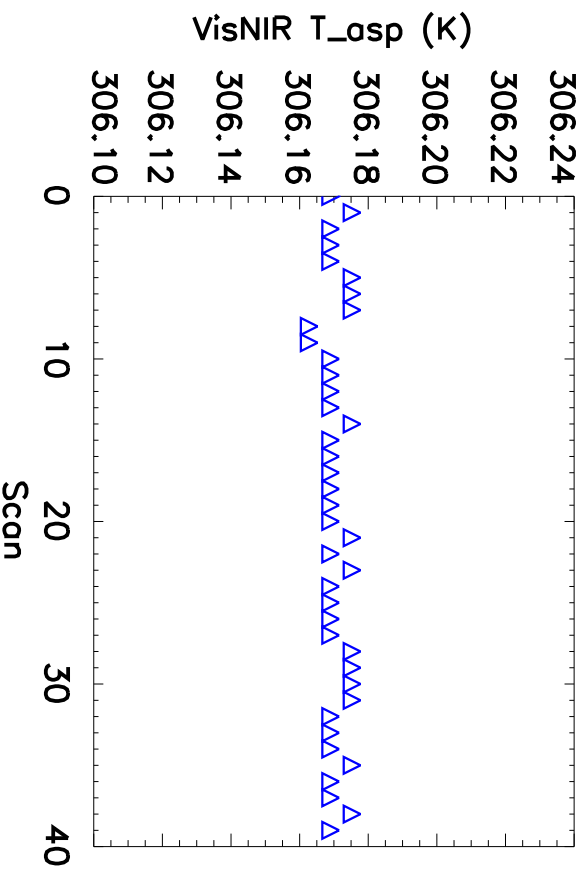


LWIR

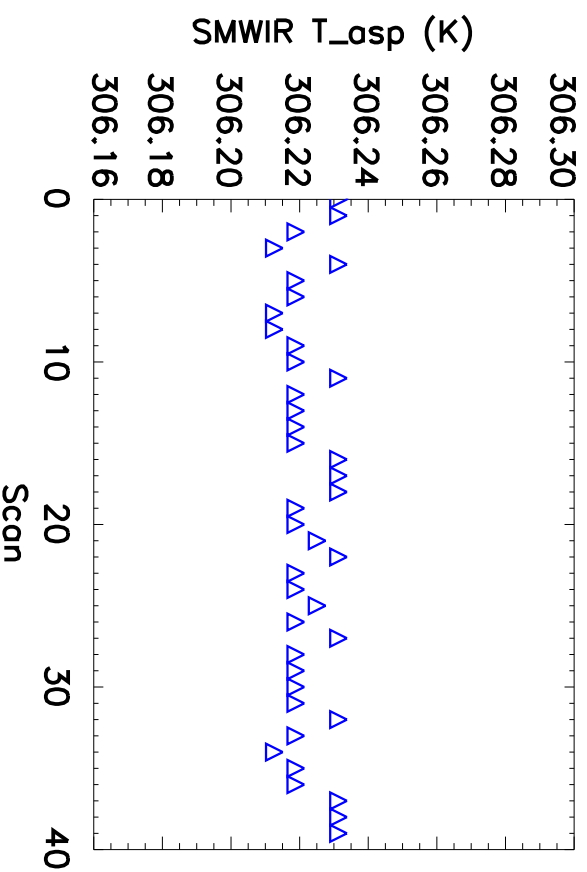


T_osp vs Scan: Collect=10

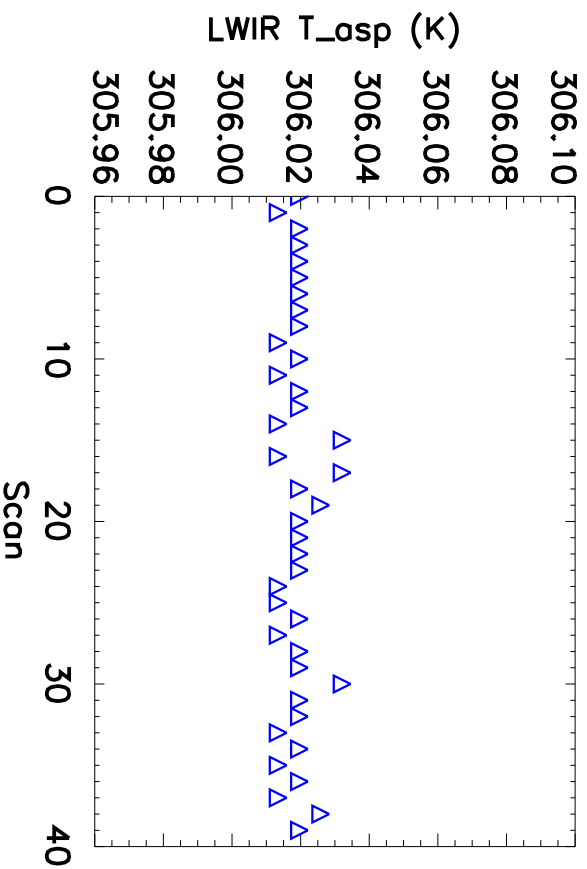
VisNIR



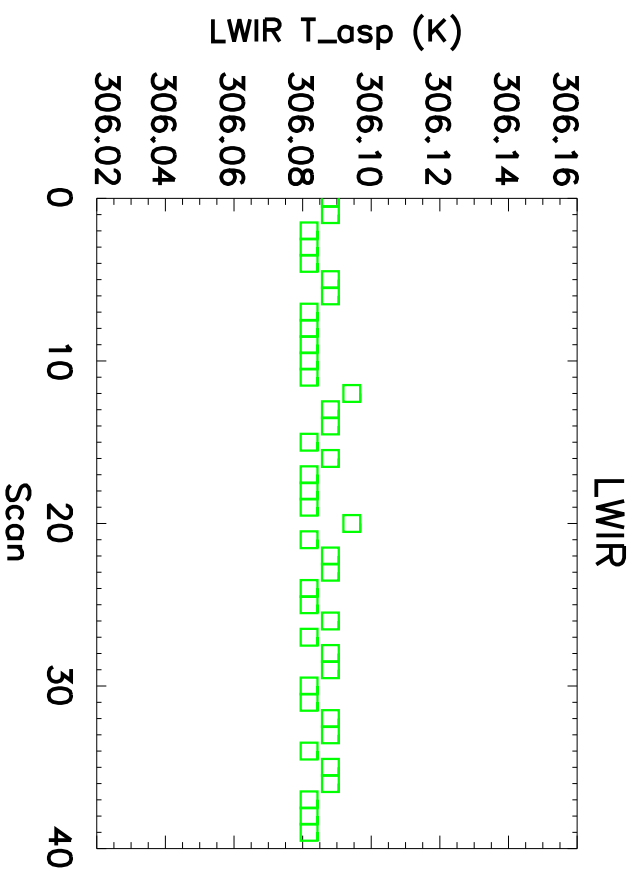
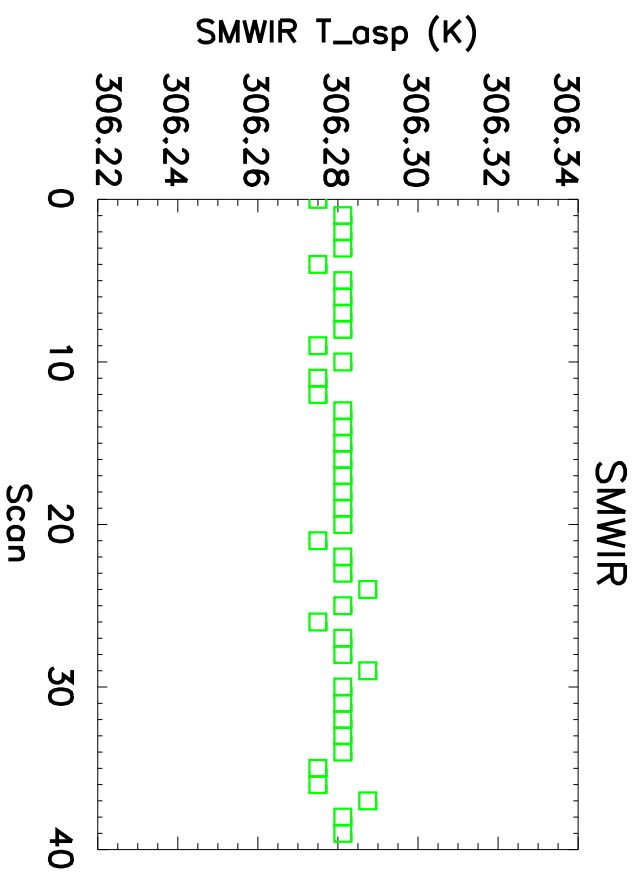
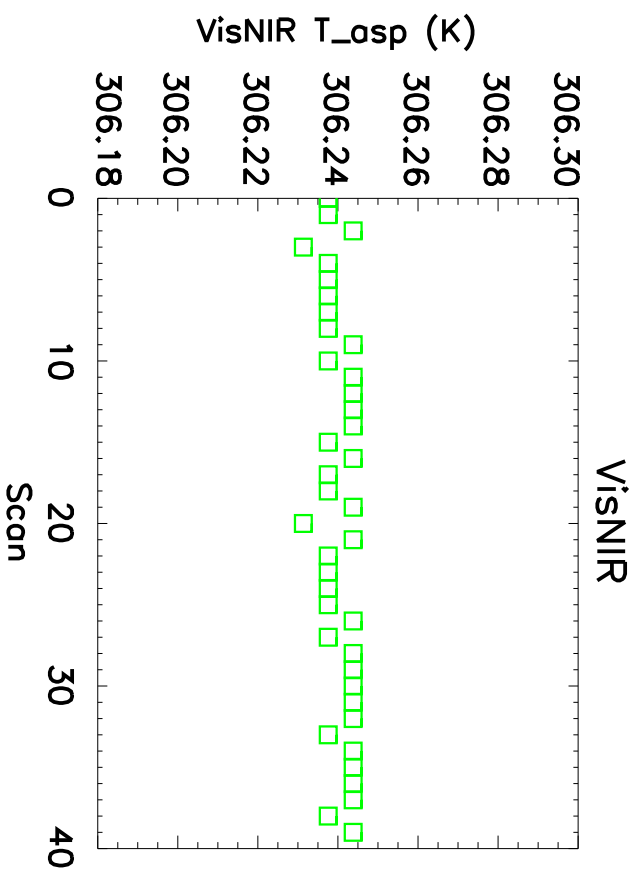
SMWIR



LWIR

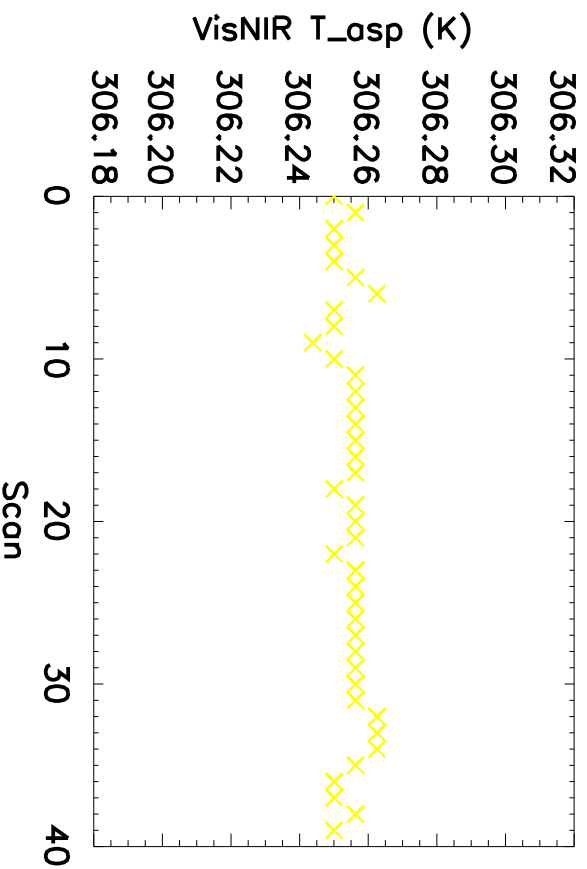


T_osp vs Scan: Collect=1 1

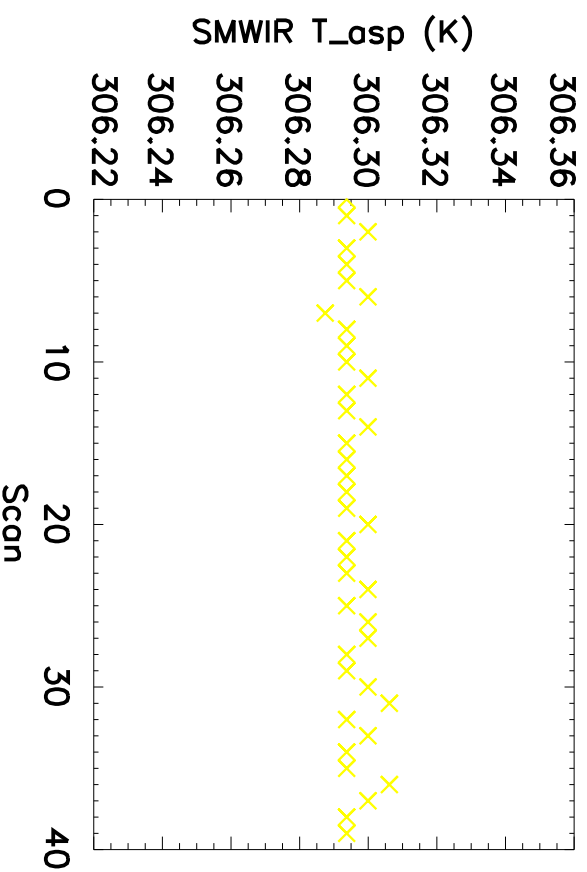


T_osp vs Scan: Collect=12

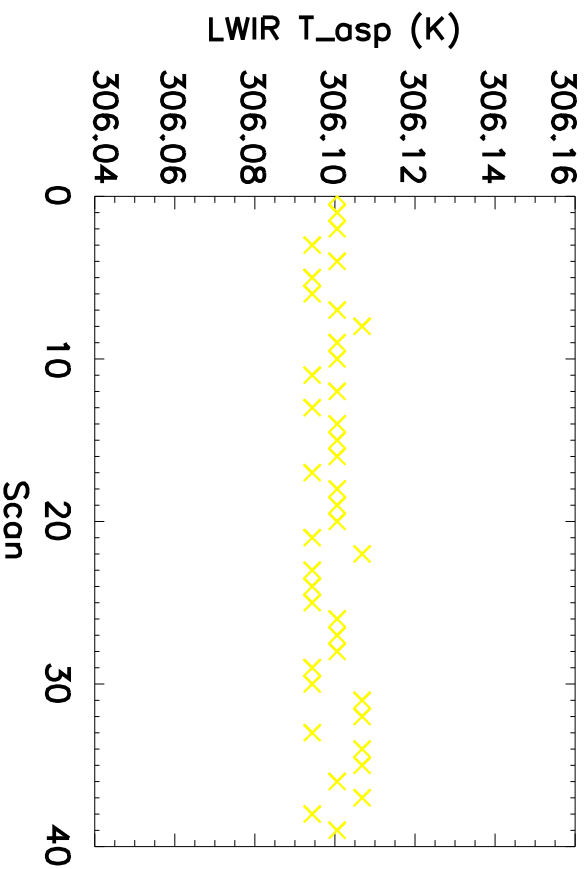
VisNIR



SMWIR

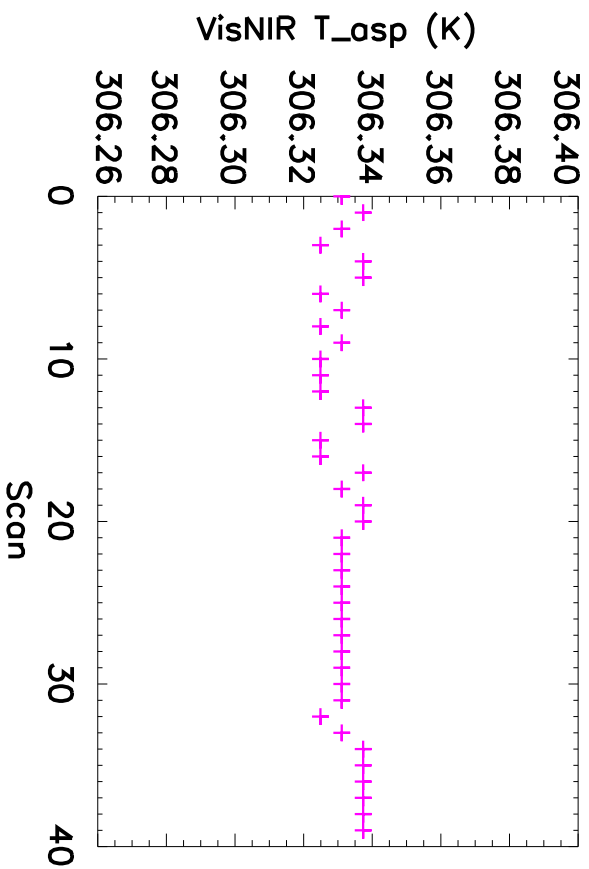


LWIR

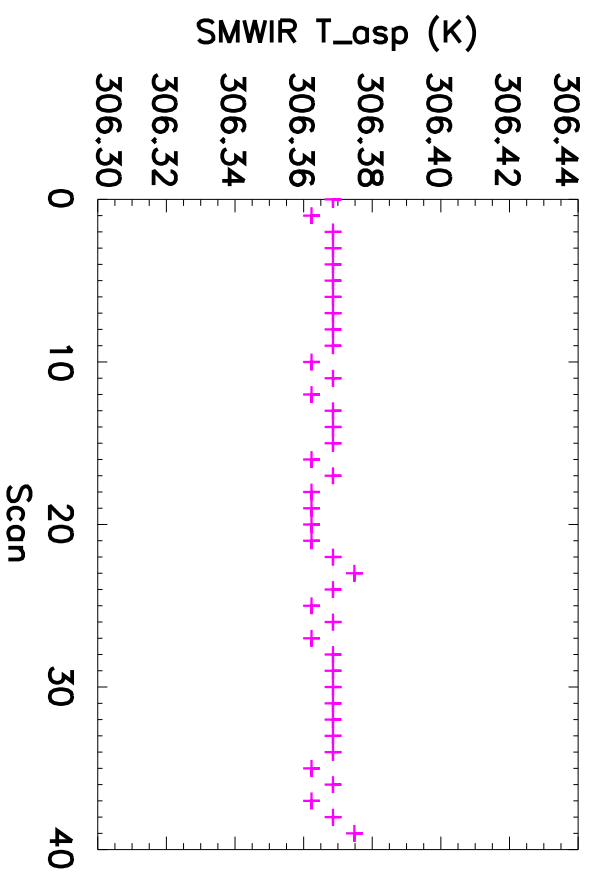


T_osp vs Scan: Collect=13

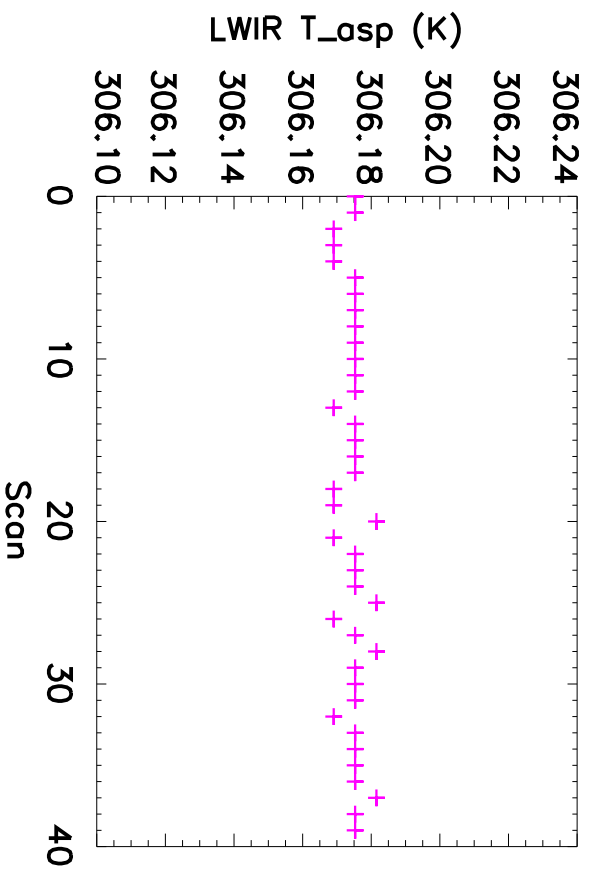
VisNIR



SMWIR

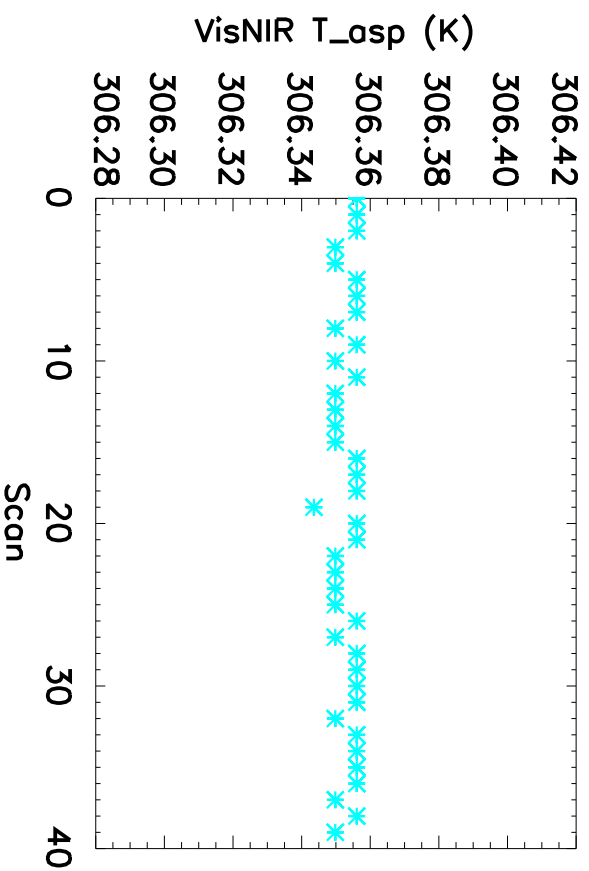


LWIR

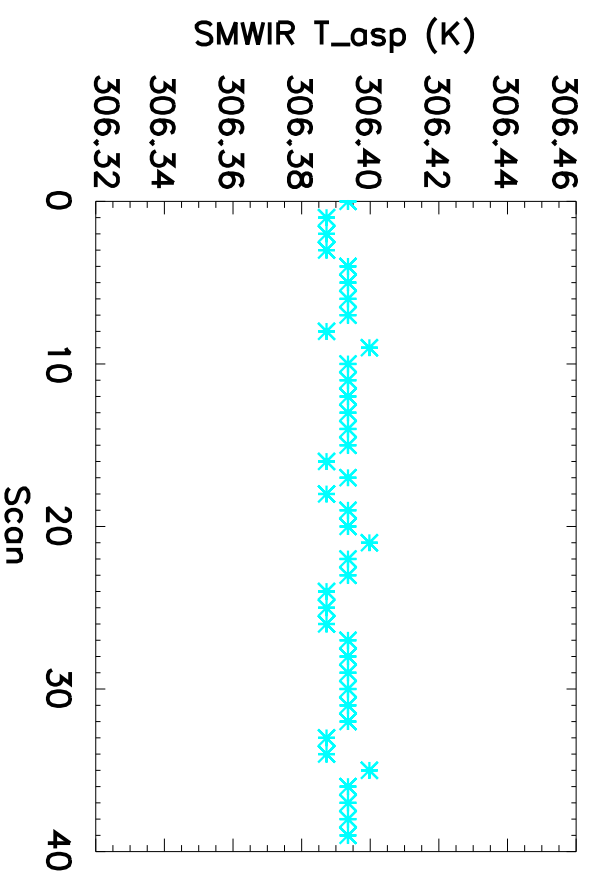


T_osp vs Scan: Collect=14

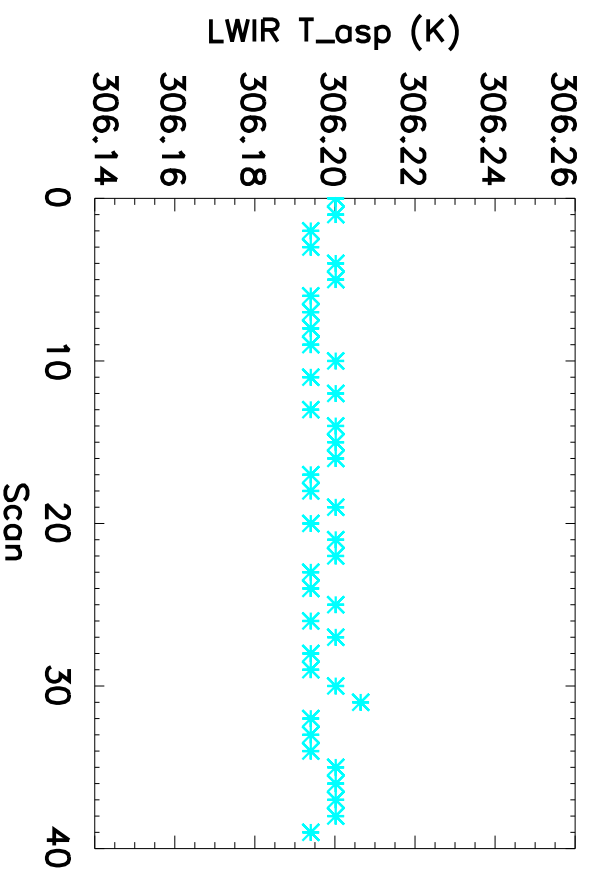
VisNIR



SMWIR

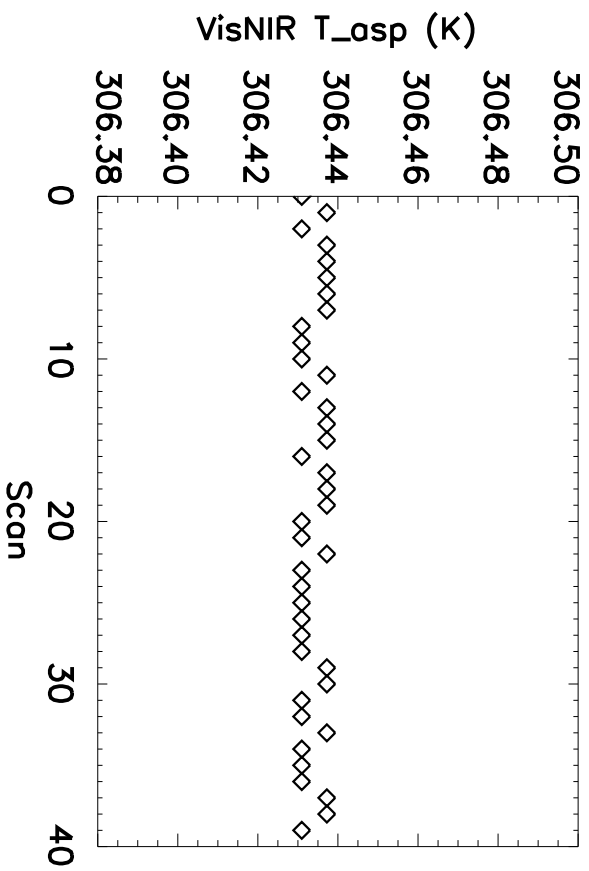


LWIR

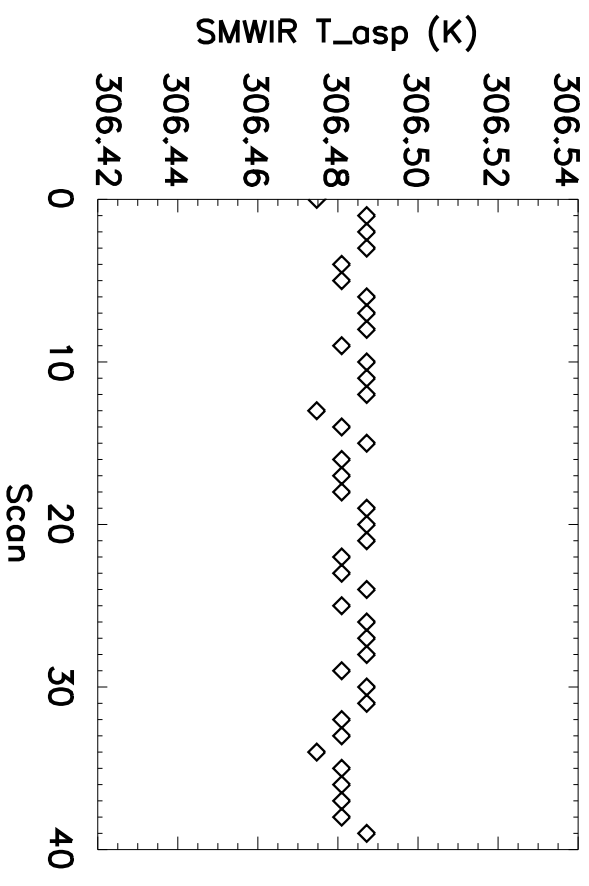


T_osp vs Scan: Collect=15

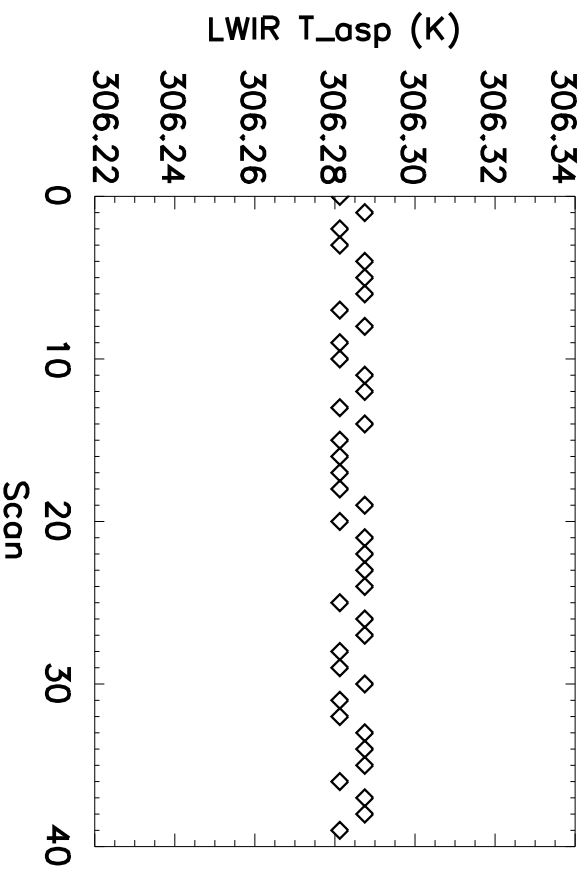
VisNIR



SMWIR

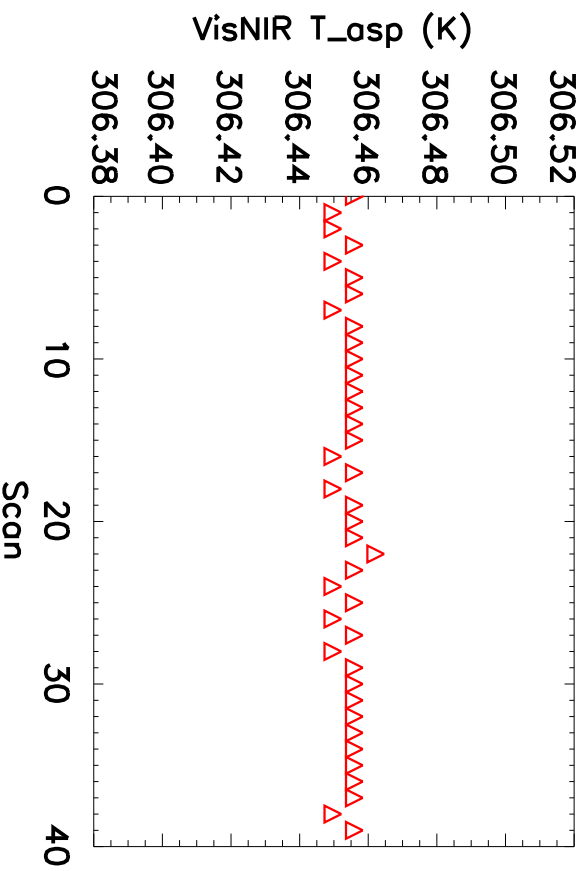


LWIR

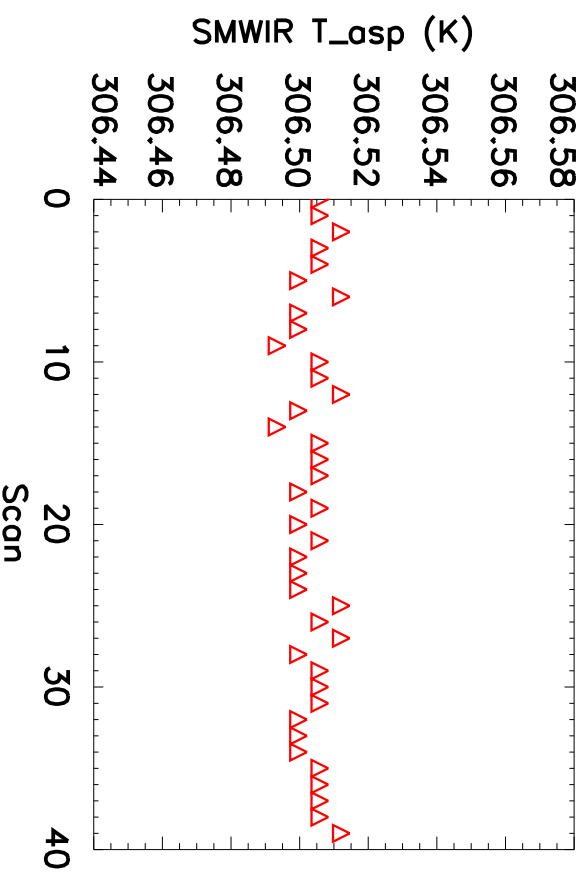


T_osp vs Scan: Collect=16

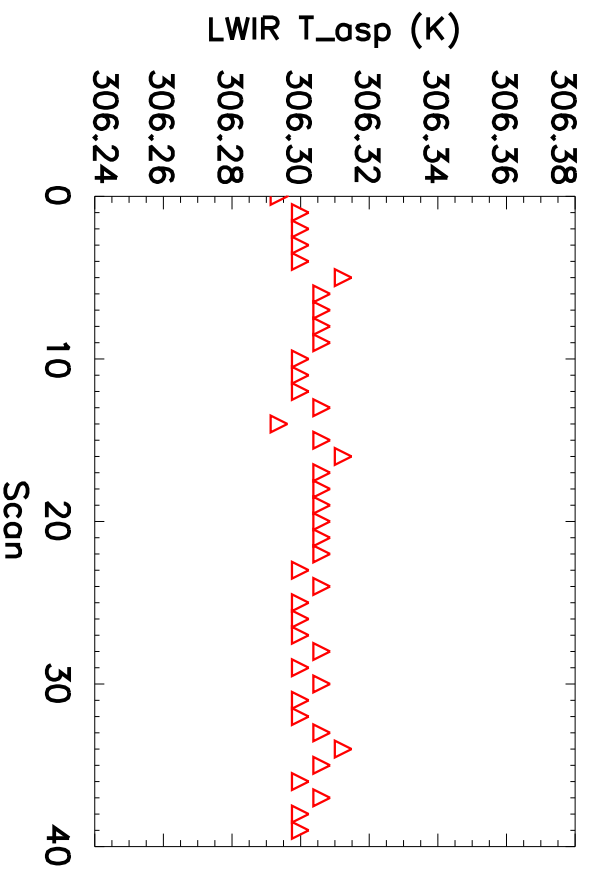
VisNIR



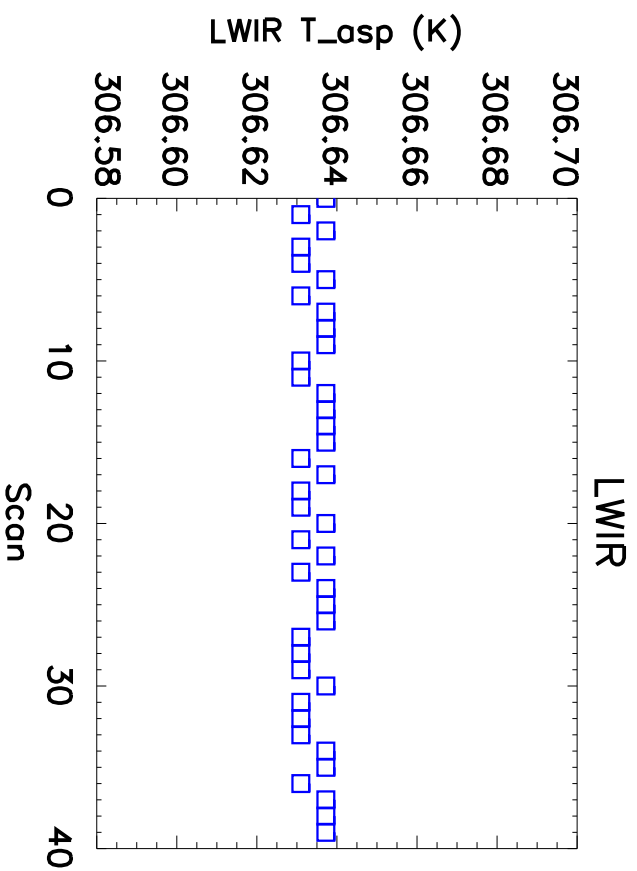
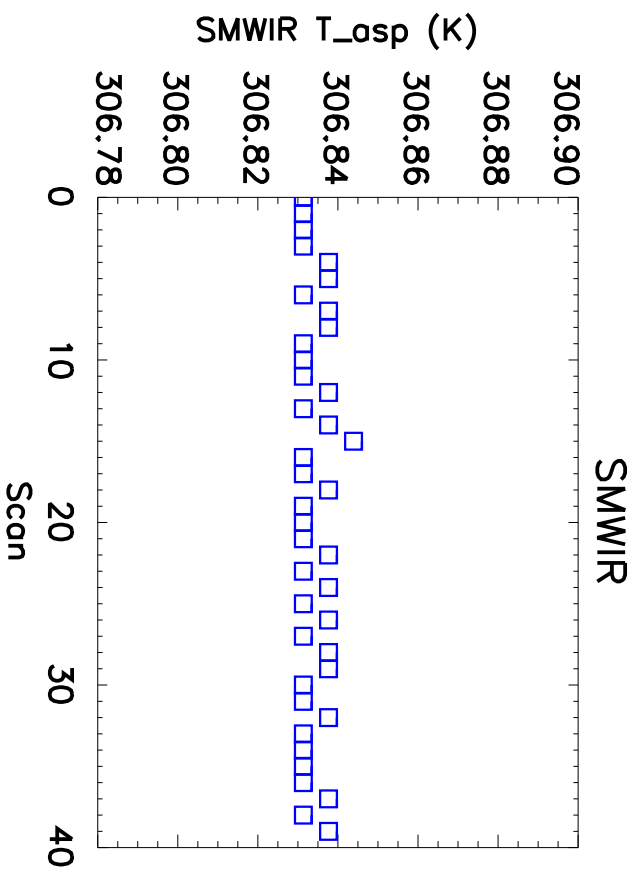
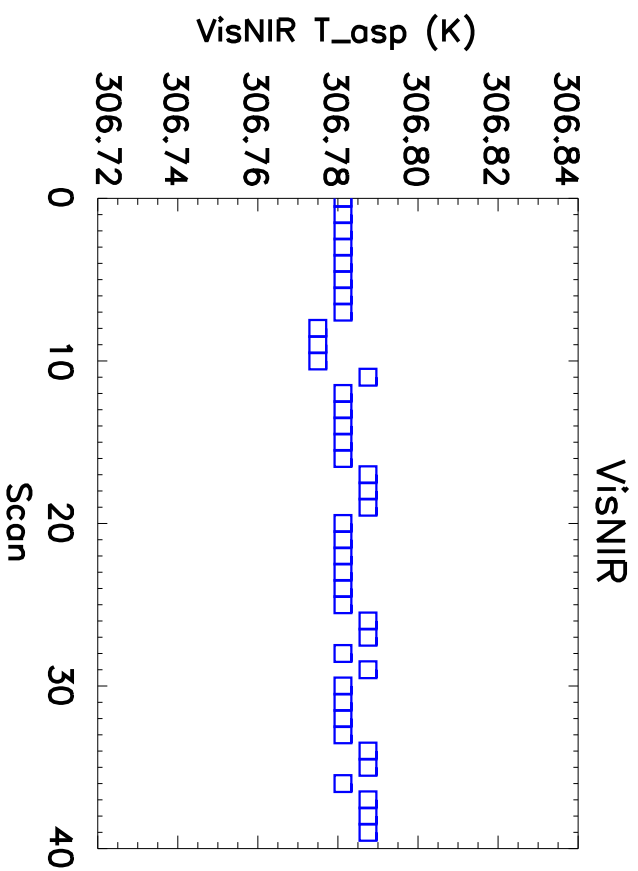
SMWIR



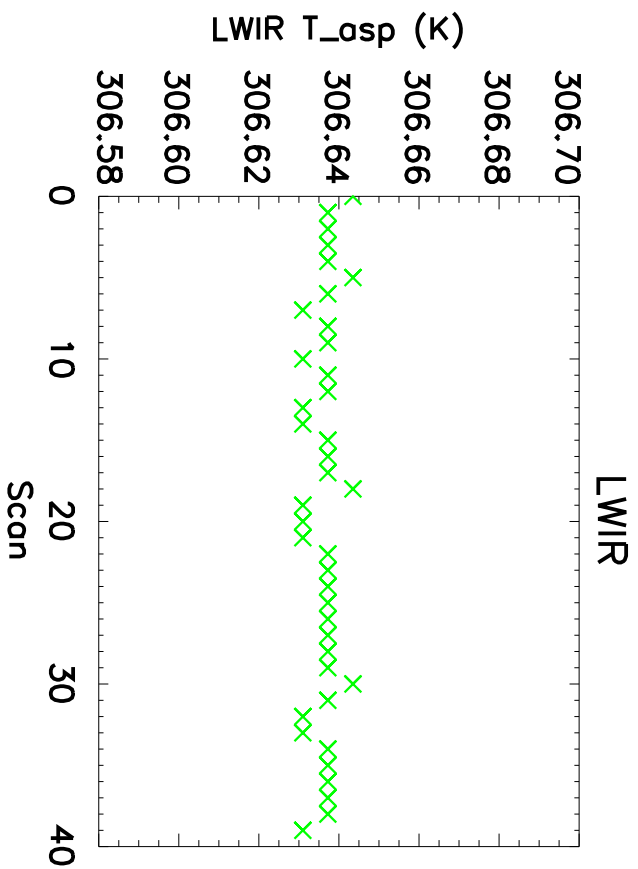
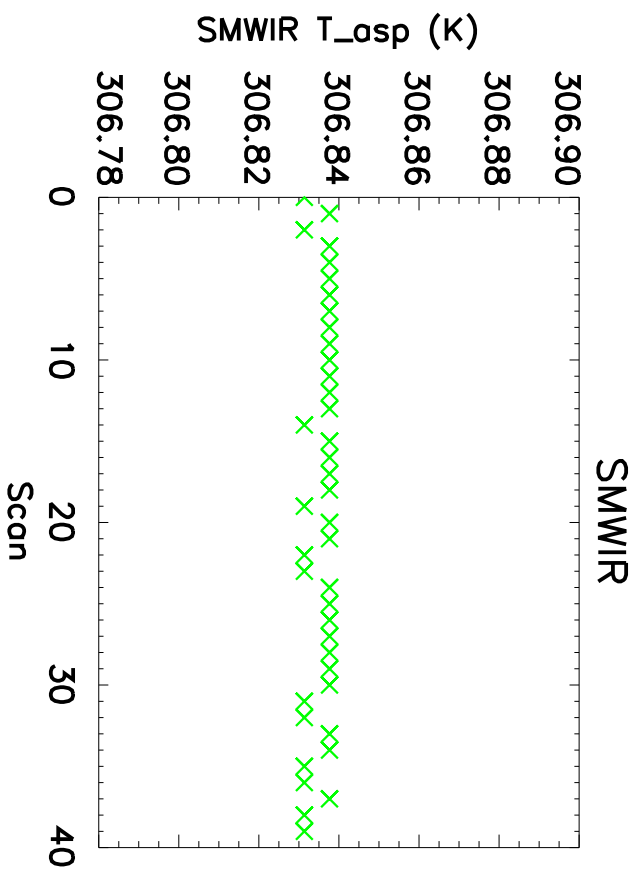
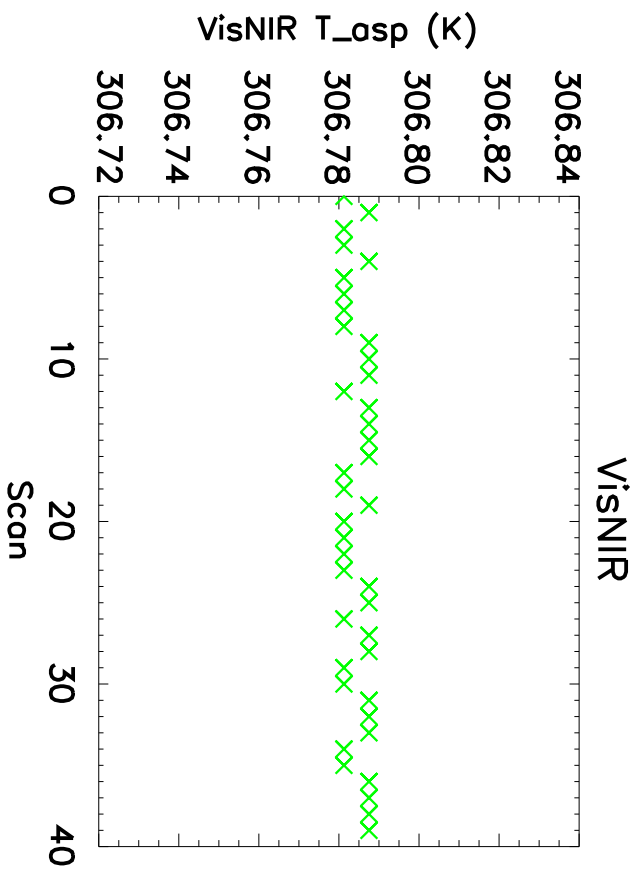
LWIR



T_osp vs Scan: Collect=17

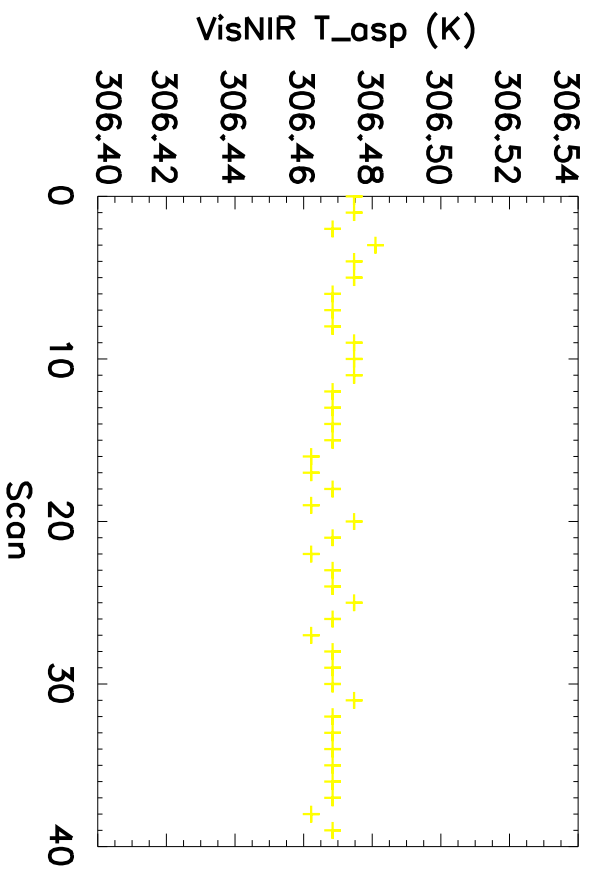


T_osp vs Scan: Collect=18

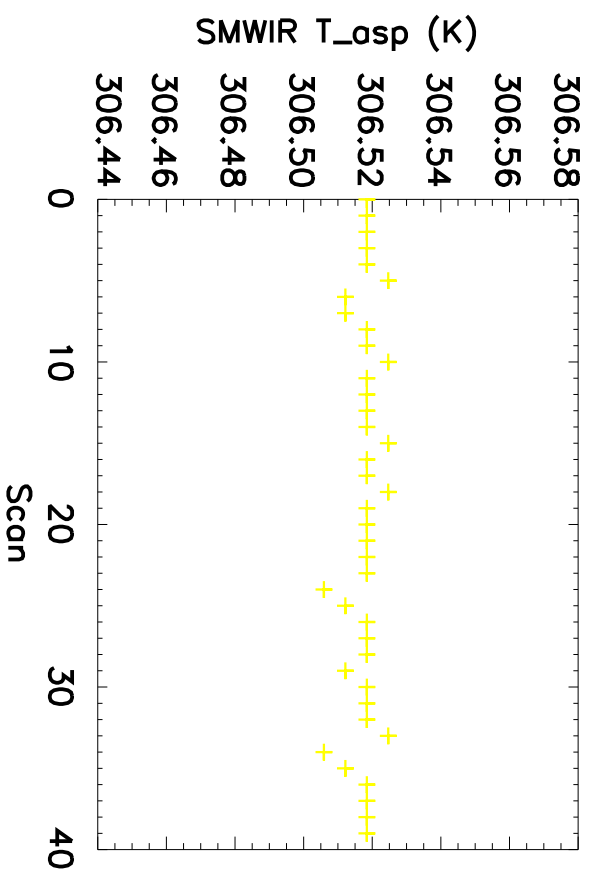


T_osp vs Scan: Collect=19

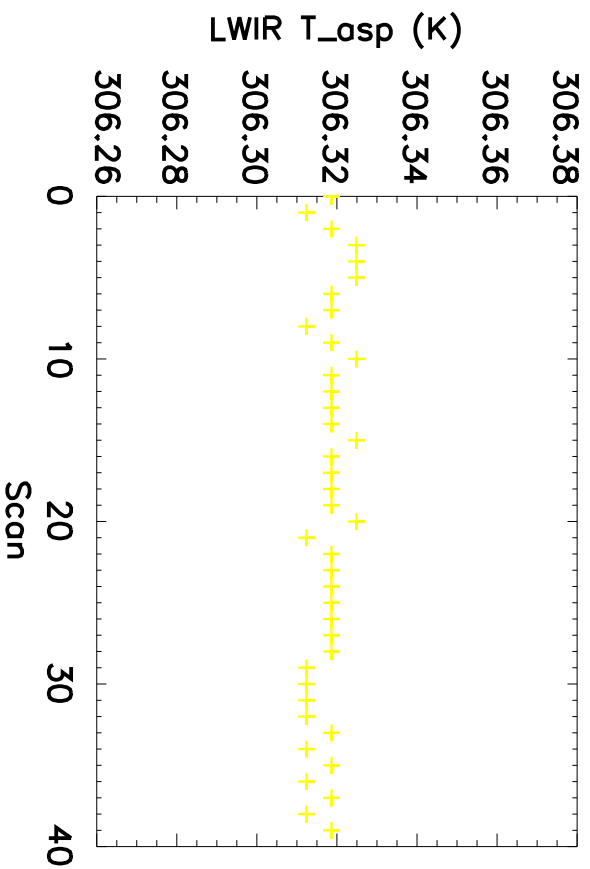
VisNIR



SMWIR

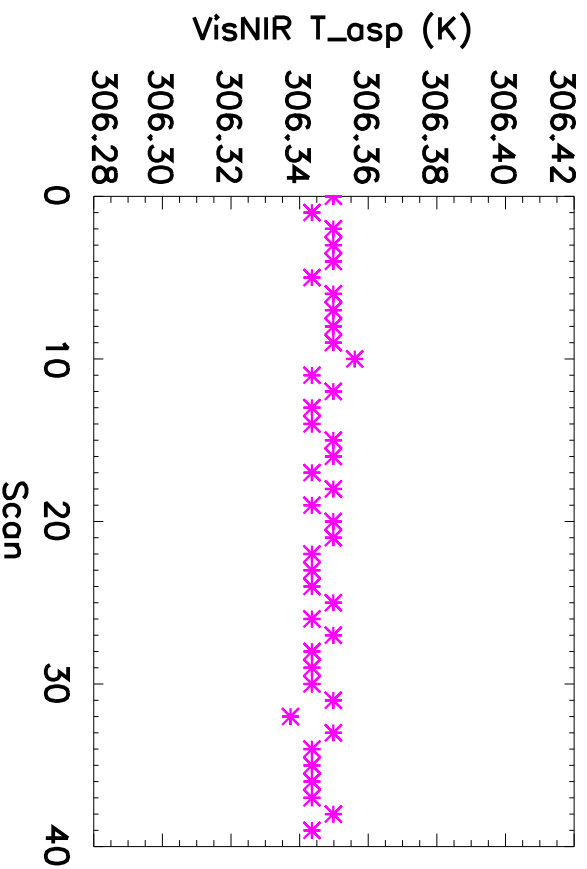


LWIR

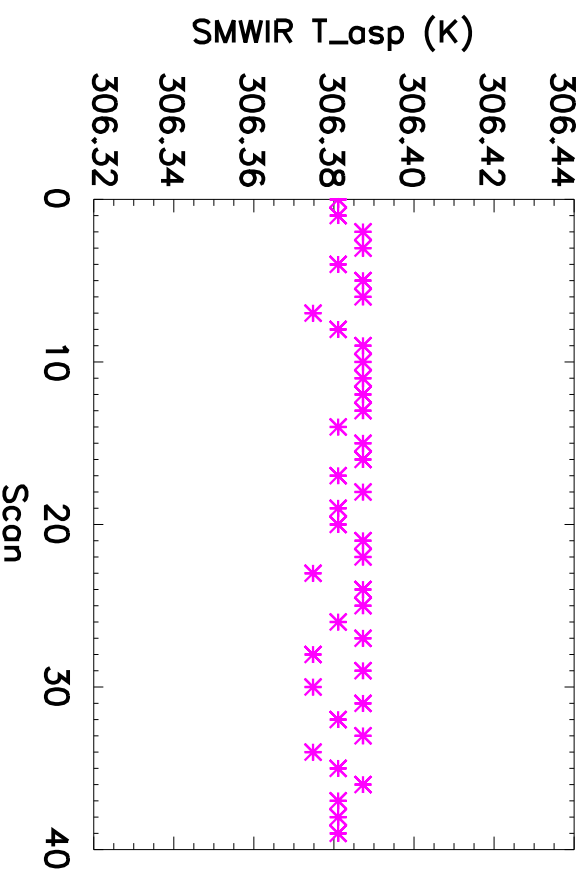


T_osp vs Scan: Collect=20

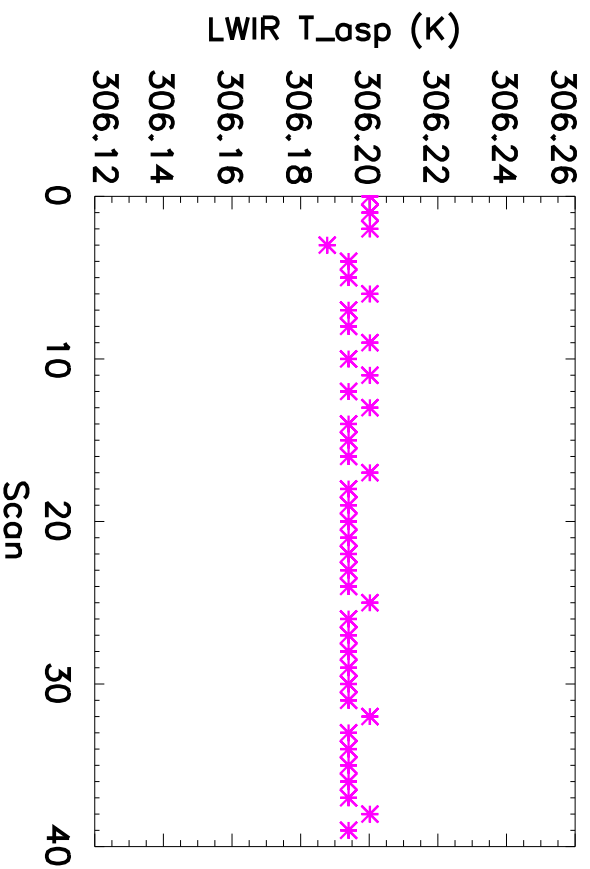
VisNIR



SMWIR

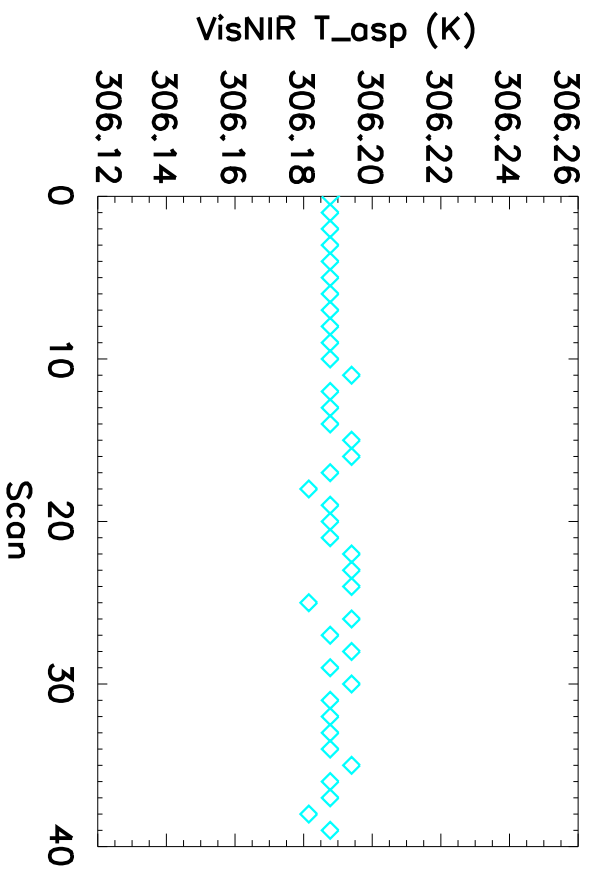


LWIR

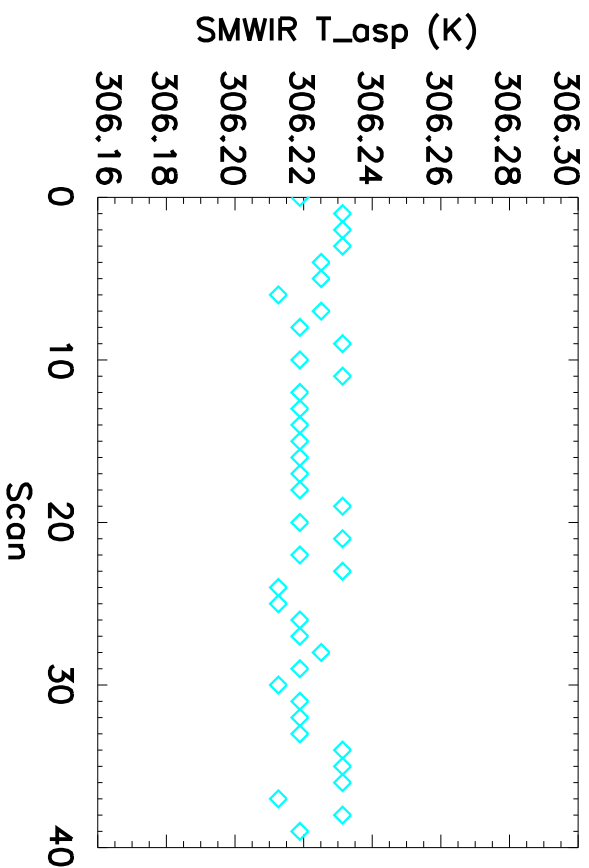


T_osp vs Scan: Collect=21

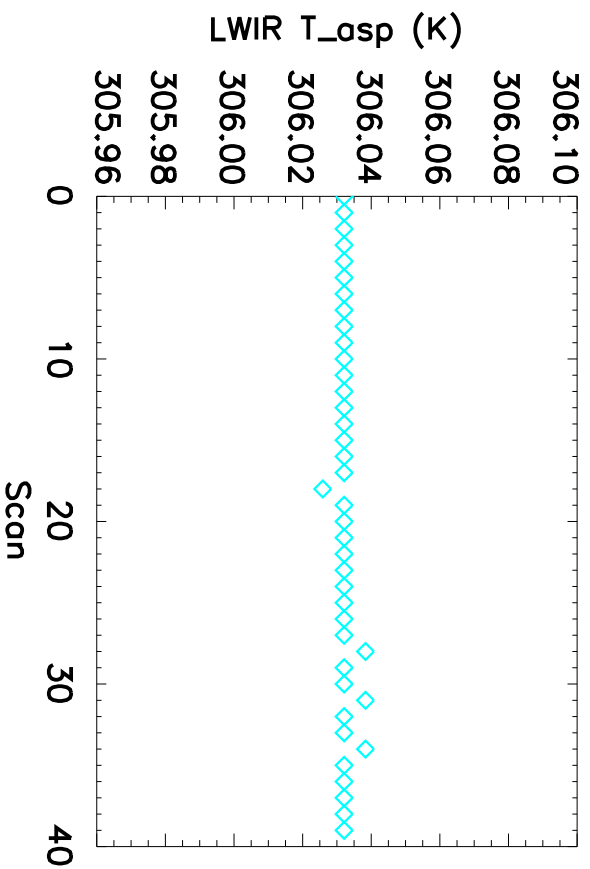
VisNIR



SMWIR

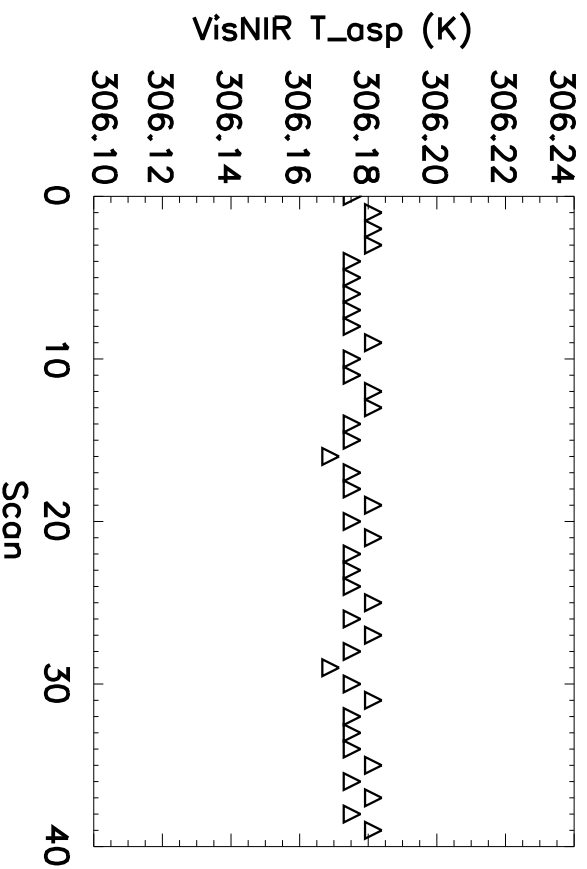


LWIR

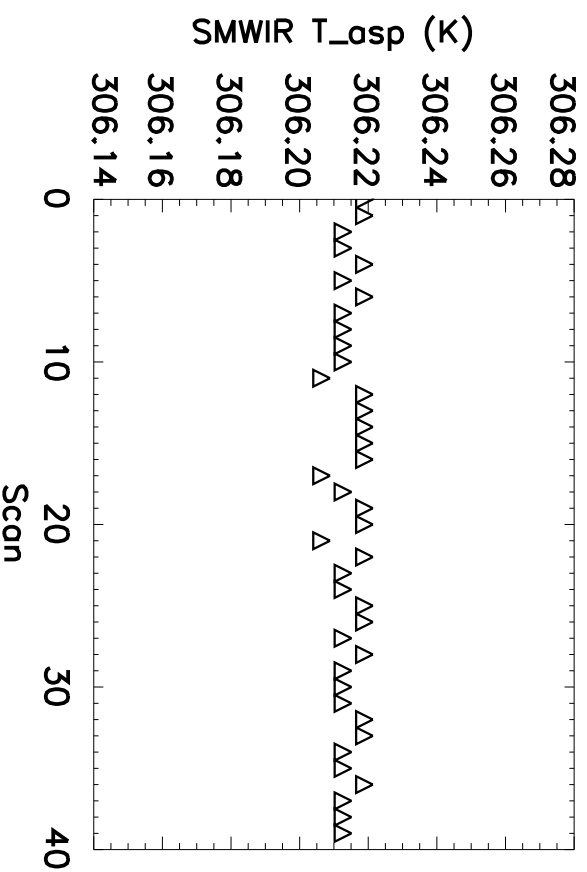


T_osp vs Scan: Collect=22

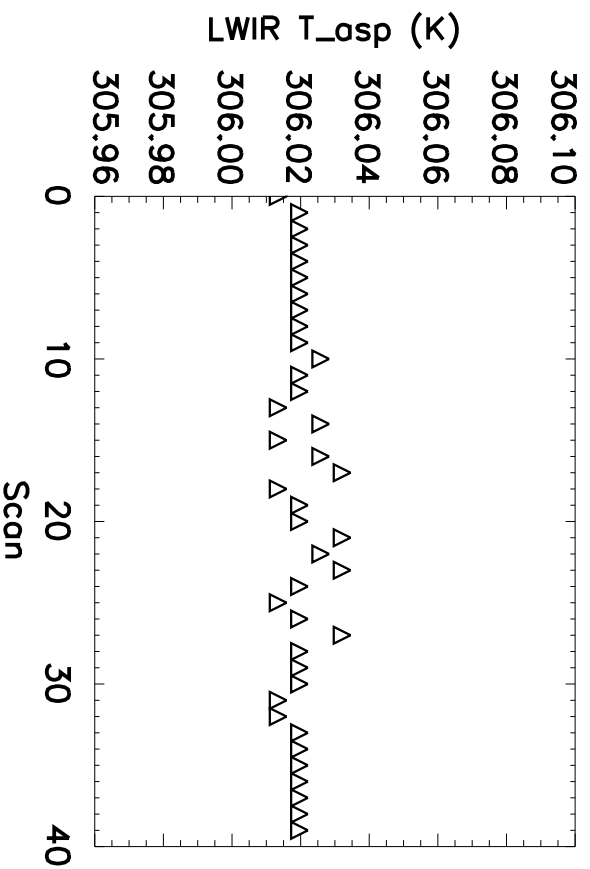
VisNIR



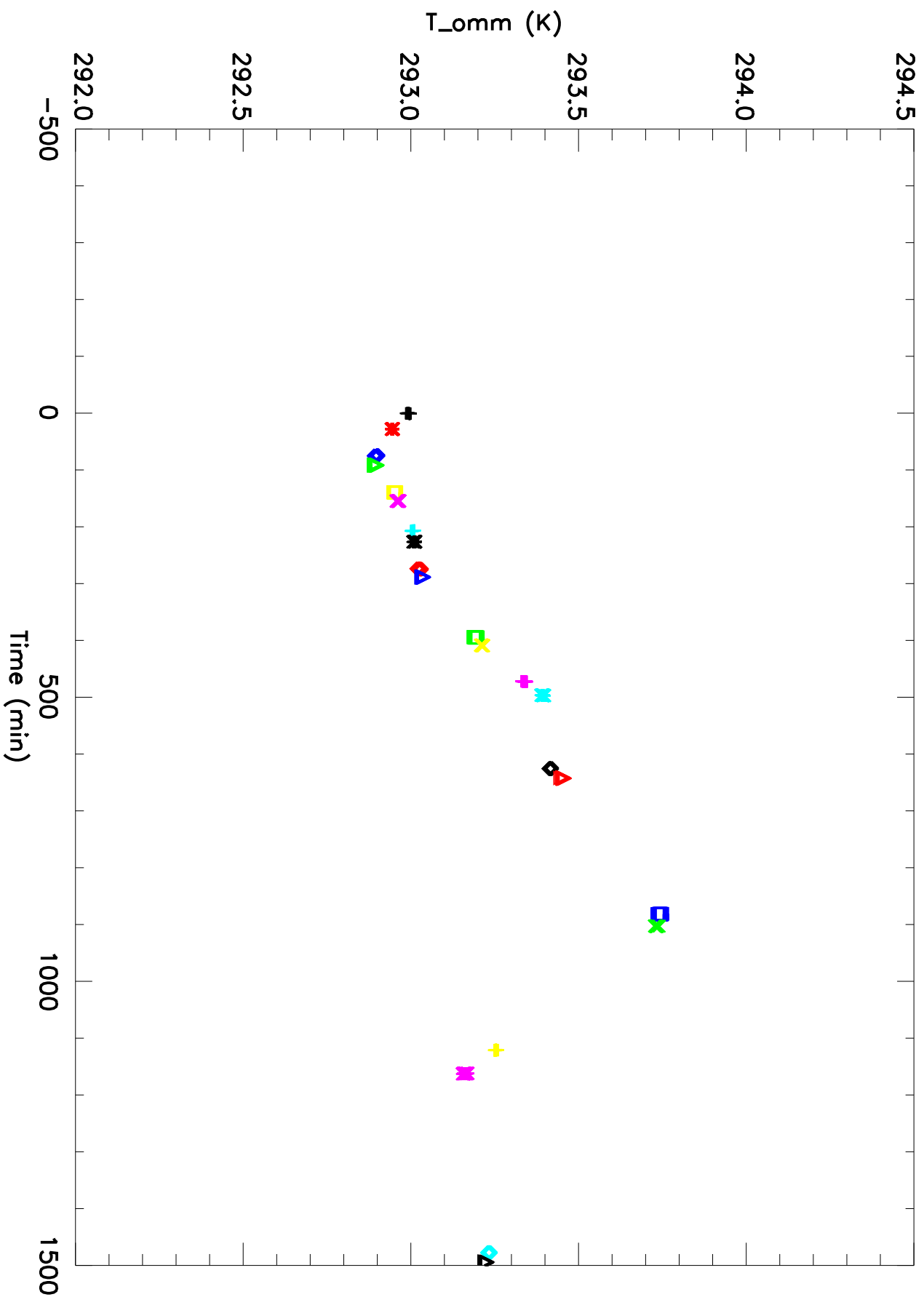
SMWIR



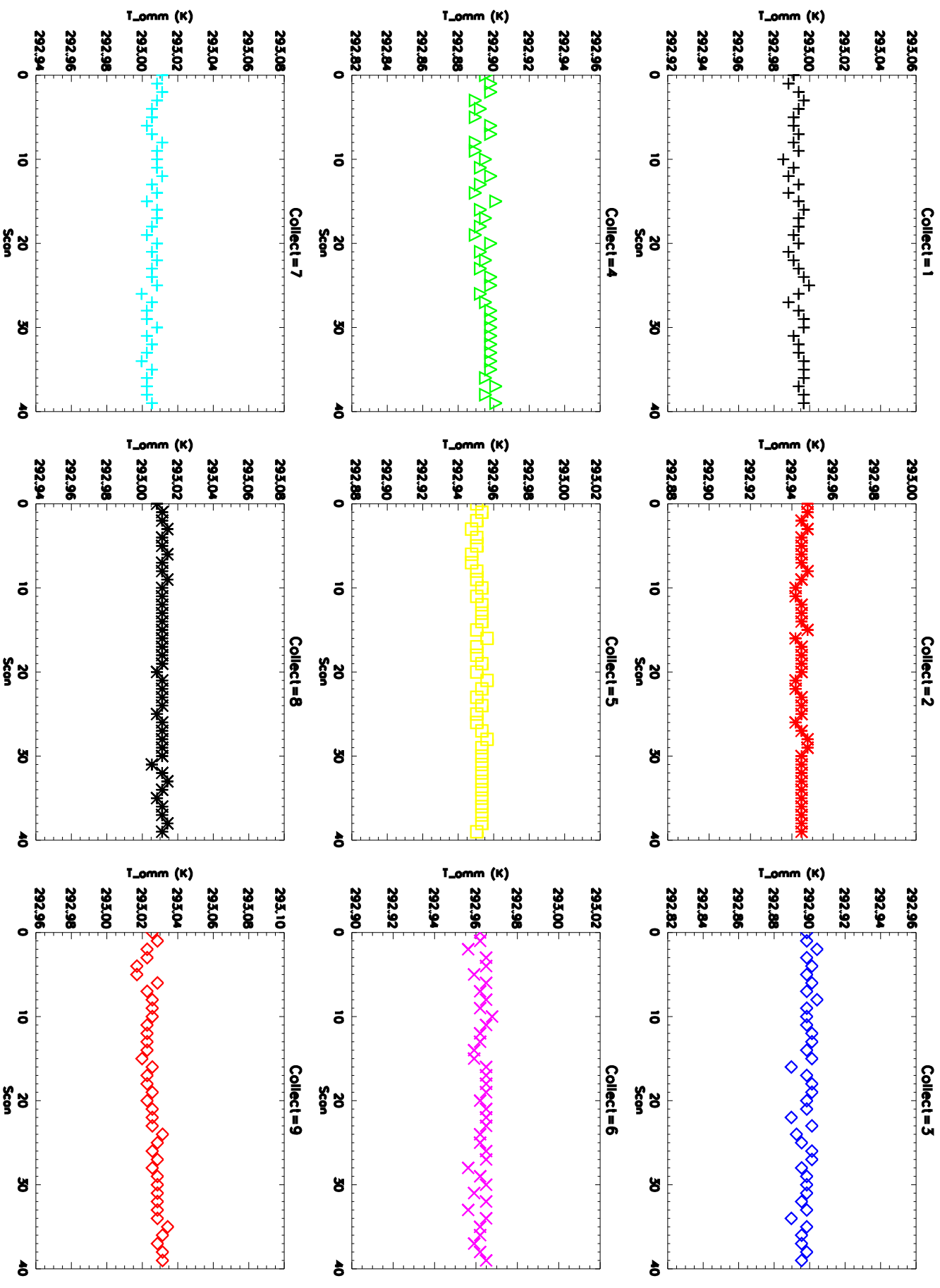
LWIR



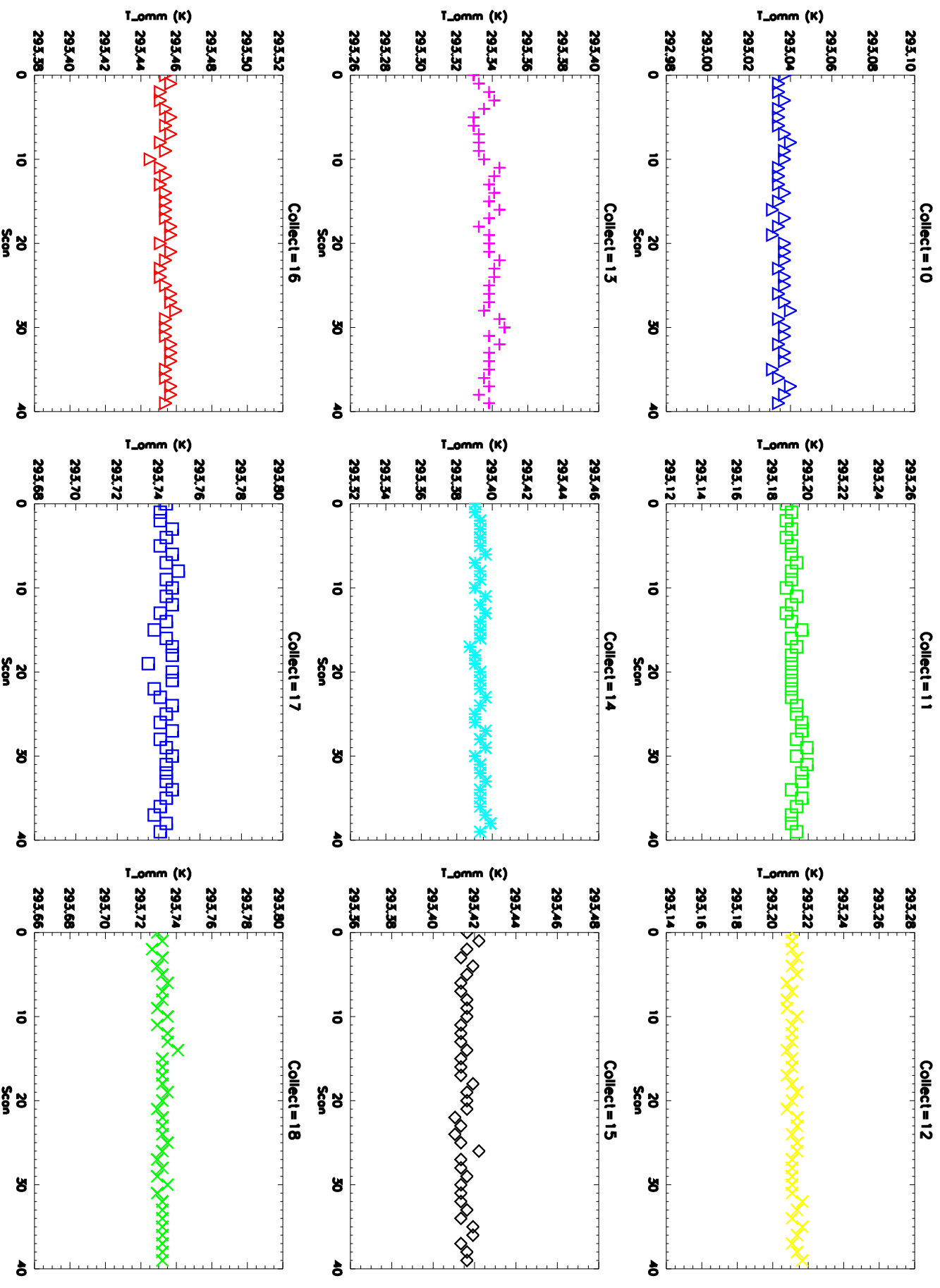
T_omm vs Time



T_{omm} vs Scan



T_omm vs Scan



T_omm vs Scan

